



## Standard Power MOSFET and MegaMOS™ FET Chips

### N-Channel Enhancement-Mode

Type $T_{j,c} = 100^{\circ}\text{C}$	$V_{DS}$ max.	$R_{DS(on)} @ I_D$		$C_{iss}$ typ.	$t_r$ typ.	Chip type	Chip size dimensions		Source @ bond wire recommended	Equivalent circuit data sheet	Dim. out- line No.
	V	$\Omega$	A	pF	ns		mm	mils			
IXTD67N10 IXTD75N10	100	0.025 0.02	5 5	4500 4500	300 300	IX77 IX77	8.84 x 7.19 8.84 x 7.19	348 x 283 348 x 283	15 mil x 3 ④ 15 mil x 3 ④	IXTH67N10 IXTH75N10	4 4
IRFC250 IXTD42N20 IXTD50N20 IXTD68N20	200	0.085 0.06 0.045 0.035	5 5 5 5	2970 4600 4600 5680	360 400 400 600	IX56 IX77 IX77 IX84	5.82 x 5.84 8.84 x 7.19 8.84 x 7.19 12.19 x 7.19	229 x 230 348 x 283 348 x 283 480 x 283	15 mil x 1 15 mil x 3 ④ 15 mil x 3 ④ 15 mil x 4	IRFP250 IXTH42N20 IXTH50N20 IXTH68N20	16 4 4 11
IRFC254 IXTD40N25	250	0.14 0.085	5 5	2990 4600	370 370	IX56 IX77	5.82 x 5.84 8.84 x 7.19	229 x 230 348 x 283	15 mil x 1 15 mil x 3 ④	IRFP254 IXTH40N25	16 4
IXTD35N30 IXTD40N30	300	0.1 0.088	5 5	4600 4600	400 400	IX77 IX77	8.84 x 7.19 8.84 x 7.19	348 x 283 348 x 283	15 mil x 3 ④ 15 mil x 3 ④	IXTH35N30 IXTH40N30	4 4
IRFC450 IRFC460 IXTD21N50 IXTD24N50 IRFC470 IXTD30N50 IXTD58N50	500	0.4 0.27 0.25 0.23 0.23 0.17 0.085	5 5 5 5 5 5 5	2800 3800 4200 4200 4200 5680 11000	500 570 600 600 600 850 250	IX54 IX62 IX77 IX77 IX77 IX84 IX9T ①	7.51 x 5.87 9.10 x 6.52 8.84 x 7.19 8.84 x 7.19 8.84 x 7.19 12.19 x 7.19 13.00 x 15.62	296 x 231 358 x 256 348 x 283 348 x 283 348 x 283 480 x 283 512 x 615	15 mil x 1 15 mil x 3 15 mil x 3 ④ 15 mil x 3 ④ 15 mil x 3 ④ 15 mil x 4 15 mil x 6	IRFP450 IRFP460 IXTH21N50 IXTH24N50 IRFP470 IXTK30N50 IXTK58N50	13 17 4 4 4 11 14
IXTD20N55	550	0.35	5	4500	600	IX77	8.84 x 7.19	348 x 283	15 mil x 3 ④	IXTH20N55	4
IXTD15N60 IXTD20N60	600	0.5 0.35	5 5	4500 4500	600 600	IX77 IX77	8.84 x 7.19 8.84 x 7.19	348 x 283 348 x 283	15 mil x 3 ④ 15 mil x 3 ④	IXTH15N60 IXTH20N60	4 4
IXTD15N70	700	0.45	5	4500	600	IX77	8.84 x 7.19	348 x 283	15 mil x 3 ④	IXTH15N70	4
IXTD01N80 IXTD6N80 IXTD11N80 IXTD13N80	800	80 1.8 0.95 0.8	0.1 3 5 5	80 2800 4500 4500	280 900 800 800	IXF1 IX5T IX7L IX77	1.68 x 1.96 6.61 x 6.73 8.91 x 7.22 8.84 x 7.19	66 x 77 260 x 265 351 x 284 348 x 283	3 mil x 1 10 mil x 4 12 mil x 4 15 mil x 3	IXTU01N80 IXTH6N80 IXTH11N80 IXTH13N80	18 8 15 4
IXTD6N90 IXTD10N90-L IXTD12N90-L	900	1.8 1.1 0.9	3 5 5	2600 4500 4500	900 900 900	IX5T IX7L IX7L	6.61 x 6.73 8.91 x 7.22 8.91 x 7.22	260 x 265 351 x 284 351 x 284	10 mil x 4 12 mil x 4 12 mil x 4	IXTH6N90 IXTH10N90 IXTH12N90	8 15 15
IXTD01N100 IXTD5N100 IXTD5N100A IXTD10N100 IXTD12N100 IXTD14N100	1000	80 2.4 2 1.2 1.05 0.82	0.1 2.5 2.5 5 5 5	80 2600 2600 4000 4000 5650	280 900 900 1000 1000 850	IXF1 IX5T IX5T IX7L IX7L IX83 ①	1.68 x 1.96 6.73 x 6.61 6.73 x 6.61 8.91 x 7.22 8.91 x 7.22 7.19 x 12.19	66 x 77 265 x 260 265 x 260 351 x 284 351 x 284 283 x 480	3 mil x 1 10 mil x 4 10 mil x 4 12 mil x 4 12 mil x 4 15 mil x 3	IXTU01N100 IXTH5N100 IXTH5N100A IXTH10N100 IXTH12N100 IXTH14N100	18 8 8 15 15 19

① Current Mirror and Temperature sense need to be connected to Source if not used. ② Recommended Gate bond wire is 8 mil.

③ 3 mil resp. 6 mil at ④ types.

### P-Channel Enhancement-Mode

Type $T_{j,c} = 150^{\circ}\text{C}$	$V_{DS}$ max.	$R_{DS(on)} @ I_D$		$C_{iss}$ typ.	$t_r$ typ.	Chip type	Chip size dimensions		Source @ bond wire recommended	Equivalent circuit data sheet	Dim. out- line No.
	V	$\Omega$	A	pF	ns		mm	mils			
IXTD7P50 IXTD8P50 IXTD10P50 IXTD11P50	500	1.5 1.2 0.9 0.75	3.5 4 5 5	3400 3400 4700 4700	400 400 500 500	IX57 IX57 IX77 IX77	6.58 x 6.58 6.58 x 6.58 8.84 x 7.19 8.84 x 7.19	259 x 259 259 x 259 348 x 283 348 x 283	20 mil x 1 20 mil x 1 15 mil x 3 ④ 15 mil x 3 ④	IXTH7P50 IXTH8P50 IXTH10P50 IXTH11P50	2 2 4 4

IXYS reserves the right to change limits, test conditions and dimensions.