

88-pin JEDEC Standard (2, 4, 8, 16 MBytes)

Pin Assignment

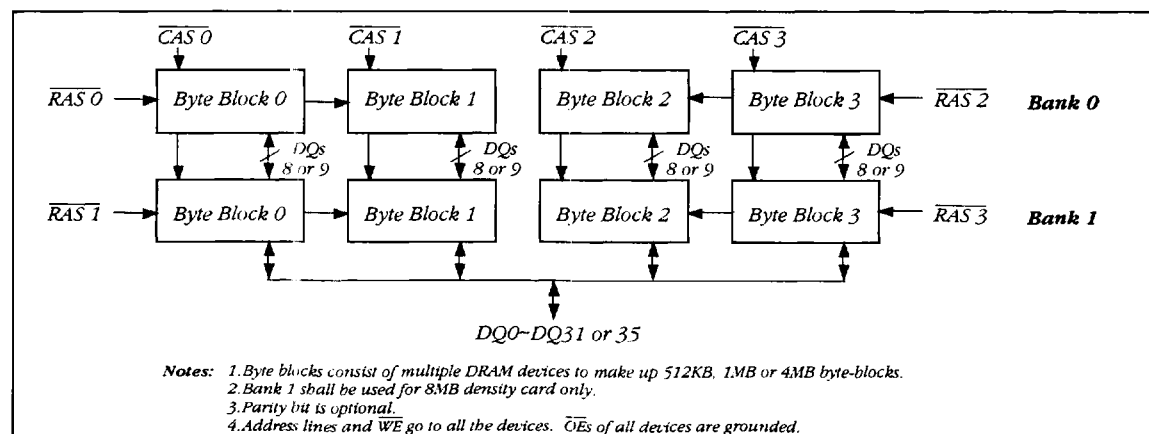
No.	Signal name	No.	Signal name	No.	Signal name
1	GND	31	NC	61	A9
2	DQ0	32	NC	62	NC
3	DQ1	33	DQ17	63	GND
4	DQ2	34	DQ9	64	NC
5	DQ3	35	NC	65	$\overline{\text{RAS1}}$
6	DQ4	36	DQ10	66	$\overline{\text{CAS2}}$
7	DQ5	37	Vcc	67	GND
8	DQ6	38	DQ11	68	$\overline{\text{CAS3}}$
9	Vcc	39	DQ12	69	$\overline{\text{RAS3}}$
10	DQ7	40	DQ13	70	$\overline{\text{WE}}$
11	NC	41	DQ14	71	PD1
12	DQ8	42	DQ15	72	PD3
13	A0	43	DQ16	73	GND
14	A2	44	GND	74	PD5
15	Vcc	45	GND	75	PD7
16	A4	46	DQ18	76	PD8
17	NC	47	DQ19	77	NC
18	A6	48	DQ20	78	NC
19	A8	49	DQ21	79	DQ35
20	A10 (1)	50	DQ22	80	DQ27
21	NC	51	DQ23	81	DQ28
22	$\overline{\text{RAS0}}$	52	DQ24	82	DQ29
23	$\overline{\text{CAS0}}$	53	DQ25	83	DQ30
24	$\overline{\text{CAS1}}$	54	DQ26	84	DQ31
25	NC	55	NC	85	DQ32
26	$\overline{\text{RAS2}}$	56	GND	86	DQ33
27	Vcc	57	A1	87	DQ34
28	PD2	58	A3	88	GND
29	PD4	59	A5		
30	PD6	60	A7		

Notes: 1. No connection for 2, 4 and 8 MB cards.

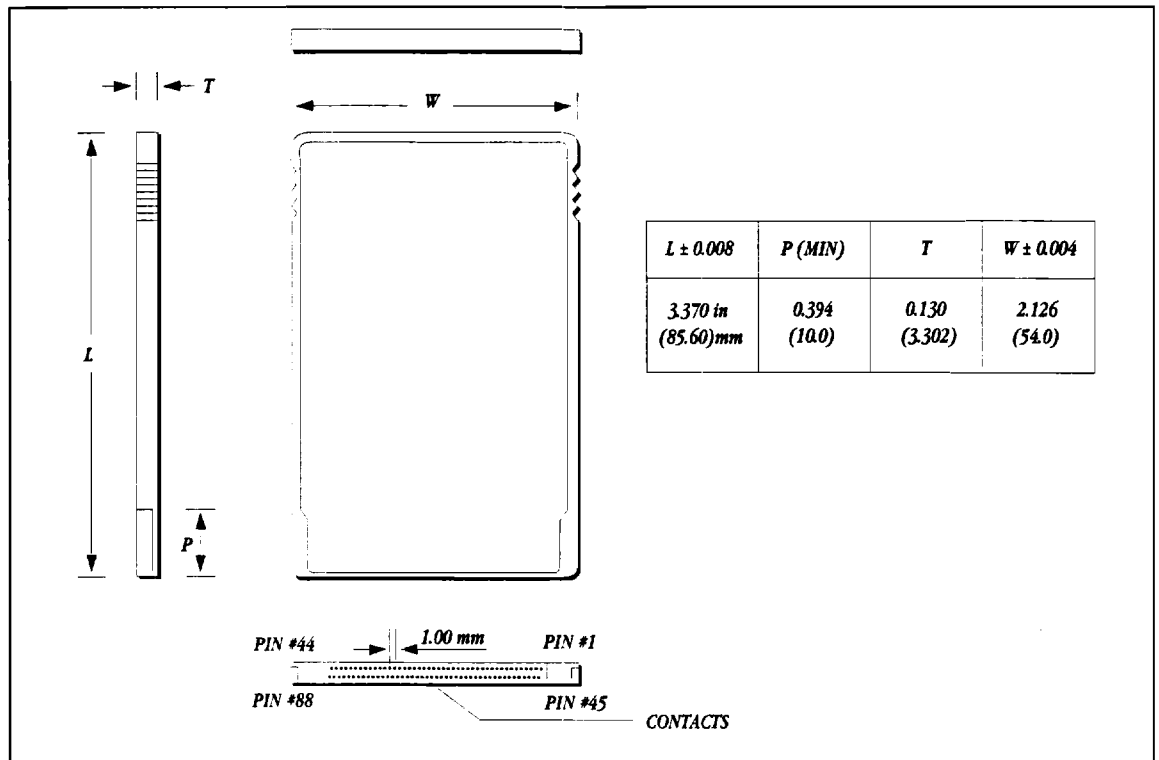
Primary Specifications

Specification	Part No.	SM9DR□2MF□□□	SM9DR□4MF□□□	SM9DR□8MF□□□	SM9DR□16MF□□□
Memory Density		2MByte	4MByte	8MByte	16MByte
Operating Voltage		5V±5% (3.3V optional)			
Active Current(max.-07/08/10)		440/400/360 mA	800/720/640 mA	816/736/656 mA	640/560/480 mA
Interface		Parallel I/O 88-pin JEDEC			
Access Time		70/80/100 ns max. (at 5V operation)			
Operating Temperature Range		0 to 55°C			
Outer Dimensions		3.37" (L) x 2.126" (W) x 0.130" (T)			

Block Diagram



Outer Dimensions



Ordering Information

