

## DM54L01/DM74L01 Quad 2-Input NAND Gates with Open-Collector Outputs

### General Description

This device contains four independent gates each of which performs the logic NAND function. The open-collector outputs require external pull-up resistors for proper logical operation.

### Pull-Up Resistor Equations

$$R_{MAX} = \frac{V_{CC} (Min) - V_{OH}}{N_1 (I_{OH}) + N_2 (I_{IH})}$$

$$R_{MIN} = \frac{V_{CC} (Max) - V_{OL}}{I_{OL} - N_3 (I_{IL})}$$

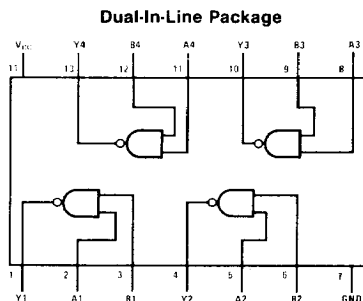
Where:  $N_1 (I_{OH})$  = total maximum output high current for all outputs tied to pull-up resistor  
 $N_2 (I_{IH})$  = total maximum input high current for all inputs tied to pull-up resistor  
 $N_3 (I_{IL})$  = total maximum input low current for all inputs tied to pull-up resistor

### Absolute Maximum Ratings (Note 1)

|                           |                 |
|---------------------------|-----------------|
| Supply Voltage            | 8V              |
| Input Voltage             | 5.5V            |
| Output Voltage            | 8V              |
| Storage Temperature Range | - 65°C to 150°C |

**Note 1:** The "Absolute Maximum Ratings" are those values beyond which the safety of the device can not be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

### Connection Diagram



TL/F/6655-1

DM54L01 (J) DM74S01 (N)

### Function Table

$$Y = \overline{AB}$$

| Inputs |   | Output |
|--------|---|--------|
| A      | B | Y      |
| L      | L | H      |
| L      | H | H      |
| H      | L | H      |
| H      | H | L      |

H = High Logic Level  
L = Low Logic Level

## Recommended Operating Conditions

| Sym             | Parameter                      | DM54L01 |     |     | DM74L01 |     |      | Units |
|-----------------|--------------------------------|---------|-----|-----|---------|-----|------|-------|
|                 |                                | Min     | Nom | Max | Min     | Nom | Max  |       |
| V <sub>CC</sub> | Supply Voltage                 | 4.5     | 5   | 5.5 | 4.75    | 5   | 5.25 | V     |
| V <sub>IH</sub> | High Level Input Voltage       | 2       |     |     | 2       |     |      | V     |
| V <sub>IL</sub> | Low Level Input Voltage        |         |     | 0.6 |         |     | 0.6  | V     |
| V <sub>OH</sub> | High Level Output Voltage      |         |     | 5.5 |         |     | 5.5  | V     |
| I <sub>OL</sub> | Low Level Output Current       |         |     | 2   |         |     | 3.6  | mA    |
| T <sub>A</sub>  | Free Air Operating Temperature | -55     |     | 125 | 0       |     | 70   | °C    |

## Electrical Characteristics over recommended operating free air temperature (unless\* otherwise noted)

| Sym              | Parameter                         | Conditions                                                              |      | Min | Typ<br>(Note 1) | Max   | Units |
|------------------|-----------------------------------|-------------------------------------------------------------------------|------|-----|-----------------|-------|-------|
| I <sub>CEX</sub> | High Level Output Current         | V <sub>CC</sub> = Min, V <sub>O</sub> = 5.5V<br>V <sub>IL</sub> = Max   |      |     |                 | 50    | μA    |
| V <sub>OL</sub>  | Low Level Output Voltage          | V <sub>CC</sub> = Min<br>I <sub>OL</sub> = Max<br>V <sub>IH</sub> = Min | DM54 |     | 0.15            | 0.3   | V     |
|                  |                                   |                                                                         | DM74 |     | 0.2             | 0.4   |       |
| I <sub>I</sub>   | Input Current @ Max Input Voltage | V <sub>CC</sub> = Max, V <sub>I</sub> = 5.5V                            |      |     |                 | 0.1   | mA    |
| I <sub>IH</sub>  | High Level Input Current          | V <sub>CC</sub> = Max, V <sub>I</sub> = 2.4V                            |      |     |                 | 10    | μA    |
| I <sub>IL</sub>  | Low Level Input Current           | V <sub>CC</sub> = Max, V <sub>I</sub> = 0.3V                            |      |     |                 | -0.18 | mA    |
| I <sub>CC</sub>  | Supply Current With Outputs High  | V <sub>CC</sub> = Max                                                   |      |     | 0.44            | 0.8   | mA    |
| I <sub>CCL</sub> | Supply Current With Outputs Low   | V <sub>CC</sub> = Max                                                   |      |     | 1.16            | 2.04  | mA    |

## Switching Characteristics at V<sub>CC</sub> = 5V and T<sub>A</sub> = 25°C (See Section 1 for Test Waveforms and Output Load)

| Parameter                                                           | Conditions | R <sub>L</sub> = 4 kΩ<br>C <sub>L</sub> = 50 pF |     |     | Units |
|---------------------------------------------------------------------|------------|-------------------------------------------------|-----|-----|-------|
|                                                                     |            | Min                                             | Typ | Max |       |
| t <sub>PLH</sub> Propagation Delay Time<br>Low to High Level Output |            |                                                 | 60  | 90  | ns    |
| t <sub>PHL</sub> Propagation Delay Time<br>High to Low Level Output |            |                                                 | 33  | 60  | ns    |

Note 1: All typicals are at V<sub>CC</sub> = 5V, T<sub>A</sub> = 25°C.