

Description

The μ PD7554/54A and μ PD7564/64A are low-end versions of the μ PD7500-series products. These microcomputers incorporate a serial interface and are useful as slave CPUs to high-end μ PD7500-series or 8-bit μ PD7800-series products.

The μ PD7554/54A/64/64A have output ports that can directly drive triacs and LEDs. Also, various mask-optional I/O circuits can be configured for a wide selection of outputs to reduce external circuitry in a design.

The μ PD7554/54A and μ PD7564/64A differ only in their clock circuitry. The μ PD7554/54A use an external resistor with an internal capacitor for an RC oscillator clock, while the μ PD7564/64A use an external ceramic oscillator as a clock. These microcomputers are ideally suited to control devices such as plain paper copiers (PPCs), printers, VCRs, and audio equipment.

Features

- 47 instructions (subset of μ PD7500 set B)
- Instruction cycle:
 - External clock: 2.86 μ s/700 kHz, 5 V
 - RC oscillator (μ PD7554/54A): 4 μ s/500 kHz, 5 V
 - Ceramic oscillator (μ PD7564/64A): 2.86 μ s/700 kHz, 5 V
- Program memory (ROM) of 1024 x 8 bits
- Data memory (RAM) of 64 x 4 bits
- 8-bit timer/event counter
- 8-bit serial interface
- I/O lines: 16 for the μ PD7554/54A and 15 for the μ PD7564/64A
- Data memory retention at low supply voltage
- Two low power standby modes: STOP, HALT
- CMOS technology
- Low power consumption
- Single power supply
 - 2.5 to 6.0 V (μ PD7554)
 - 2.7 to 6.0 V (μ PD7564/64A)
 - 2.0 to 6.0 V (μ PD7554A)

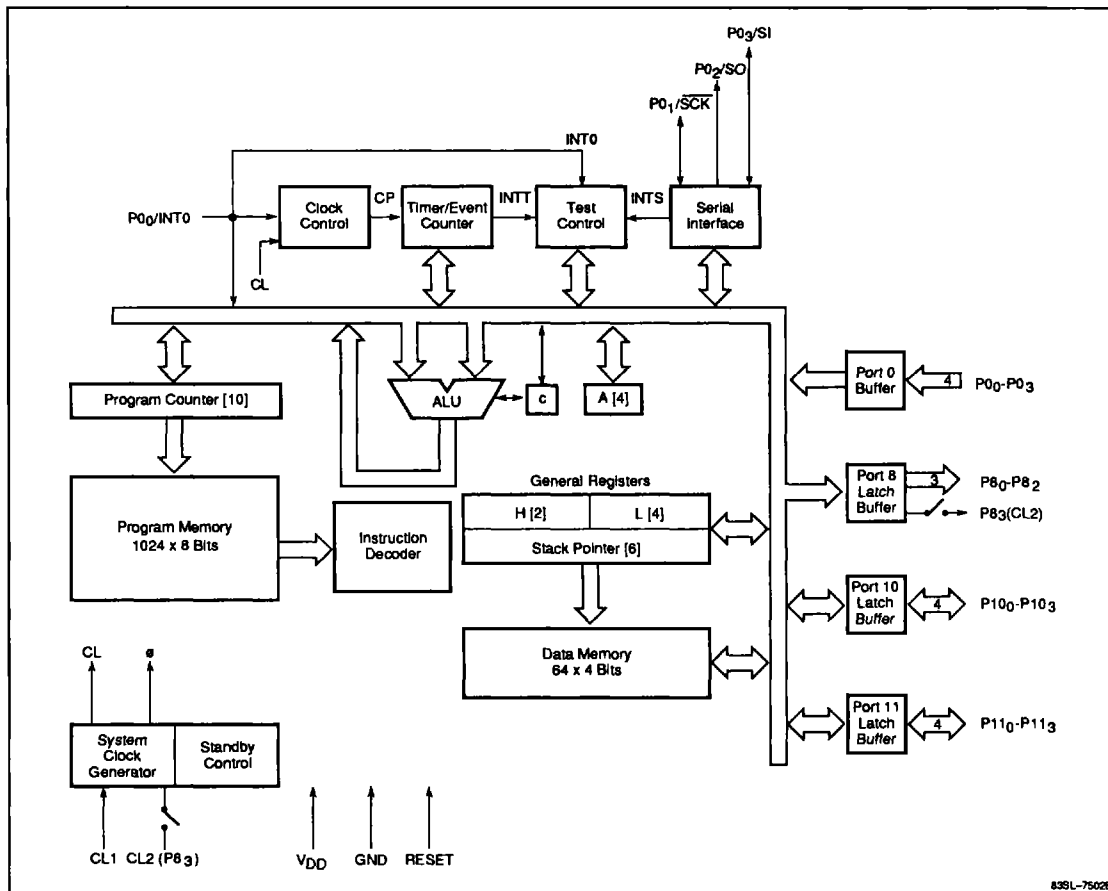
Ordering Information

Part Number	Package
μ PD7554CS-xxx	20-pin plastic shrink DIP
μ PD7554ACS-xxx	
μ PD7564CS-xxx	
μ PD7564ACS-xxx	
μ PD7554G-xxx	20-pin plastic SOP
μ PD7554AG-xxx	
μ PD7564G-xxx	
μ PD7564AG-xxx	

Notes:

- (1) xxx indicates ROM code suffix.

Block Diagram, μPD7554/54A



83SL-75028

Block Diagram, μPD7564/64A

