



Data Sheet

48V Input Mini Family* DC-DC Converter Module

Features

- DC input range: 36 - 75V
- Input surge withstand: 100V for 100ms
- DC output: 2V - 48V
- Programmable output: 10 to 110%
- Regulation: $\pm 0.2\%$ no load to full load
- Efficiency: Up to 91%
- Maximum operating temperature: 100°C at full load
- Power density: Up to 100W/cubic inch
- Height above board: 0.43 in. (10,9 mm)
- Parallelable, with N+M fault tolerance
- Low noise ZCS/ZVS architecture

Typical Applications: telecommunications and distributed power systems

Product Overview

This DC-DC converter module uses 2nd Generation power processing, control and packaging technologies to provide the performance, flexibility and cost effectiveness expected of a mature power component. For example, a plated-cavity core transformer couples widely separated primary and secondary windings, resulting in low in-to-out parasitic capacitance and noise. High frequency ZCS/ZVS switching, advanced power semiconductor packaging and thermal management provide high power density with low temperature gradients. Extensive use of silicon integration results in 1/3 the part count of a 1st Generation converter.

Part Numbering

| | | | | | | | | |
|---------------|----|---------------------------------|-------------|--------------|-----|-----------|--|--|
| V | 48 | B | 15 | C | 250 | A | | |
| Input Voltage | | Output Voltage | | Output Power | | Pin Style | | |
| 24 = 24Vin | | | | | | | | |
| 48 = 48Vin | | | | | | | | |
| 300 = 300Vin | | | | | | | | |
| 375 = 375Vin | | | | | | | | |
| A = Maxi | | Product Grade Temperatures (°C) | | Baseplate | | | | |
| B = Mini | | Grade | Storage | Operating | | | | |
| C = Micro | | C = -40 to +125 | -20 to +100 | | | | | |
| | | T = -40 to +125 | -40 to +100 | | | | | |
| | | H = -55 to +125 | -40 to +100 | | | | | |
| | | M = -65 to +125 | -55 to +100 | | | | | |

For pin styles see page 6. Slotted baseplate (style 1) requires no designator. For threaded baseplate add a "2" to the end of the part number. For thru-hole, add a "3". See page 7 for dimensions.



Shown actual size:
2.28 x 2.2 x 0.5 in
57,9 x 55,9 x 12,7 mm

Absolute Maximum Ratings

| Parameter | Rating | Unit | Notes |
|---------------------------------|--------------|--------------|---|
| +In to -In voltage | -0.5 to +75 | Vdc | |
| +In to -In voltage | 100 | Vdc | <100ms |
| PC to -In voltage | -0.5 to +7.0 | Vdc | |
| PR to -In voltage | -0.5 to +7.0 | Vdc | |
| SC to -Out voltage | -0.5 to +1.5 | Vdc | |
| -Sense to -Out voltage | 1.0 | Vdc | |
| Isolation voltage (in to out) | 3000 | Vrms | |
| Isolation voltage (in to base) | 1550 | Vrms | |
| Isolation voltage (out to base) | 500 | Vrms | |
| Storage temperature (C-Grade) | -40 to +125 | °C | See part numbering chart on this page for other product grades. |
| Operating temperature (C-Grade) | -20 to +100 | °C | Baseplate |
| Pin soldering temperature | 500 (260) | °F (°C) | <5 sec; wave solder |
| Pin soldering temperature | 750 (390) | °F (°C) | <7 sec; hand solder |
| Mounting torque | 5 (0.57) | in-lbs (N-m) | 6 each, # 4-40 or M3 |

Thermal Resistance and Capacity

| Parameter | Min | Typ | Max | Unit |
|--|------|-----|-----|-------------|
| Baseplate to sink; flat, greased surface | 0.16 | | | °C/Watt |
| Baseplate to sink; thermal pad (P/N 20264) | 0.14 | | | °C/Watt |
| Baseplate to ambient | 8.0 | | | °C/Watt |
| Baseplate to ambient; 1000 LFM | 1.9 | | | °C/Watt |
| Thermal capacity | 83 | | | Watt-sec/°C |

* The modules described here are the highest power for each output voltage. Consult factory for availability of lower power units.

ELECTRICAL CHARACTERISTICS

Electrical characteristics apply over the full operating range of input voltage, output load (resistive) and baseplate temperature, unless otherwise specified. All temperatures refer to the operating temperature at the center of the baseplate.

■ MODULE OPERATING SPECIFICATIONS

| Parameter | Min | Typ | Max | Unit | Notes |
|-------------------------|-----|-----|-----------|------|--------------------------------|
| Operating input voltage | 36 | 48 | 75 | Vdc | |
| Input surge withstand | | | 100 | Vdc | <100ms |
| Output voltage setpoint | | ±1% | Vout nom. | | Nominal input; full load; 25°C |

■ MODULE OUTPUT VOLTAGE RELATED SPECIFICATIONS

| Parameter | 1.8V ¹ | 2V | 2.5V ² | 3.3V | 5V | 12V | 15V | 24V | 28V | 48V | Unit | Notes |
|------------------------------|-------------------|------|-------------------|------|------|------|------|------|------|------|---------|---|
| Efficiency (typ.) | 70.9 | 73.0 | 76.2 | 79.0 | 83.0 | 86.0 | 87.5 | 88.5 | 88.5 | 89.5 | % | Nominal input; 80% load; 25°C |
| Ripple and noise, p-p (typ.) | 95 | 85 | 64 | 60 | 80 | 160 | 115 | 125 | 65 | 80 | mV | Nominal input; full load; 25°C; 20MHz BW |
| Output power (max.) | 90 | 100 | 114 | 150 | 200 | 250 | 250 | 250 | 250 | 250 | Watts | At 100°C baseplate temperature |
| Output OVP setpoint (nom.) | 2.8 | 2.8 | 4.3 | 4.3 | 6.3 | 14.3 | 17.8 | 28.1 | 32.7 | 55.7 | Volts | 25°C; recycle input voltage to restart (1 minute off) |
| Dissipation, standby (typ.) | 4.3 | 4.3 | 3.5 | 3.5 | 3.3 | 4.4 | 5.3 | 5.0 | 4.5 | 3.9 | Watts | No load; nominal input |
| Load regulation (max.) | 0.3 | 0.3 | 0.3 | 0.3 | 0.2 | 0.2 | 0.2 | 0.2 | 0.2 | 0.2 | ±% Vout | No load to full load; nominal input |
| Current limit (typ.) | 115 | 115 | 115 | 115 | 115 | 115 | 115 | 115 | 115 | 115 | % Iout | Output voltage 95% of nominal |
| Short circuit current (typ.) | 115 | 115 | 115 | 115 | 115 | 115 | 115 | 115 | 115 | 115 | % Iout | Output voltage <250mV |

■ MODULE INPUT SPECIFICATIONS

| Parameter | Min | Typ | Max | Unit | Notes |
|------------------------|------|------|------|------|-------|
| Undervoltage turn-on | | 34.9 | 35.7 | Vdc | |
| Undervoltage turn-off | 29.4 | 30.5 | | Vdc | |
| Oversupply turn-off/on | 75.7 | 78.8 | 82.5 | Vdc | |

■ MODULE OUTPUT SPECIFICATIONS

| Parameter | Min | Typ | Max | Unit | Notes |
|------------------------|-----|--------|--------|--------|---|
| Line regulation | | ±0.02 | ±0.2 | % | Low line to high line; full load |
| Temperature regulation | | ±0.002 | ±0.005 | % / °C | Over operating temperature range |
| Power sharing accuracy | | ±2 | ±5 | % | 10 to 100% of full load |
| Programming range | 10 | | 110 | % | Of nominal output voltage. For trimming below 90% of nominal, a minimum load of 10% of maximum rated power may be required. |

Note: For important information relative to applications where the converter modules are subject to continuous dynamic loading, contact Vicor applications engineering at 800-927-9474.

¹ Nominal 2V output trimmed to 1.8V by adding external 9,000 ohm resistor from the SC pin to -S pin. (See Fig. 7)

² Nominal 3.3V output trimmed to 2.5V by adding external 3,125 ohm resistor from the SC pin to -S pin. (See Fig. 7)

ELECTRICAL CHARACTERISTICS, continued

■ MODULE CONTROL SPECIFICATIONS

| Parameter | Min | Typ | Max | Unit | Notes |
|---|-------------|-------------|-------------|-----------|--|
| PRIMARY SIDE (PC = Primary Control; PR = Parallel) | | | | | |
| PC bias voltage current limit | 5.50 1.5 | 5.75 2.1 | 6.00 3.0 | Vdc mA | PC current = 1.0 mA PC voltage = 5.5V |
| PC module disable | 2.3 | 2.6 | 2.9 | Vdc | Must be able to sink ≥4 mA. See Fig. 1 |
| PC module enable delay | | 4 | 7 | ms | |
| PC module alarm | | | 0.5 | Vavg | UV, OV, OT, module fault. See Figs. 2 and 4 |
| PR emitter amplitude | 5.7 | 5.9 | 6.1 | Volts | PR load >30 ohms, < 30 pF |
| PR emitter current | 150 | | | mA | |
| PR receiver impedance | 375 | 500 | 625 | ohms | 25°C |
| PR receiver threshold | 2.4 | 2.5 | 2.6 | Volts | Minimum pulse width: 20ns |
| PR drive capability | | | 12 | modules | Without PR buffer amplifier |
| SECONDARY SIDE (SC = Secondary Control) | | | | | |
| SC bandgap voltage | 1.21 | 1.23 | 1.25 | Vdc | Referenced to –Sense |
| SC resistance | 990 | 1000 | 1010 | ohms | |
| SC capacitance | | 0.033 | | µF | |
| SC module alarm | | 0 | | Vdc | With open trim; referenced to –Sense. See Fig. 6 |

■ MODULE GENERAL SPECIFICATIONS

| Parameter | Min | Typ | Max | Unit | Notes |
|----------------------------------|-----------------------------|-----------|---------|----------------|--|
| Remote sense (total drop) | | | 0.5 | Vdc | 0.25V per leg (senses must be closed) |
| Isolation voltage (in to out) | 3000 | | | Vrms | Complies with reinforced insulation requirements |
| Isolation voltage (in to base) | 1550 | | | Vrms | Complies with basic insulation requirements |
| Isolation voltage (out to base) | 500 | | | Vrms | Complies with operational insulation requirements |
| Isolation resistance (in to out) | | 10 | | megohms | |
| Weight | | 3.7 (104) | 4 (112) | ounces (grams) | |
| Temperature limiting | 100 | 115 | | °C | See Figs. 2 and 4 |
| Agency approvals | UL, CSA, TÜV, BABT, CE, VDE | | | | UL1950, CSA950, EN60950, VDE0805, BS7002, IEC 60950. With a fuse in series with the +Input |

Note:

Specifications are subject to change without notice.

The latest data sheets for individual models are available on the Vicor website at www.vicr.com.

CONTROL FUNCTIONS - PC PIN

Module Enable/Disable

The module may be disabled by pulling PC below 2.3V with respect to the –In input. This may be done with an open collector transistor, relay, or optocoupler. Multiple converters may be disabled with a single transistor or relay either directly or via “OR’ing” diodes. See Figure 1.

Primary Auxiliary Supply

At 5.7V, PC can source up to 1.5mA. In the example shown in Figure 3, PC powers a module enabled LED.

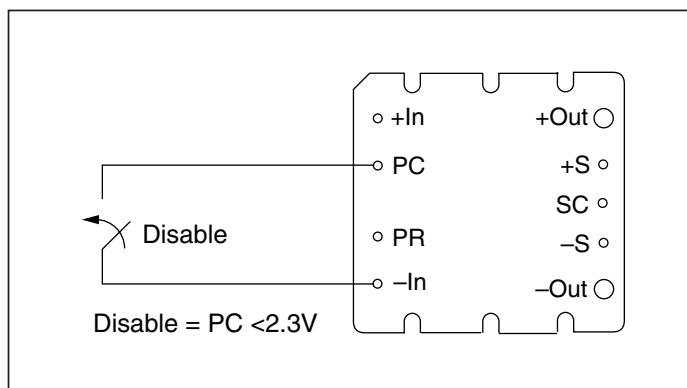


Figure 1—Module enable/disable.

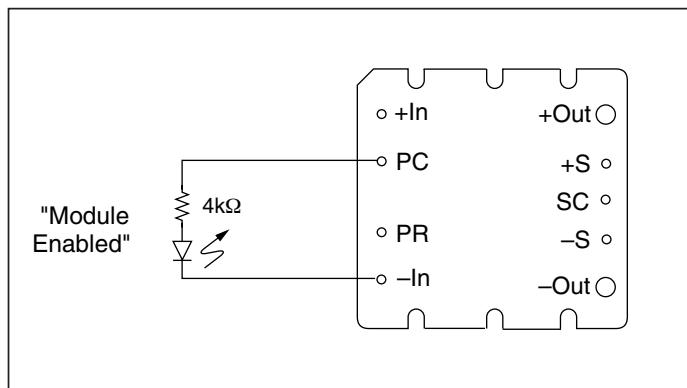


Figure 3—LED on-state indicator.

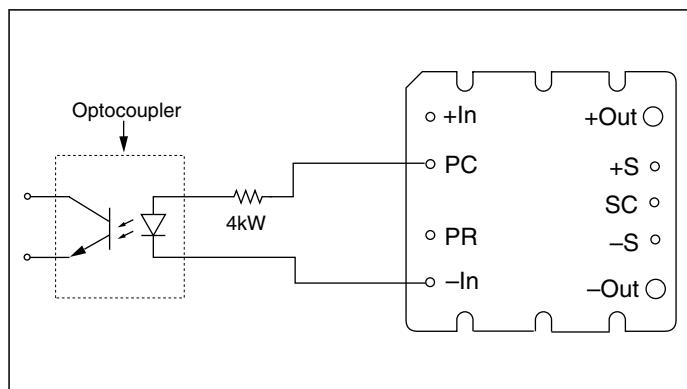


Figure 5—Isolated on-state indicator.

Module Alarm

The module contains “watchdog” circuitry which monitors input voltage, operating temperature and internal operating parameters. In the event that any of these parameters are outside of their allowable operating range, the module will shut down and PC will go low. PC will periodically go high and the module will check to see if the fault (as an example, overtemperature) has cleared. If the fault has not been cleared, PC will go low again and the cycle will restart. The SC pin will go low in the event of a fault and return to its normal state after the fault has been cleared. See Figures 2 and 4.

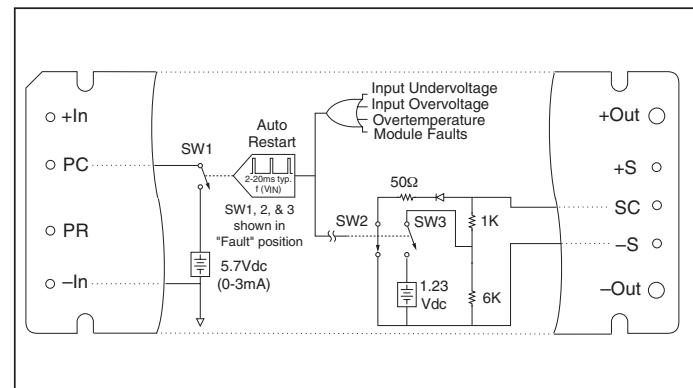


Figure 2—PC/SC module alarm logic.

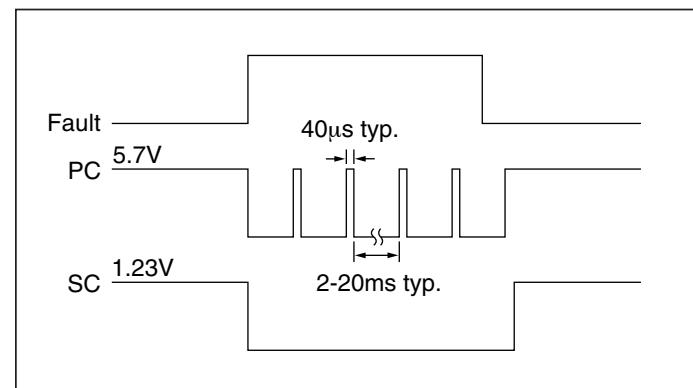


Figure 4—PC/SC module alarm timing.

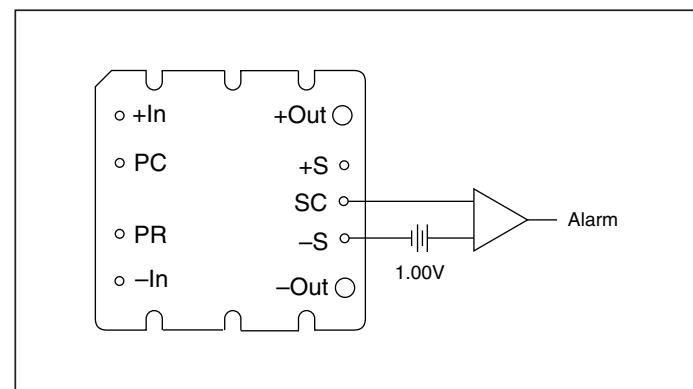


Figure 6—Secondary side on-state indicator.

CONTROL FUNCTIONS - SC PIN

Output Voltage Programming

The output voltage of the converter can be adjusted or programmed via fixed resistors, potentiometers or voltage DACs. See Figures 7 and 8.

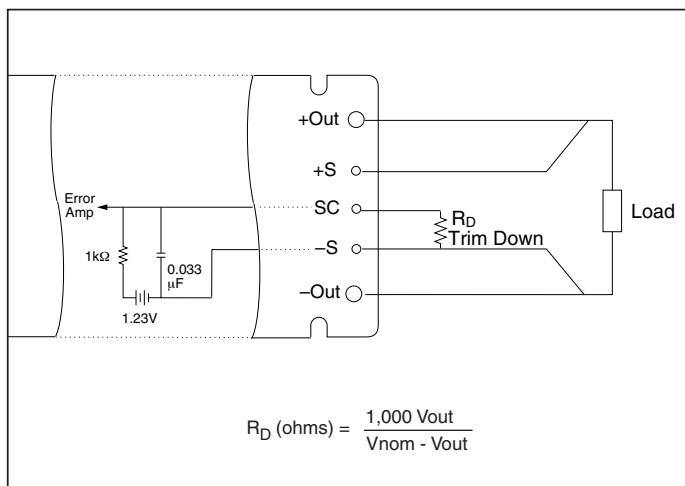


Figure 7—Output voltage trim down circuit.

Trim Down

1. This converter is not a constant power device – it has a constant current limit. Hence, available output power is reduced by the same percentage that output voltage is trimmed down. Do not exceed maximum rated output current.
2. The trim down resistor must be connected to the –Sense pin.

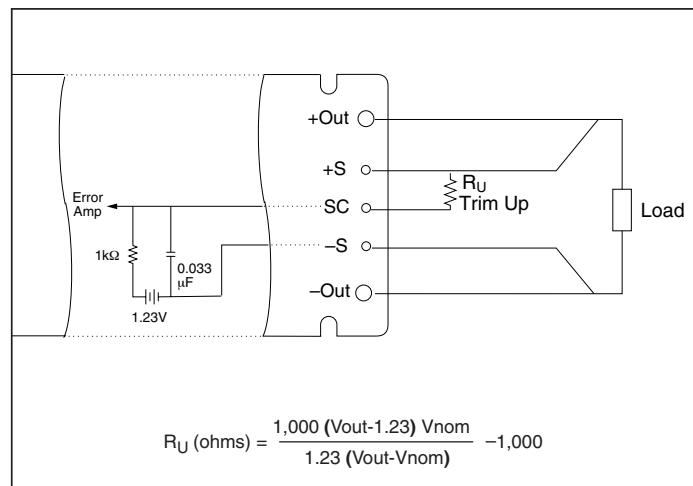
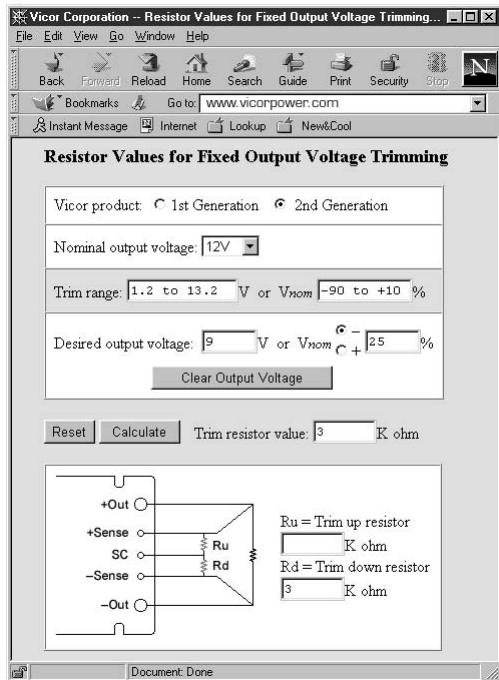


Figure 8—Output voltage trim up circuit.

Trim Up

1. The converter is rated for a maximum delivered power. To ensure that maximum rated power is not exceeded, reduce maximum output current by the same percentage increase in output voltage.
2. The trim up resistor must be connected to the +Sense pin.
3. Do not trim the converter above maximum trim range (typically +10%) or the output over voltage protection circuitry may be activated.



Trim resistor values calculated automatically:

On-line calculators for trim resistor values are available on the vicor website at: vicorpowers.com/tools.html.

Resistor values can be calculated for fixed trim up, fixed trim down and for variable trim up or down cases for both 1st and 2nd Generation DC-DC converters.

In addition to trimming information, the web site and the Applications Manual also include design tips, applications circuits, EMC suggestions, thermal design guidelines and PDF data sheets for all available Vicor products.

CONTROL FUNCTIONS - PR PIN

Parallel Operation

The PR pin supports paralleling for increased power with N+1 (N+M) redundancy and phased array capability. Modules of the same input voltage, output voltage, and power level will current share if all PR pins are suitably interfaced.

Compatible interface architectures include the following:

DC coupled single-wire interface. All PR pins are directly connected to one another. This interface supports current

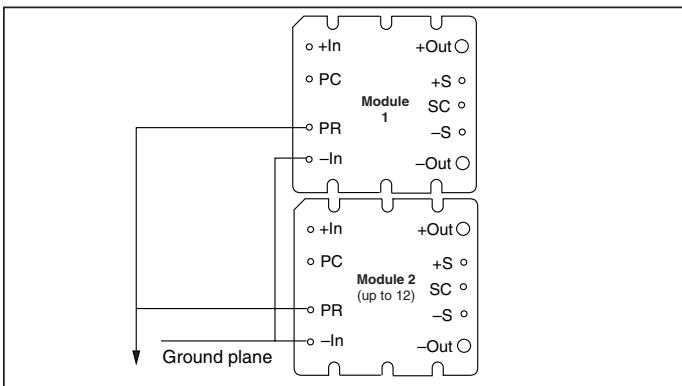


Figure 9—DC coupled single-wire interface.

sharing but is not fault tolerant. Minus In pins must be tied to the same electric potential. See Figure 9.

AC coupled single-wire interface. All PR pins are connected to a single communication bus through $0.001\mu F$ (500V) capacitors. This interface supports current sharing and is fault tolerant except for the communication bus. See Figure 10.

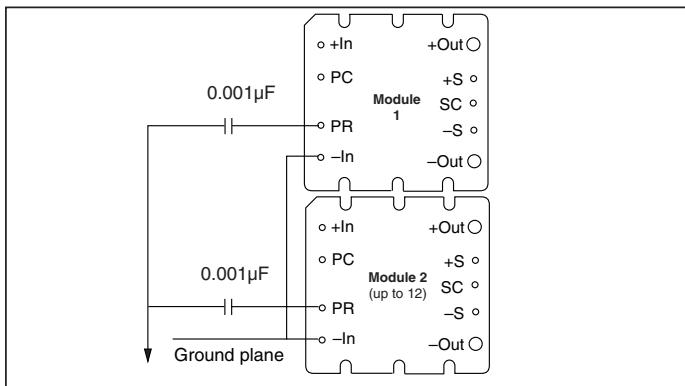


Figure 10—AC coupled single-wire interface.

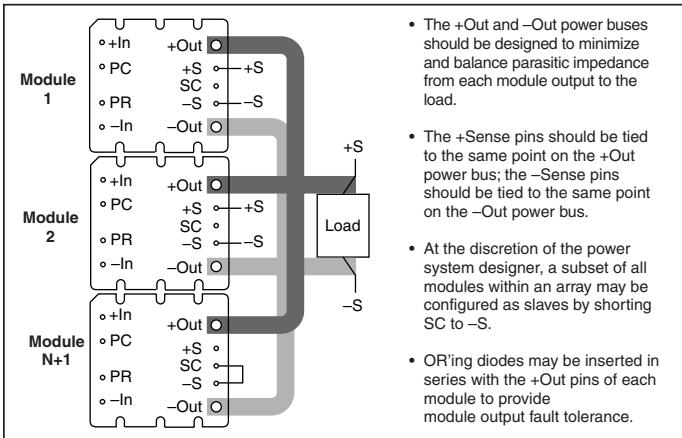


Figure 11—N+1 module array output connections.

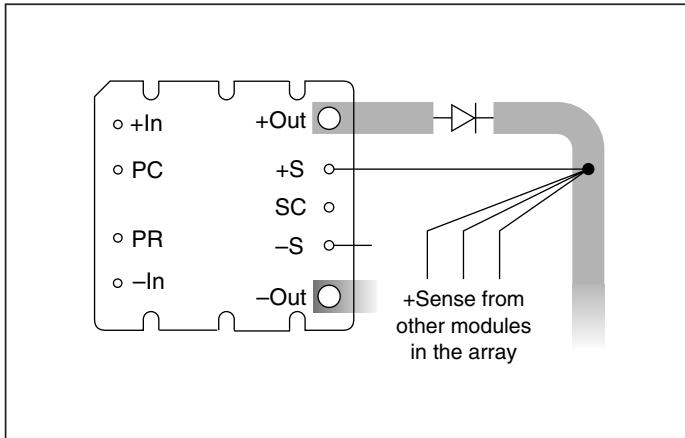


Figure 12—OR'ing diodes connections.

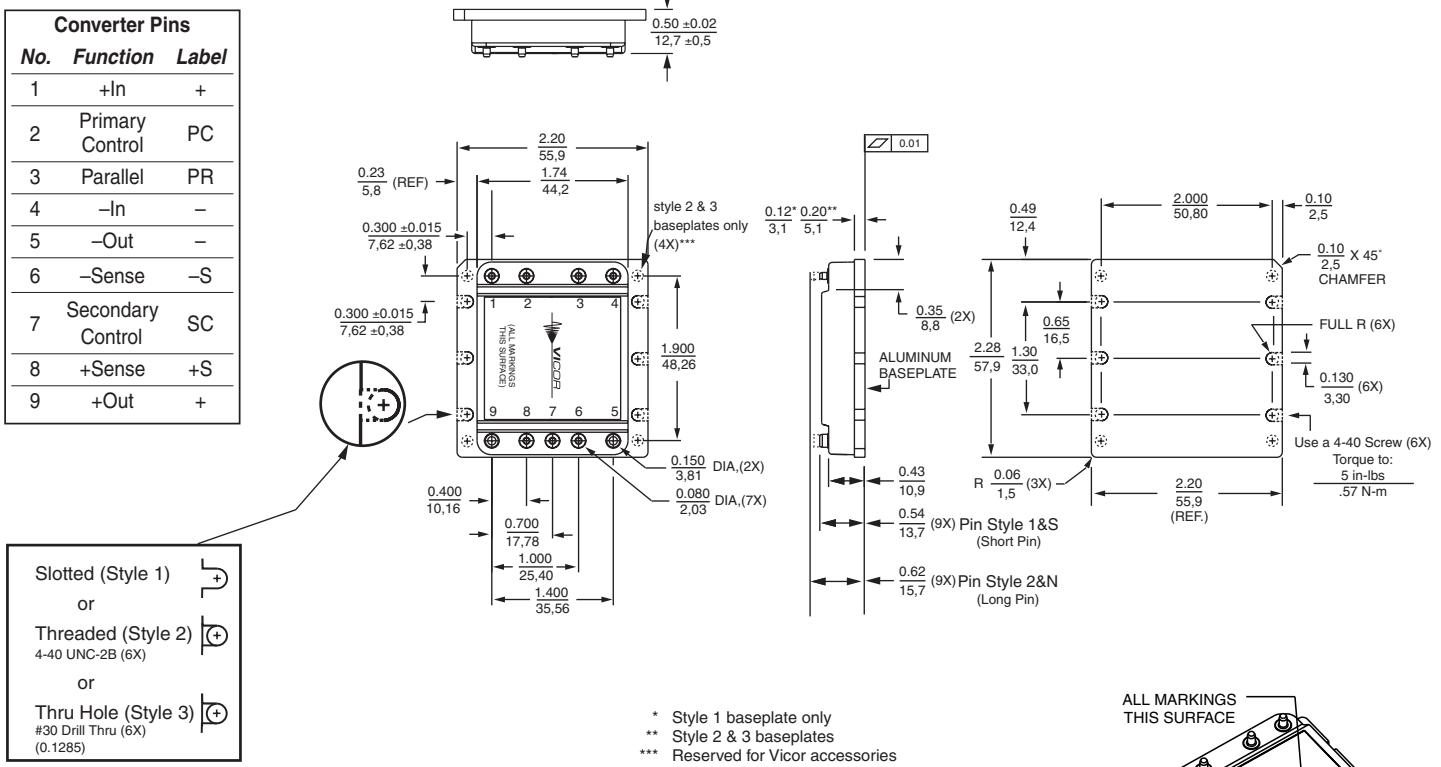
PIN STYLES *

| Designator | Description | Notes |
|------------|----------------|--------------------------------------|
| (None) | Short solder | Requires in-board, mounting |
| L | Long solder | On-board mounting for 0.065" boards |
| S | Short ModuMate | SurfMate or in-board socket mounting |
| N | Long ModuMate | On-board socket mounting |

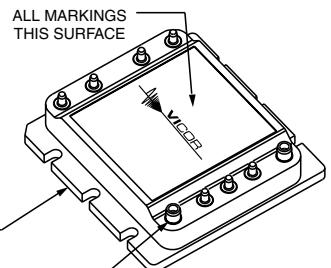
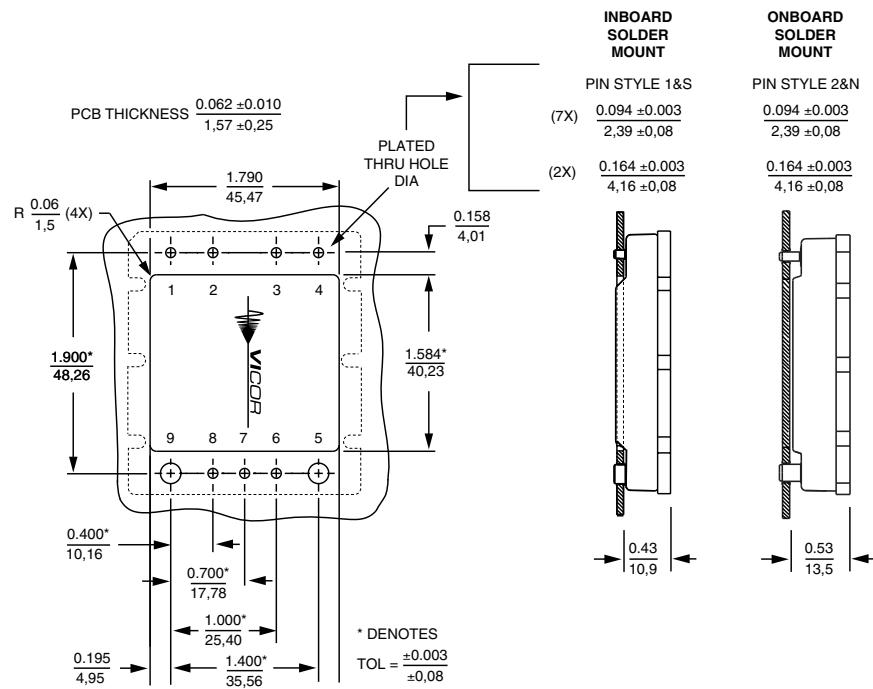
* Pin style designator follows the "A" after the output power and precedes the baseplate designator.
Ex. V375A24C600AN — Long ModuMate Pins

MECHANICAL DRAWINGS

MODULE OUTLINE



PCB MOUNTING SPECIFICATIONS



ALUMINUM BASEPLATE
PINS STYLES
SHORT & LONG (L) SOLDER:TIN/LEAD
HOT SOLDER DIPPED
STYLE S & N: GOLD PLATED COPPER

Unless otherwise specified, dimensions are in inches
mm

| Decimals | Tol. | Angles |
|----------|----------------------------|---------------|
| 0.XX | ± 0.01 ± 0.25 | $\pm 1^\circ$ |
| 0.XXX | ± 0.005 ± 0.127 | |

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