

EMDS12 E 2 J -161.1328M

Series —
RoHS Compliant (Pb-free) 2.5V 6 Pad 5mm x 7mm
Plastic SMD LVDS MEMS Oscillator

Frequency Tolerance/Stability ±25ppm Maximum over 0°C to +70°C

Duty Cycle -50 ±5(%) Nominal Frequency
161.1328MHz

Logic Control / Additional Output
Standby (ST) and Complementary Output

ELECTRICAL SPECIFICAT	TIONS	
Nominal Frequency	161.1328MHz	
Frequency Tolerance/Stability	±25ppm Maximum over 0°C to +70°C (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, 1st Year Aging at 25°C, Reflow, Shock, and Vibration)	
Aging at 25°C	±1ppm First Year Maximum	
Supply Voltage	+2.5Vdc ±0.125Vdc	
Input Current	75mA Maximum (Excluding Load Termination Current)	
Output Voltage Logic High (Voh)	1.425Vdc Typical	
Output Voltage Logic Low (Vol)	1.075Vdc Typical	
Differential Output Voltage (Vod)	247mVdc Minimum, 350mVdc Typical, 454mVdc Maximum	
Offset Voltage (Vos)	1.125V Minimum, 1.250V Typical, 1.375V Maximum	
Rise/Fall Time	225pSec Typical, 325pSec Maximum (Measured over 20% to 80% of waveform)	
Differential Output Error (dVod)	50mVdc Maximum	
Duty Cycle	50 ±5(%) (Measured at 50% of waveform)	
Offset Error (dVos)	50mVdc Maximum	
Load Drive Capability	100 Ohms Between Output and Complementary Output	
Output Logic Type	LVDS	
Logic Control / Additional Output	Standby (ST) and Complementary Output	
Output Control Input Voltage	Vih of 70% of Vcc Minimum or No Connect to Enable Output and Complementary Output, Vil of 30% of Vcc Maximum to Disable Output and Complementary Output (High Impedance)	
Standby Current	30μA Maximum (ST) Without Load	
Period Jitter (Deterministic)	0.2pSec Typical	
Period Jitter (Random)	2.0pSec Typical	
Period Jitter (RMS)	2.5pSec Typical, 3.5pSec Maximum	
Period Jitter (pk-pk)	25pSec Typical, 30pSec Maximum	
RMS Phase Jitter (Fj = 637kHz to 10MHz; Random)	1.6pSec Typical	
RMS Phase Jitter (Fj = 1MHz to 20MHz; Random)	0.7pSec Typical	
RMS Phase Jitter (Fj = 1.875MHz to 20MHz; Random)	0.6pSec Typical	
Start Up Time	10mSec Maximum	
Storage Temperature Range	-55°C to +125°C	

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS		
ESD Susceptibility	MIL-STD-883, Method 3015, Class 2, HBM 2000V	
Flammability	UL94-V0	
Mechanical Shock	MIL-STD-883, Method 2002, Condition G, 30,000G	
Moisture Resistance	MIL-STD-883, Method 1004	
Moisture Sensitivity Level	J-STD-020, MSL 1	
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K	
Resistance to Solvents	MIL-STD-202, Method 215	
Solderability	MIL-STD-883, Method 2003 (Six I/O Pads on bottom of package only)	
Temperature Cycling	MIL-STD-883, Method 1010, Condition B	

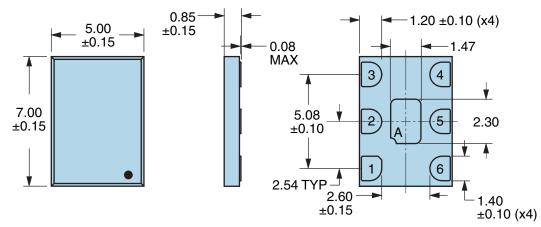


ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

Thermal Shock MIL-STD-883, Method 1011, Condition B

Vibration MIL-STD-883, Method 2007, Condition A, 20G

MECHANICAL DIMENSIONS (all dimensions in millimeters)



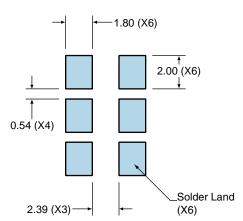
Note A: Center paddle is connected internally to oscillator ground (Pad 3).

PIN	CONNECTION
1	Standby (ST)
2	No Connect
3	Case Ground
4	Output
5	Complementary Output
6	Supply Voltage

LINE MARKING 1 XXXX or XXXXX XXXX or XXXXX=Ecliptek Manufacturing Lot Code

Suggested Solder Pad Layout

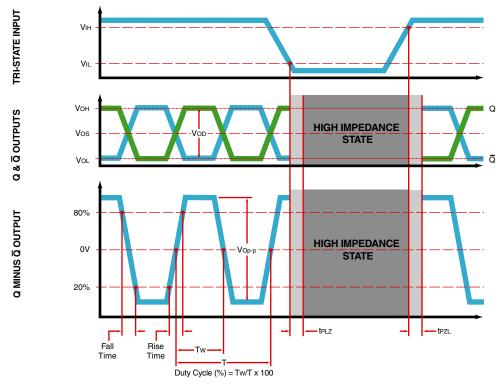
All Dimensions in Millimeters



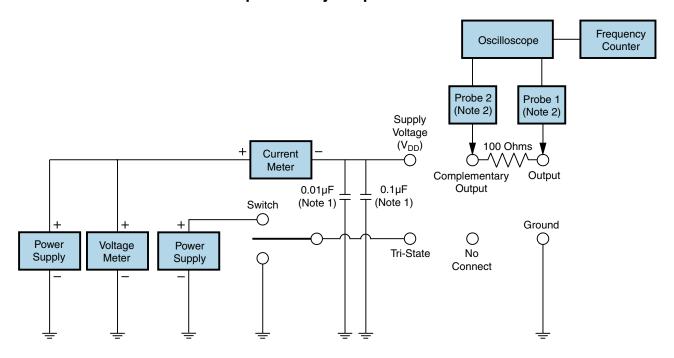
All Tolerances are ±0.1



OUTPUT WAVEFORM & TIMING DIAGRAM



Test Circuit for Tri-State and Complementary Output



- Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.
- Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>500MHz) passive probe is recommended.
- Note 3: Test circuit PCB traces need to be designed for a characteristic line impedance of 50 ohms.



Recommended Solder Reflow Methods



High Temperature Infrared/Convection

T _s MAX to T _∟ (Ramp-up Rate)	3°C/second Maximum
Preheat	
- Temperature Minimum (T _s MIN)	150°C
- Temperature Typical (T _s TYP)	175°C
- Temperature Maximum (T _S MAX)	200°C
- Time (t _s MIN)	60 - 180 Seconds
Ramp-up Rate (T _L to T _P)	3°C/second Maximum
Time Maintained Above:	
- Temperature (T∟)	217°C
- Time (t∟)	60 - 150 Seconds
Peak Temperature (T _P)	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (T _P Target)	250°C +0/-5°C
Time within 5°C of actual peak (tp)	20 - 40 seconds
Ramp-down Rate	6°C/second Maximum
Time 25°C to Peak Temperature (t)	8 minutes Maximum
Moisture Sensitivity Level	Level 1



Recommended Solder Reflow Methods



Low Temperature Infrared/Convection 240°C

T _S MAX to T _L (Ramp-up Rate)	5°C/second Maximum
Preheat	
- Temperature Minimum (T _s MIN)	N/A
- Temperature Typical (T _s TYP)	150°C
- Temperature Maximum (T _s MAX)	N/A
- Time (t _s MIN)	60 - 120 Seconds
Ramp-up Rate (T _L to T _P)	5°C/second Maximum
Time Maintained Above:	
- Temperature (T∟)	150°C
- Time (t∟)	200 Seconds Maximum
Peak Temperature (T _P)	240°C Maximum
Target Peak Temperature (T _P Target)	240°C Maximum 1 Time / 230°C Maximum 2 Times
Time within 5°C of actual peak (tp)	10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time
Ramp-down Rate	5°C/second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1

Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum.

High Temperature Manual Soldering

260°C Maximum for 5 seconds Maximum, 2 times Maximum.