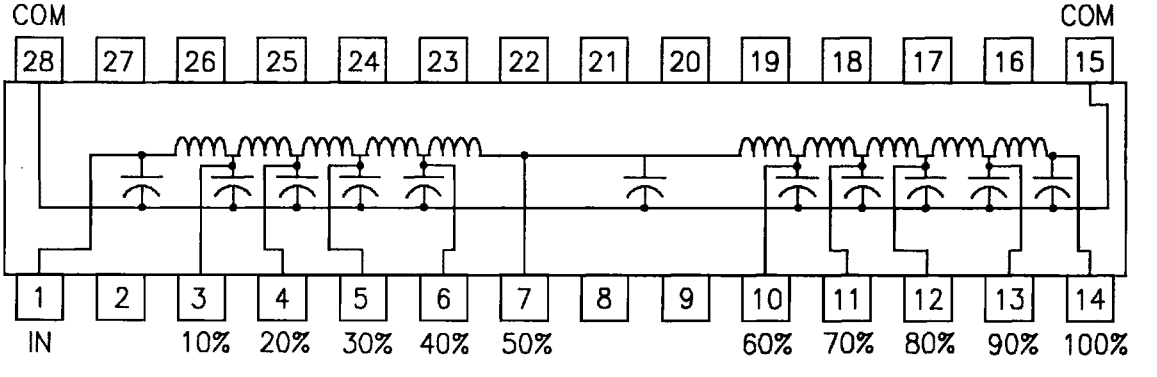
Fast Rise Time 10 Tap Passive Delay
28 Pin Dual-In-Line Modules

Pulse Overshoot (Pos)	5% to 10%, typical
Pulse Distortion (S)	3% typical
Working Voltage	25 VDC maximum
Dielectric Strength	100VDC minimum
Insulation Resistance	1,000 Megohms min. @ 100VDC
Input-to-Tap Tolerance	±5% or 1ns, whichever is greater
Impedance	±5%
Temperature Coefficient	70 ppm/°C, typical
Operating Temperature Range	-55° to +125°C
Storage Temperature Range	-65° to +150°C

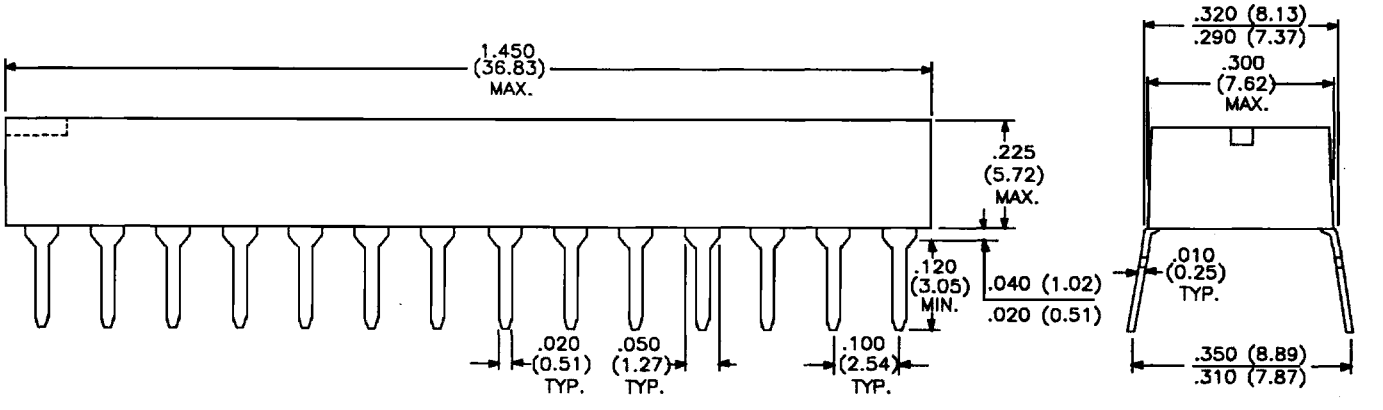
Electrical Specifications at 25°C

Delay Tolerances		50 Ohm Impedance			75 Ohm Impedance			100 Ohm Impedance		
Total (ns)	Tap-to-Tap (ns)	Part Number	Rise Time max. (ns)	DCR max. (Ohms)	Part Number	Rise Time max. (ns)	DCR max. (Ohms)	Part Number	Rise Time max. (ns)	DCR max. (Ohms)
10 ± 0.5	1.0 ± 0.4	TF10-5	1.5	1.1	TF10-7	1.5	1.2	TF10-10	1.5	1.3
20 ± 1.0	2.0 ± 0.5	TF20-5	3.2	1.3	TF20-7	3.2	1.4	TF20-10	3.2	1.5
40 ± 2.0	4.0 ± 1.0	TF405	5.7	1.6	TF40-7	5.7	1.7	TF40-10	5.9	2.0
50 ± 2.8	5.0 ± 1.0	TF50-5	6.2	1.9	TF50-7	6.2	2.0	TF50-10	6.4	2.2
75 ± 3.7	7.5 ± 2.0	TF75-5	9.2	2.1	TF75-7	9.2	2.2	TF75-10	9.4	2.3
80 ± 4.0	8.0 ± 2.0	TF805	9.5	2.2	TF80-7	9.6	2.3	TF80-10	9.9	2.4
100 ± 5.0	10.0 ± 2.0	TF100-5	10.6	2.3	TF100-7	10.8	2.5	TF100-10	12.5	2.7
120 ± 6.0	12.0 ± 2.0	TF120-5	11.5	2.4	TF120-7	11.6	2.8	TF120-10	13.0	2.9
150 ± 7.5	15.0 ± 2.0	TF150-5	15.7	2.6	TF150-7	15.8	3.0	TF150-10	16.1	3.2
160 ± 8.0	16.0 ± 2.0	TF160-5	16.4	2.7	TF160-7	16.5	3.1	TF160-10	16.8	3.3
200 ± 10.0	10.0 ± 2.0	TF200-5	19.5	2.9	TF200-7	19.6	3.3	TF200-10	20.0	3.8

TF Style Schematic



PHYSICAL DIMENSIONS inches (mm)



VARIATIONS AVAILABLE. FOR INTERMEDIATE VALUES AND/OR CUSTOM DESIGNS PLEASE CONSULT THE FACTORY.

Specifications subject to change without notice.

TF-6/93

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