

PDM: Rev:J

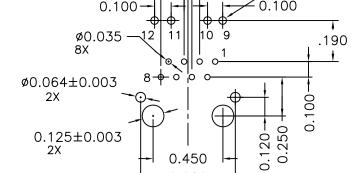
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-0.0250.050 Ø0.040±0.003 0.100 4X 0.100



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4xR0.010 - 0.670 -Max 560 0.010 o

RECOMMENDED PANEL OPENING

# Mounting Pattern:

- 0.620

#### NOTES:

1. RECOMMENDED PCB THICKNESS 0.062".

0.040 -

- 2. HOUSING MATERIAL: HIGH TEMP. THERMOPLASTIC, COLOR BLACK, UL94V-0.
- 3. CONTACT:

MATERIAL: COPPER ALLOY MATING AREA PLATING: GOLD OVER NICKEL

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4. SHIELD:

MATERIAL: COPPER ALLOY

PLATING: NICKEL

GROUND TABS PLATING: TIN DIPPED

- 5. IF LEAD FREE PART NUMBER, THE HOUSING WILL WITHSTAND EXPOSURE TO 260° PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.60mm MIN. THICK CIRCUIT BOARD.
- 6. PACKAGING OF LEAD FREE PRODUCTS MEETS LABELING AND PACKAGING SPECIFICATION GS-14-920.
- 7. LEAD FREE PRODUCTS MEET EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008.

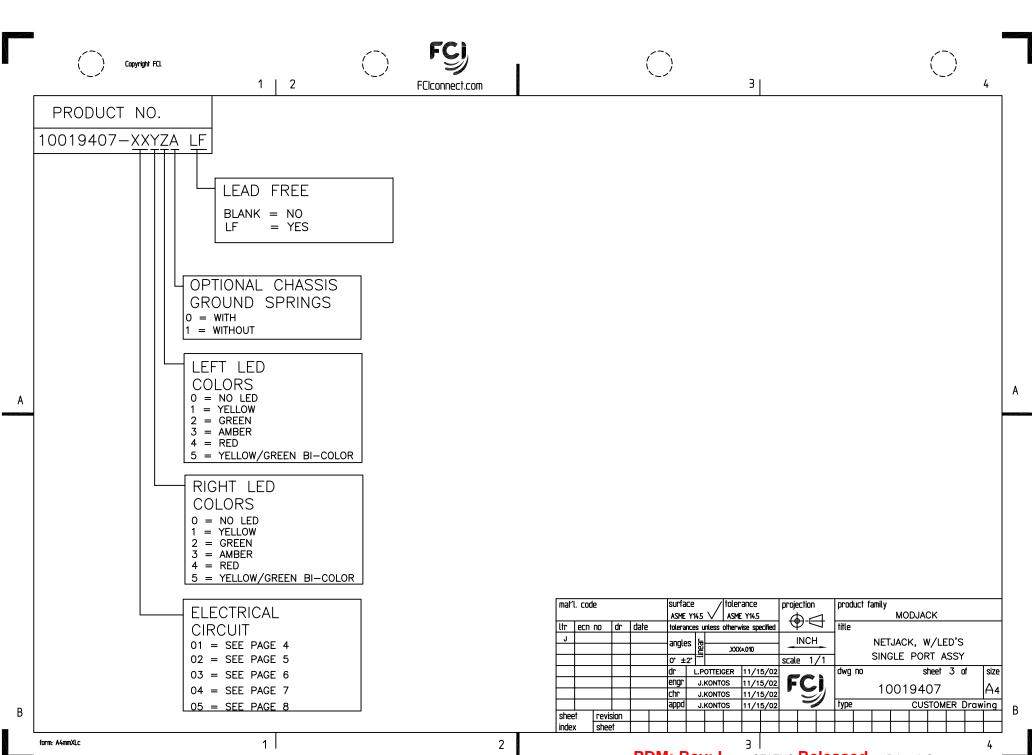
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Printed: Sep 02, 2006

form: A4mmXLc



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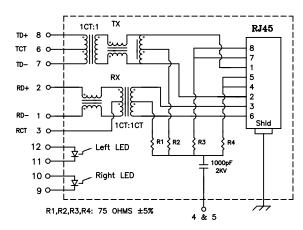
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Schematic: for P/N 10019407-01YZA OR -01YZALF

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NOTE: CONNECT CHS GND TO PCB GROUND

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### Electrical Specifications: @25°C

Isolation Voltage: 1500 Vrms (INPUT to OUTPUT) Isolation Voltage: 500 Vrms (8+6+7-2+1+3) TURNS RATIO: TX 1CT:1 ±5% RX 1CT:1CT ±5% OCL: 350uH Minimum @100KHz 100mV 8mADC Insertion Loss (1MHz-100MHz): -1.0dB Max Return Loss: 30MHz -16dB Typical 60MHz -14dB Typical

80MHz -12dB Typical CROSSTALK: 1MHz-100MHz -35dB Minimum CMRR: 1MHz-100MHz -35dB Minimum

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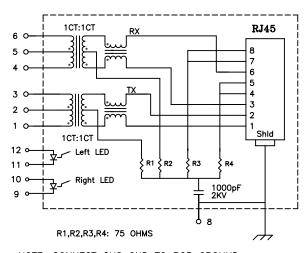
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Schematic: for P/N 10019407-02YZA OR -02YZALF

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NOTE: CONNECT CHS GND TO PCB GROUND

### Electrical Specifications: @25°C

Isolation Voltage: 1500 Vrms (Input to Output)
Isolation Voltage: 500 Vrms (P1+2+3 to P4+5+6)
Turns Ratio: TX 1CT:1CT ±3% RX 1CT:1CT ±3%

CABLE SIDE OCL: 350uH Minimum @100KHz 100mV 8mADC

Cw/w: 27pF Typical @100KHz 100mV

Insertion Loss (300KHz-100MHz): -1.0dB Minimum

Return Loss: @30MHz -18dB Minimum @80MHz -12dB Minimum

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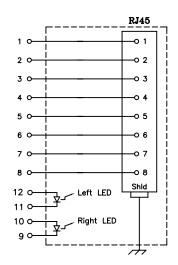
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Schematic: for P/N 10019407-03YZA OR -03YZALF



Electrical Specifications: @25°C

Isolation Voltage: 500 Vrms (WIRE to WIRE)

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No Magnetics

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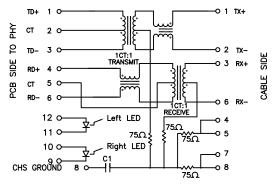
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Schematic: for P/N 10019407-04YZA OR -04YZALF

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NOTE: CONNECT CHS GND TO PCB GROUND

## Electrical Specifications: @25°C

Isolation Voltage: 1500 Vrms (INPUT TO OUTPUT) TURNS RATIO: TX 1CT:1 RX 1CT:1CT OCL: 350uH Minimum OCL WITH 8 mA BIAS CURRENT Insertion Loss (1MHz-65MHz): -1.0dB Max Return Loss: 30MHz -16dB Typical

60MHz -12dB Typical

80MHz -10dB Typical

CROSSTALK: 1MHz-100MHz -30dB Minimum 1MHz-50MHz -30dB Minimum 50MHz-150MHz -20dB Minimum

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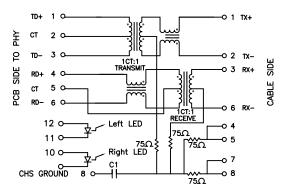
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Schematic: for P/N 10019407-05YZA OR -05YZALF

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NOTE: CONNECT CHS GND TO PCB GROUND

### Electrical Specifications: @25°C

Isolation Voltage: 1500 Vrms (INPUT TO OUTPUT)

TURNS RATIO: TX 1CT:1 RX 1CT:1CT

OCL: 350uH Minimum OCL WITH 8 mA BIAS CURRENT

Insertion Loss (1MHz-65MHz): -1.0dB Max

Return Loss: 30MHz -16dB Typical

60MHz -12dB Typical

80MHz -10dB Typical

CROSSTALK: 1MHz-100MHz -30dB Minimum

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1MHz-50MHz -30dB Minimum 50MHz-150MHz -20dB Minimum CMRR:

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