

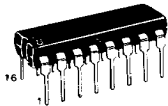
1200 BAUD MINIMUM SHIFT KEY MODEM

FEATURES:

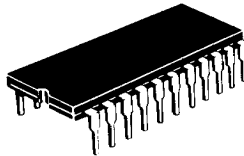
- Full Duplex 1200 Baud MSK
- On-Chip R_x/T_x Bandpass Filters
- R_x/T_x Synchronization Clocks
- Carrier Detect O/P
- Narrow Band Frequency Shift
- Few External Components Required
- Read/Write Sync Control
- Easy Microprocessor Interface

APPLICATIONS:

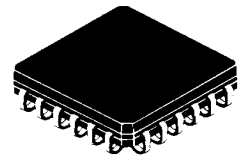
- Nordic Mobile Telephone 450/900 Cellular
- German ZVEI
- French Radiocomm 2000
- UK Band III
- Japanese Personal Radio trunked SMR systems
- Packet Radio
- Radio Telemetry



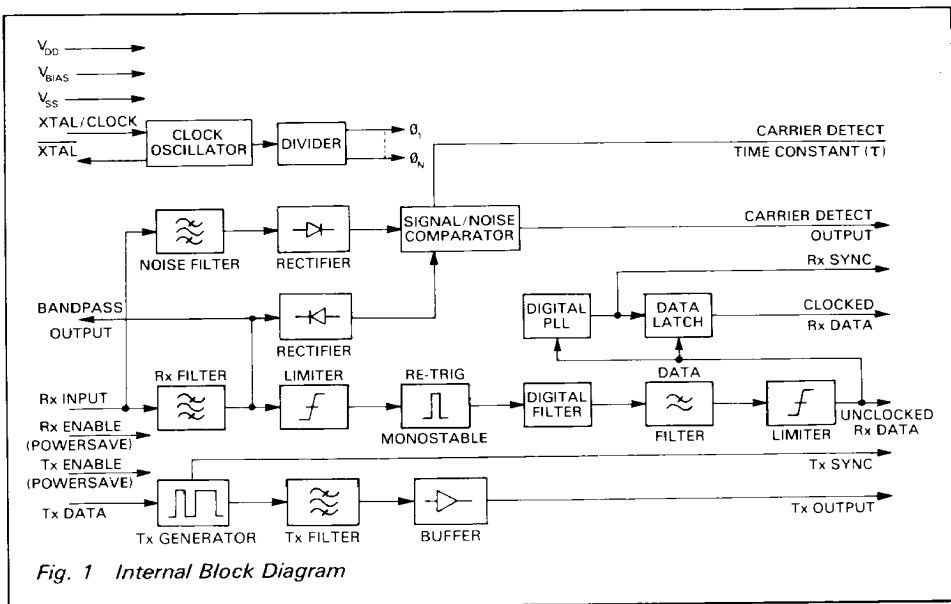
**MX519J (CDIP)
MX519P (PDIP)
16 pins**



**MX419J (CDIP)
MX419P (PDIP)
22 pins**



**MX419LH
(24p PLCC)**



DESCRIPTION:

The MX419/519 is a single chip CMOS LSI Modem employing Minimum Shift Key signaling at 1200 baud, compatible with the data transmission standards of numerous operating systems worldwide. In addition to the applications listed above, the American Association of Railroads uses 1200 baud MSK on board trains in a radio telemetry application that replaced the caboose.

MSK (also known as FFSK) operation at 1200 baud employs phase continuous 1200/1800 Hz mark and space frequencies in which data transitions occur only at the signal zero crossing point. This provides identical mark/space period timing and an occupied band of only 1200 Hz. The MX419/519 derives its carriers, switched capacitor filter clocks, and R_x and T_x synchronization from an external 1.008 MHz crystal. A carrier detect O/P is provided as well as a recovered R_x clock.

The MX419 succeeds the MX409 in a functionally equivalent package, while adding an output from the R_x bandpass filter. The MX519 employs the same die in a smaller, more economical 16 pin DIP package.

MX419/519 PIN FUNCTION TABLE

PIN			FUNCTION/DESCRIPTION
DIP MX419J,P	PLCC MX419LH	DIP MX519J,P	
1	1	16	Xtal/Clock: The input to an on-chip inverter for use with a 1.008MHz xtal. Alternatively, a 1.008MHz clock may be used.
2	2	1	Xtal: Output of on-chip inverter.
3	3	2	Tx Sync O/P: A 1200Hz squarewave used to synchronize the input of logic data and transmission of the MSK signal (See Fig. 5).
4	4		No Connection: Leave open circuit.
5	5	3	Tx Signal O/P: With transmitter disabled, this pin is set to a high impedance state. When transmitter is enabled, this pin outputs the 1200/1800Hz (140 step pseudo-sinewave) MSK signal (See Fig. 5).
6	7	4	Tx Data I/P: Serial logic data to be transmitted, is input to this pin and synchronized by the "Tx Sync O/P" (See Fig. 5).
7	8	5	Tx Enable: A logic '1' applied to this input will put the transmitter into powersave while forcing "Tx Sync O/P" to logic '1' and "Tx Signal O/P" to a high impedance state. A logic '0' will enable the transmitter (See Fig. 5). This pin is internally pulled to V_{DD} .
8	9		Bandpass O/P: This is the output of the Rx 900-2100Hz bandpass filter. The output impedance of this pin is typically 10k Ω and may require buffering prior to use.
9	10	6	Rx Enable: A logic '0' applied to this input will put the receiver into powersave while forcing "Clocked Data O/P" and "Carrier Detect" to logic '0.' A logic '1' will enable the receiver (See Figures 2 and 6). "Rx Sync Out" may be logic '1' or '0' during powersave. This pin is internally pulled to V_{DD} .
10	11	7	Bias: Provides bias internally and should be decoupled externally to V_{SS} by a capacitor (See Fig. 2).
11	12	8	V_{SS}: Negative supply.
12	13	9	Unclocked Data O/P: This pin outputs recovered asynchronous serial data from the receiver.
13	14	10	Clocked Data O/P: This pin outputs recovered synchronous serial data from the receiver and is internally latched out by a recovered clock appearing on the "Rx Sync O/P" pin (See Figures 2 and 6).

MX419/519 PIN FUNCTION TABLE

PIN			FUNCTION/DESCRIPTION
DIP MX419J,P	PLCC MX419LH	DIP MX519J,P	
14	15	11	Carrier Detect: This pin will output a logic '1' when an MSK signal is being received.
15	16	12	Rx Signal I/P: This is the MSK signal input pin for the receiver and should be decoupled via a capacitor C_3 .
16	17		No Connection: Leave open circuit.
17	18	13	Rx Sync O/P: This is a flywheel 1200Hz squarewave output which, upon presentation of MSK data signal, is synchronized internally to the incoming data (<i>See Figures 2 and 6</i>).
18,19	19, 20, 21		No Connection: Leave open circuit.
20	22	14	Carrier Detect Time Constant (τ): This input forms part of the carrier detect integration function. The value of C_4 connected to this pin will affect the carrier detect response time and hence noise performance (<i>See Fig. 2, Note 4</i>).
21	23		No Connection: Leave open circuit.
22	24	15	V_{DD}: Positive supply.

Note: Output Loading.

Large capacitive loads could cause the output pins of this device to oscillate. If capacitive loads in excess of 200pF are unavoidable, a resistor of typically $<100\Omega$ put in series with the load should minimize this effect.

MX419/519 ELECTRICAL SPECIFICATIONS

Absolute Maximum Ratings

Exceeding the maximum rating can result in device damage. Operation of the device outside the operating limits is not implied.

Supply voltage	– 0.3V to 7.0V
Input voltage at any pin (ref $V_{SS} = 0V$)	– 0.3V to ($V_{DD} + 0.3V$)
Output sink/source current (total)	20mA
Operating temperature range:	MX419J/MX519J – 30°C to +85°C
	MX419P,LH/MX519P – 30°C to +70°C
Storage temperature range:	MX419J/MX519J – 55°C to +125°C
	MX419P,LH/MX519P – 40°C to +85°C
Maximum device dissipation:	All versions 100mW

Operating Limits

$V_{DD} = +5V$, $T_{amb} = 25^\circ C$, $\varnothing = 1.008MHz$ (Xtal), $\Delta f\varnothing = 0$

All characteristics measured using the standard test circuit (figure 4) with the following test parameters which is valid for all tests unless otherwise stated:

0dB reference	300mV rms
Noise	(band limited 5kHz gaussian white noise)
SNR ratio measured in bit rate bandwidth (1200Hz)	

Characteristics	See Note	Min	Typ	Max	Unit
Static Characteristics					
Supply volts		4.5	5.0	5.5	V
Supply current: Rx (Enabled) Tx (Disabled)		—	3.6	—	mA
Rx (Enabled) Tx (Enabled)		—	4.5	—	mA
Rx (Disabled) Tx (Disabled)		—	650	—	μA
Logic '1' level		80%V _{DD}	—	—	V
Logic '0' level		—	—	20%V _{DD}	V
Digital O/P Impedance		—	4	—	kΩ
Analog and Digital input impedance		100	—	—	kΩ
Tx O/P impedance		—	10	—	kΩ
On-chip crystal oscillator:					
R _{in}		10	—	—	MΩ
R _{out}		5	—	15	kΩ
Inverter gain		10	—	20	dB
Gain Bandwidth Product		3 × 10 ⁶	—	—	
Crystal frequency	1	—	1.008	—	MHz
Dynamic Characteristics					
Receiver:					
Signal Input: Dynamic range (50dB SNR)	2,3	100	230	1000	mV rms
Bit Error Rate: 12dB SNR	3	—	7.0	—	10 ⁻⁴
20dB SNR	3	—	1.0	—	10 ⁻⁸
Receiver Synchronization 12dB SNR:	6				
Probability of bit 8 being correct			0.99		
Probability of bit 16 being correct			0.995		
Carrier Detect	6				
Probability of Carrier Detect being high:					
12dB SNR after bit 8	4		0.99	0.98	
12dB SNR after bit 16	4		0.999	0.995	
0dB noise				0.01	
Transmitter O/P					
Tx O/P level		—	775	—	mV rms
Output level variation 1200/1800Hz		0	—	± 1.00	dB
O/P distortion		—	3	5	%
3rd harmonic distortion		—	2	3	%
Logic '1' carrier frequency	5	—	1200	—	Hz
Logic '0' carrier frequency	5	—	1800	—	Hz
Isochronous distortion					
1200Hz-1800Hz		—	25	40	μs
1800Hz-1200Hz		—	20	40	μs

Notes: 1. Crystal tolerance depends on system requirements.

2. See Fig. 3.

3. SNR (Bit Rate Bandwidth).

4. Signal In = 0db (300 mV rms), SNR ≥ 20dB

5. Depending on crystal tolerance.

6. 101010 Pattern.

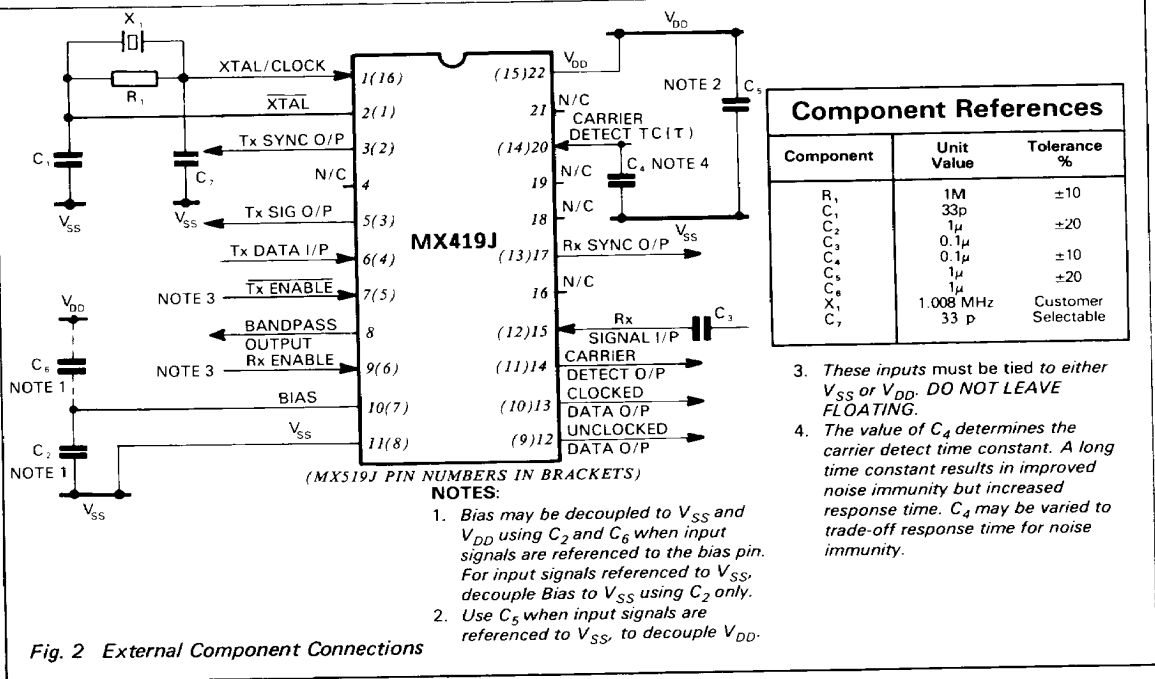


Fig. 2 External Component Connections

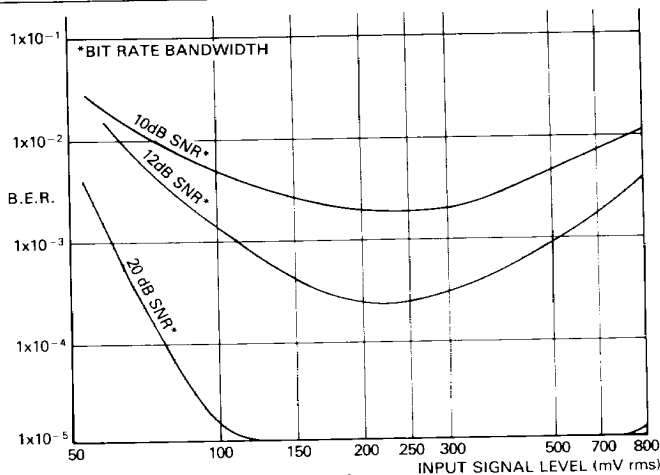


Fig. 3 Typical Variation of 'B.E.R.' with Input Signal Level

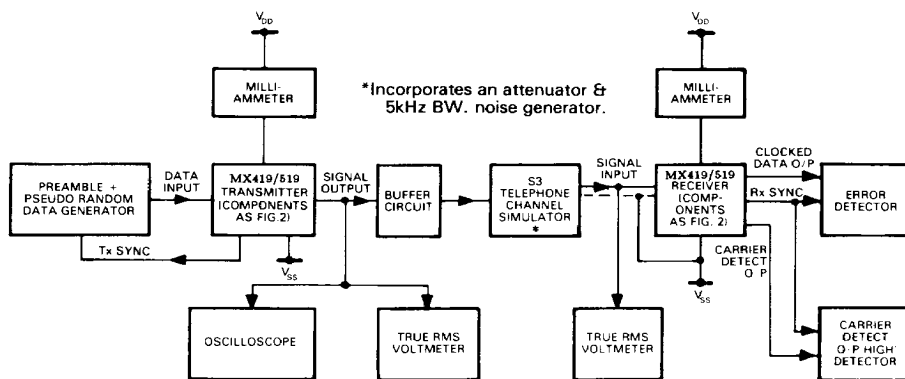
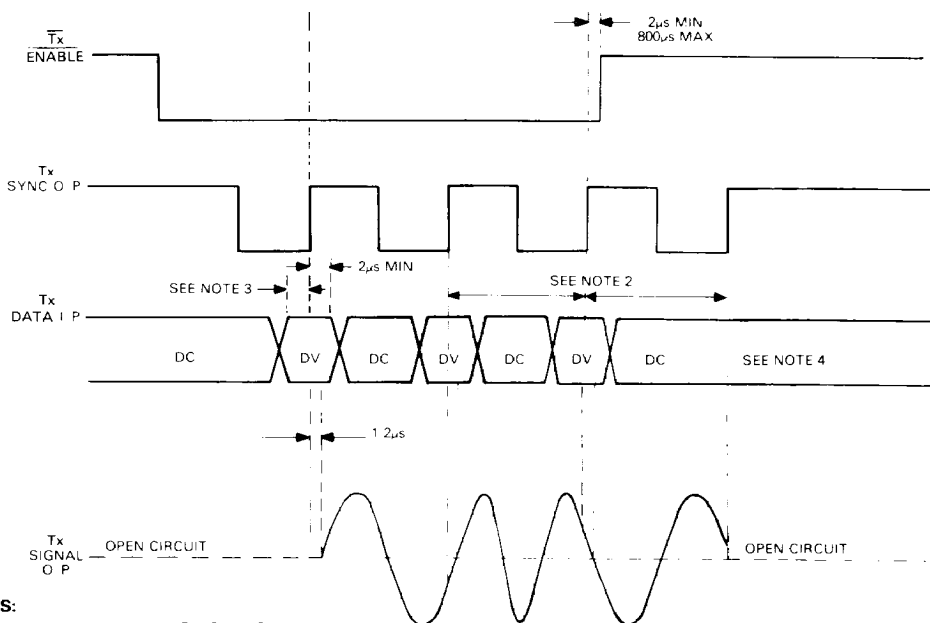


FIG. 4: MX419/519 TEST SET-UP



NOTES:

1. All timings are related to TX Sync Output.
2. 0.833ms for 1.008MHz Crystal Input.
3. 2µs Min + Crystal tolerance.
4. DC = Don't Care, DV = Data Valid.

Fig. 5 Transmitter Timing Diagram

