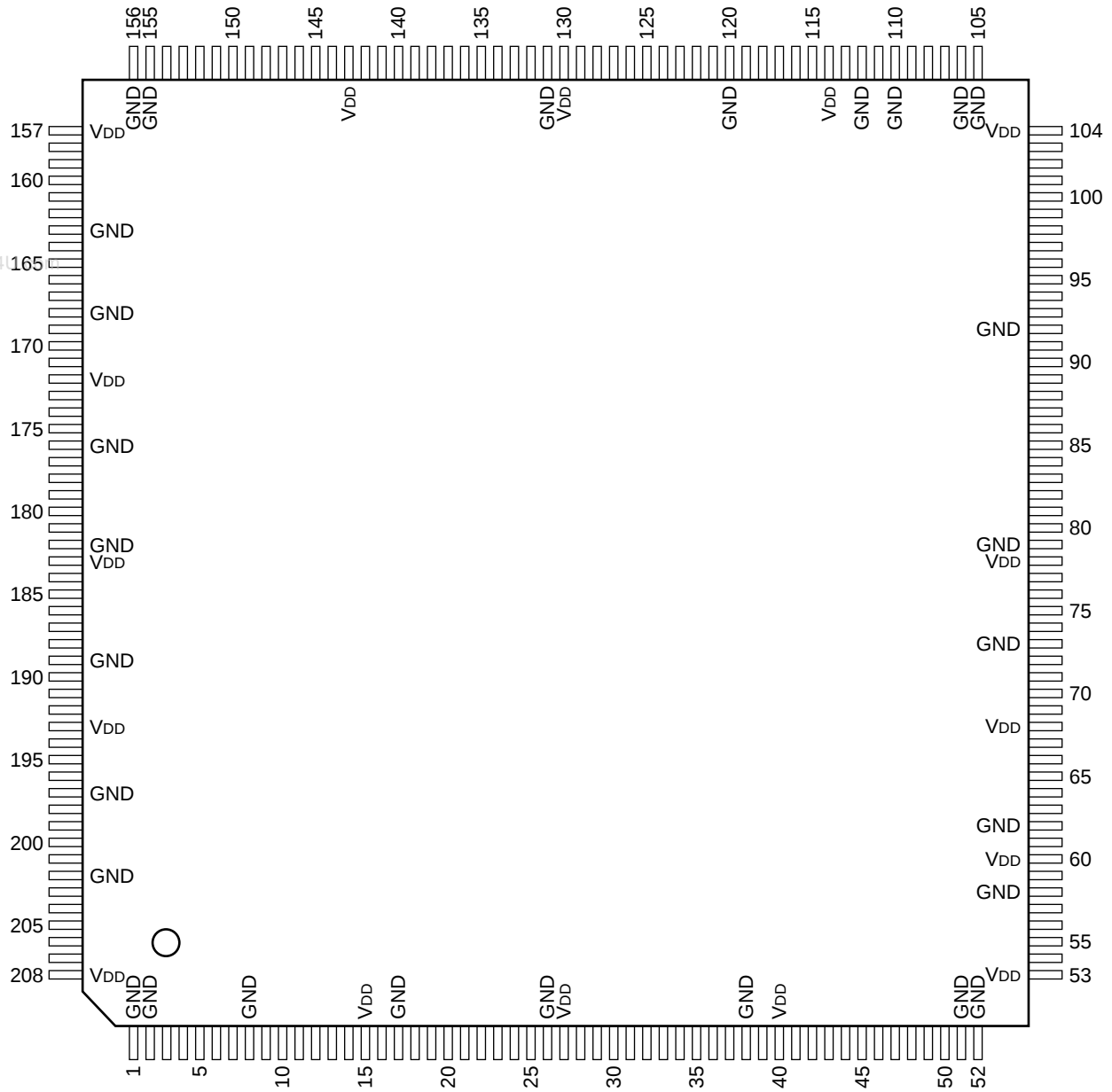


C-MOS PROCESSOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	53	—	V _{DD}	105	—	GND	157	—	V _{DD}
2	—	GND	54	I	$\overline{\text{TE}}$	106	—	GND	158	O	GPO00
3	O	BPO08	55	I	SCLM	107	I	DTL09	159	O	GPO01
4	O	BPO09	56	I	SDAMI	108	I	DTL10	160	O	GPO02
5	O	ZO	57	O	SDAMO	109	I	DTL11	161	O	GPO03
6	O	MK1O	58	—	GND	110	—	GND	162	O	GPO04
7	O	MK2O	59	I	TRST	111	I	CLK	163	—	GND
8	—	GND	60	—	V _{DD}	112	—	GND	164	O	GPO05
9	I	SYNCI	61	I	TCK	113	I	$\overline{\text{POR}}$	165	O	GPO06
10	I	TLI	62	—	GND	114	—	V _{DD}	166	O	GPO07
11	I	TLBI	63	I	TMS	115	I	BPI00	167	O	GPO08
12	O	VDO	64	I	TDI	116	I	BPI01	168	—	GND
13	O	HDO	65	O	TDO	117	I	BPI02	169	O	GPO09
14	O	SKD	66	I	HDI	118	I	BPI03	170	O	YPO00
15	—	V _{DD}	67	I	VDI	119	I	BPI04	171	O	YPO01
16	O	CK18	68	—	V _{DD}	120	—	GND	172	—	V _{DD}
17	—	GND	69	O	DET10	121	I	BPI05	173	O	YPO02
18	I	PDA8	70	O	DET11	122	I	BPI06	174	O	YPO03
19	I	PDA7	71	O	DET12	123	I	BPI07	175	O	YPO04
20	I	PDA6	72	O	DET13	124	I	BPI08	176	—	GND
21	I	PDA5	73	—	GND	125	I	BPI09	177	O	YPO05
22	I	PDA4	74	O	DET20	126	I	BPI10	178	O	YPO06
23	I	PDA3	75	O	DET21	127	I	BPI11	179	O	YPO07
24	I	PDA2	76	O	DET22	128	I	GPI00	180	O	YPO08
25	I	PDA1	77	O	DET23	129	I	GPI01	181	O	YPO09
26	—	GND	78	—	V _{DD}	130	—	V _{DD}	182	—	GND
27	—	V _{DD}	79	—	GND	131	—	GND	183	—	V _{DD}
28	I	PDA0	80	I	MPXI00	132	I	GPI02	184	O	YPO10
29	I/O	PDI7	81	I	MPXI01	133	I	GPI03	185	O	YPO11
30	I/O	PDI6	82	I	MPXI02	134	I	GPI04	186	O	RPO00
31	I/O	PDI5	83	I	MPXI03	135	I	GPI05	187	O	RPO01
32	I/O	PDI4	84	I	MPXI04	136	I	GPI06	188	O	RPO02
33	I/O	PDI3	85	I	MPXI05	137	I	GPI07	189	—	GND
34	I/O	PDI2	86	I	MPXI06	138	I	GPI08	190	O	RPO03
35	I/O	PDI1	87	I	MPXI07	139	I	GPI09	191	O	RPO04
36	I/O	PDI0	88	I	MPXI08	140	I	GPI10	192	O	RPO05
37	I	PORS	89	I	MPXI09	141	I	GPI11	193	—	V _{DD}
38	—	GND	90	I	MPXI10	142	I	RPI00	194	O	RPO06
39	I	$\overline{\text{WE}}$	91	I	MPXI11	143	—	V _{DD}	195	O	RPO07
40	—	V _{DD}	92	—	GND	144	I	RPI01	196	O	RPO08
41	I	$\overline{\text{CE}}$	93	I	MPXI12	145	I	RPI02	197	—	GND
42	I	$\overline{\text{RE}}$	94	I	MPXI13	146	I	RPI03	198	O	RPO09
43	I	SCLI	95	I	DTL00	147	I	RPI04	199	O	BPO00
44	I	SDAII	96	I	DTL01	148	I	RPI05	200	O	BPO01
45	O	SDAIO	97	I	DTL02	149	I	RPI06	201	O	BPO02
46	I	IRC	98	I	DTL03	150	I	RPI07	202	—	GND
47	I	SLAI	99	I	DTL04	151	I	RPI08	203	O	BPO03
48	I	SLAM	100	I	DTL05	152	I	RPI09	204	O	BPO04
49	I	TEN	101	I	DTL06	153	I	RPI10	205	O	BPO05
50	I	TIN	102	I	DTL07	154	I	RPI11	206	O	BPO06
51	—	GND	103	I	DTL08	155	—	GND	207	O	BPO07
52	—	GND	104	—	V _{DD}	156	—	GND	208	—	V _{DD}

INPUT

BPI00 - BPI11	: B CHANNEL SIGNAL
$\overline{\text{CE}}$	: L: PARALLEL I/O CHIP ENABLE
CLK	: MASTER CLOCK
DTL00 - DTL11	: DETAIL
GPI00 - GPI11	: G CHANNEL SIGNAL
HDI	: HORIZONTAL DRIVE
IRC	: IRIS CPU PRESENCE
MPXI00 - MPXI13	: DETECTION MULTIPLEX
PDA0 - PDA8	: PARALLEL DATA ADDRESS
$\overline{\text{POR}}$	: L: POWER ON RESET
PORS	: PARALLEL I/O ; SERIAL I/O SELECT
RPI00 - RPI11	: R CHANNEL SIGNAL
$\overline{\text{RE}}$	: L: PARALLEL I/O READ ENABLE
SCLI	: SERIAL I/O IRIS CLOCK
SCLM	: SERIAL I/O MAIN CLOCK
SDAI	: SERIAL I/O IRIS DATA
SDAMI	: SERIAL I/O MAIN DATA
SLAI	: IRIS SERIAL I/O LOCAL ADDRESS
SLAM	: MAIN SERIAL I/O LOCAL ADDRESS
SYNCI	: SYNC
TCK	: BOUNDARY SCAN CLOCK
TDI	: BOUNDARY SCAN TEST DATA
$\overline{\text{TE}}$	: L: RAM TEST ENABLE
TEN	: TEST ENABLE
TIN	: RAM TEST
TLBI	: TITLE BACK
TLI	: TITLE
TMS	: BOUNDARY SCAN MODE
TRST	: BOUNDARY SCAN RESET
$\overline{\text{VDI}}$	: VERTICAL DRIVE
$\overline{\text{WE}}$	: L: PARALLEL I/O WRITE ENABLE

OUTPUT

BPO00 - BPO09	: B SIGNAL
CK18	: DIVIDED-BY-TWO CLOCK
DET10 - DET23	: COLOR DETECTION SIGNAL
GPO00 - GPO09	: G SIGNAL
HDO	: HORIZONTAL DRIVE
MK10, MK20	: MARKER1, 2
RPO00 - RPO09	: R SIGNAL
SDAIO	: SERIAL I/O IRIS DATA
SDAMO	: SERIAL I/O MAIN DATA
SKD	: DOMAIN OF COLOR DETECTION
TDO	: BOUNDARY SCAN TEST DATA
VDO	: VERTICAL DRIVE
YPO00 - YPO11	: Y SIGNAL
ZO	: ZEBRA SIGNAL

INPUT/OUTPUT

PDI0 - PDI7	: PARALLEL I/O DATA
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