

REGISTRATION PENDING  
 Currently Available as FRF150(D, R, H)

Radiation Hardened  
 N-Channel Power MOSFETs

December 1992

### Features

- 25A, 100V, RDS(on) = 0.07 $\Omega$
- Second Generation Rad Hard MOSFET Results From New Design Concepts
- Gamma
  - Meets Pre-Rad Specifications to 100KRAD(Si)
  - Defined End Point Specs at 300KRAD(Si) and 1000KRAD(Si)
  - Performance Permits Limited Use to 3000KRAD(Si)
- Gamma Dot
  - Survives 3E9RAD(Si)/sec at 80% BVDSS Typically
  - Survives 2E12 Typically if Current Limited to IDM
- Photo Current
  - 7.0nA Per-RAD(Si)/sec Typically
- Neutron
  - Pre-RAD Specifications for 3E13 Neutrons/cm<sup>2</sup>
  - Usable to 3E14 Neutrons/cm<sup>2</sup>
- Single Event
  - Typically Survives 1E5Ions/cm<sup>2</sup> Having an LET  $\leq$  35MeV/mg/cm<sup>2</sup> and a Range  $\geq$  30 $\mu$ m at 80% BVDSS

### Description

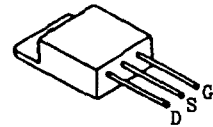
The Harris Semiconductor Sector has designed a series of SECOND GENERATION hardened power MOSFETs of both N and P channel enhancement types with ratings from 100V to 500V, 1A to 60A, and on resistance as low as 25m $\Omega$ . Total dose hardness is offered at 100K RAD(Si) and 1000KRAD(Si) with neutron hardness ranging from 1E13n/cm<sup>2</sup> for 500V product to 1E14n/cm<sup>2</sup> for 100V product. Dose rate hardness (GAMMA DOT) exists for rates to 1E9 without current limiting and 2E12 with current limiting. Heavy ion survival from signal event drain burn-out exists for linear energy transfer (LET) of 35 at 80% of rated voltage.

This MOSFET is an enhancement-mode silicon-gate power field effect transistor of the vertical DMOS (VDMOS) structure. It is specially designed and processed to exhibit minimal characteristic changes to total dose (GAMMA) and neutron (n<sup>o</sup>) exposures. Design and processing efforts are also directed to enhance survival to heavy ion (SEE) and/or dose rate (GAMMA DOT) exposure.

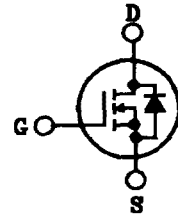
This part may be supplied as a die or in various packages other than shown above. Reliability screening is available as either non TX (commercial), TX equivalent of MIL-S-19500, TXV equivalent of MIL-S-19500, or space equivalent of MIL-S-19500. Contact the Harris Semiconductor High-Reliability Marketing group for any desired deviations from the data sheet.

### Package

TO-254AA



### Symbol



### Absolute Maximum Ratings (TC = +25°C) Unless Otherwise Specified

	2N7292D, R, H	UNITS	
Drain-Source Voltage.....	VDS	100	V
Drain-Gate Voltage (RGS = 20k $\Omega$ ).....	VDGR	100	V
Continuous Drain Current			
TC = +25°C.....	ID	25	A
TC = +100°C.....	ID	20	A
Pulsed Drain Current.....	IDM	75	A
Gate-Source Voltage.....	VGS	$\pm$ 20	V
Maximum Power Dissipation			
TC = +25°C.....	PT	125	W
TC = +100°C.....	PT	50	W
Derated Above +25°C.....		1.00	W/°C
Inductive Current, Clamped, L = 100 $\mu$ H, (See Test Figure).....	ILM	75	A
Continuous Source Current (Body Diode).....	IS	25	A
Pulsed Source Current (Body Diode).....	ISM	75	A
Operating And Storage Temperature.....	TJC, TSTG	-55 to +150	°C
Lead Temperature (During Soldering)			
Distance > 0.063 in. (1.6mm) From Case, 10s Max.....	TL	300	°C

## Specifications 2N7292D, 2N7292R, 2N7292H - Registration Pending

**Pre-Radiation Electrical Specifications** TC = +25°C, Unless Otherwise Specified

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS		UNITS
			MIN	MAX	
Drain-Source Breakdown Volts	BVDSS	VGS = 0, ID = 1mA	100	-	V
Gate-Threshold Volts	VGS(th)	VDS = VGS, ID = 1mA	2.0	4.0	V
Gate-Body Leakage Forward	IGSSF	VGS = +20V	-	100	nA
Gate-Body Leakage Reverse	IGSSR	VGS = -20V	-	100	nA
Zero-Gate Voltage Drain Current	IDSS1 IDSS2 IDSS3	VDS = 100V, VGS = 0	-	1	mA
		VDS = 80V, VGS = 0	-	0.025	
		VDS = 80V, VGS = 0, TC = +125°C	-	0.25	
Rated Avalanche Current	IAR	Time = 20μs	-	75	A
Drain-Source On-State Volts	VDS(on)	VGS = 10V, ID = 25A	-	1.84	V
Drain-Source On Resistance	RDS(on)	VGS = 10V, ID = 20A	-	.07	Ω
Turn-On Delay Time	td(on)	VDD = 50V, ID = 25A Pulse Width = 3μs Period = 300μs, Rg = 25Ω 0 ≤ VGS ≤ 10 (See Test Circuit)	-	134	ns
Rise Time	tr		-	628	
Turn-Off Delay Time	td(off)		-	642	
Fall Time	tf		-	490	
Gate-Charge Threshold	QG(th)	VDD = 50V, ID = 25A IGS1 = IGS2 0 ≤ VGS ≤ 20	4	17	nc
Gate-Charge On State	QG(on)		79	314	
Gate-Charge Total	QGM		138	552	
Plateau Voltage	VGP		2	12	V
Gate-Charge Source	QGS		11	46	nc
Gate-Charge Drain	QGD		40	164	
Diode Forward Voltage	VSD	ID = 25A, VGD = 0	0.6	1.8	V
Reverse Recovery Time	TT	I = 25A; di/dt = 100A/μs	-	1400	ns
Junction-To-Case	Rθjc		-	1.0	°C/W
Junction-To-Ambient	Rθja	Free Air Operation	-	48	

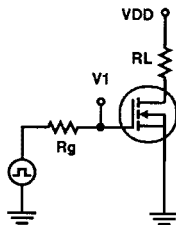


FIGURE 1. SWITCHING TIME TESTING

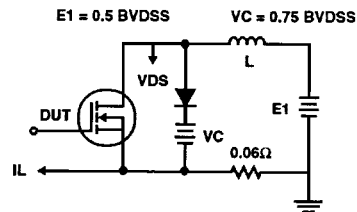


FIGURE 2. CLAMPED INDUCTIVE SWITCHING, ILM

**Specifications 2N7279D, 2N7292R, 2N7292H - Registration Pending**

**Post-Radiation Electrical Specifications** TC = +25°C, Unless Otherwise Specified

PARAMETER	SYMBOL	TYPE	TEST CONDITIONS	LIMITS		UNITS	
				MIN	MAX		
Drain-Source Breakdown Volts	(Note 4, 6)	BVDSS	2N7292D, R	VGS = 0, ID = 1mA	100	-	V
	(Note 5, 6)	BVDSS	2N7292H	VGS = 0, ID = 1mA	95	-	V
Gate-Source Threshold Volts	(Note 4, 6)	VGS(th)	2N7292D, R	VGS = VDS, ID = 1mA	2.0	4.0	V
	(Note 3, 5, 6)	VGS(th)	2N7292H	VGS = VDS, ID = 1mA	1.5	4.5	V
Gate-Body Leakage Forward	(Note 4, 6)	IGSSF	2N7292D, R	VGS = 20V, VDS = 0	-	100	nA
	(Note 5, 6)	IGSSF	2N7292H	VGS = 20V, VDS = 0	-	200	nA
Gate-Body Leakage Reverse	(Note 2, 4, 6)	IGSSR	2N7292D, R	VGS = -20V, VDS = 0	-	100	nA
	(Note 2, 5, 6)	IGSSR	2N7292H	VGS = -20V, VDS = 0	-	200	nA
Zero-Gate Voltage Drain Current	(Note 4, 6)	IDSS	2N7292D, R	VGS = 0, VDS = 80V	-	25	μA
	(Note 5, 6)	IDSS	2N7292H	VGS = 0, VDS = 80V	-	100	μA
Drain-Source On-State Volts	(Note 1, 4, 6)	VDS(on)	2N7292D, R	VGS = 10V, ID = 25A	-	1.84	V
	(Note 1, 5, 6)	VDS(on)	2N7292H	VGS = 16V, ID = 25A	-	2.76	V
Drain-Source On Resistance	(Note 1, 4, 6)	RDS(on)	2N7292D, R	VGS = 10V, ID = 20A	-	0.07	Ω
	(Note 1, 5, 6)	RDS(on)	2N7292H	VGS = 14V, ID = 20A	-	0.105	Ω

**NOTES:**

1. Pulse test, 300μs max
2. Absolute value
3. Gamma = 300KRAD(Si)
4. Gamma = 10KRAD(Si) for "D", 100KRAD(Si) for "R". Neutron = 3E13
5. Gamma = 1000KRAD(Si). Neutron = 3E13
6. Insitu Gamma bias must be sampled for both VGS = +10V, VDS = 0V and VGS = 0V, VDS = 80% BVDSS
7. Gamma data taken 11/16/89 on TA 17651 devices by GE ASTRO SPACE; EMC/SURVIVABILITY LABORATORY; KING OF PRUSSIA, PA 19401
8. Single event drain burnout testing by Titus, J.L., et al of NWSA, Crane, IN at Brookhaven Nat. Lab. Dec 11-14, 1989
9. Neutron derivation, HARRIS Application note AN-8831, Oct. 1988

Typical Performance Characteristics

