

LOW DROP FIXED AND ADJUSTABLE POSITIVE VOLTAGE REGULATOR

The KIA1117BS/BF $\times \times$ Series are a Low Drop Voltage Regulator able to provide up to 1A of output current, available even in adjustable version ($V_{ref}=1.25V$)

FEATURES

- Low Dropout Voltage : 1.1V/Typ. ($I_{out}=1.0A$)
- Very Low Quiescent Current : 5mA/Typ.
- Output Current up to 1A
- Fixed Output Voltage of 1.2V, 1.5V, 1.8V, 2.5V, 2.85V, 3.3V, 5.0V
- Adjustable Version Availability : $V_{ref}=1.25V$
- Internal Current and Thermal Limit
- A Minimum of $10\mu F$ for stability
- Suitable for MLCC, Tantalum and Low ESR Electrolytic Capacitors
- ESR Range for stability : $1m\Omega \sim 200m\Omega$
- Available in $\pm 2\%$ (at $25^\circ C$)
- High Ripple Rejection : 80dB/Typ
- Temperature Range : -40 ~ 150

LINE UP

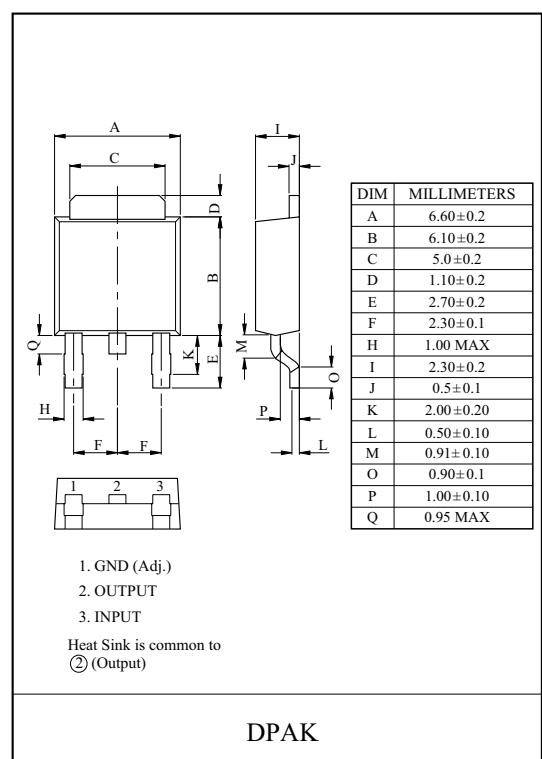
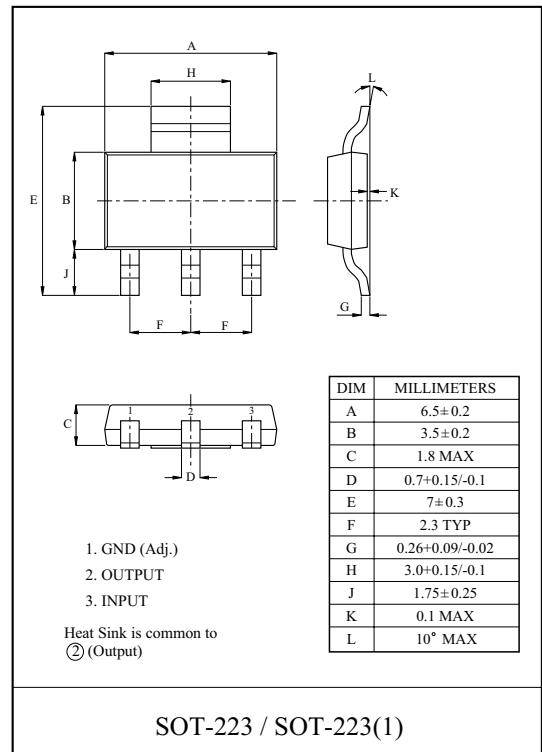
ITEM	OUTPUT VOLTAGE (V)	PACKAGE
KIA1117BS/BF00	Adjustable (1.25~10V)	S : SOT-223 F : DPAK
KIA1117BS/BF12	1.2	
KIA1117BS/BF15	1.5	
KIA1117BS/BF18	1.8	
KIA1117BS/BF25	2.5	
KIA1117BS/BF28	2.85	
KIA1117BS/BF33	3.3	
KIA1117BS/BF50	5.0	

MAXIMUM RATINGS ($T_a=25^\circ C$)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Input Voltage		V_{IN}	12	V
Output Current		I_{OUT}	1.0	A
Power Dissipation 1 (No Heatsink)	S (Note)	P_{D1}	1.0	W
	F		1.3	
Power Dissipation 2 (Infinite Heatsink)	S	P_{D2}	8.3	W
	F		13	
Maximum Junction Temperature		$T_{j(max)}$	150	
Operating Junction Temperature		T_{opr}	-40 ~ 150	
Storage Temperature		T_{stg}	-55 ~ 150	

Note) Package Mounted on FR-4 PCB 36 x 18 x 1.5mm.

: mounting pad for the GND Lead min. 6cm²



KIA1117BS/BF00 ~ KIA1117BS/BF50

ELECTRICAL CHARACTERISTICS

KIA1117BS/BF00 (Unless otherwise specified, Tj=25)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT1}	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, Tj=25	V _{OUT} × 0.98	V _{OUT}	V _{OUT} × 1.02	V
	V _{OUT2}	10mA I _{OUT} 1A, V _{OUT} +1.5V V _{IN} 10V	V _{OUT} × 0.97	V _{OUT}	V _{OUT} × 1.03	
Line Regulation	Reg Line	V _{OUT} +1.5V V _{IN} 10V, I _{OUT} =10mA	-	1	10	mV
Load Regulation	Reg Load	10mA I _{OUT} 1A, V _{IN} =V _{OUT} +2.0V	-	0.5	1	%
Quiescent Current	I _{B1}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =0A	-	5	10	mA
	I _{B2}	V _{IN} =10V, I _{OUT} =0A	-	5	10	
Adjustable Pin Current	I _{ADJ}	V _{IN} =V _{OUT} +1.5V	-	35	-	µA
Minimum Load Current	I _{MIN}	V _{IN} =V _{OUT} +1.5V	10	-	-	mA
Output Noise Voltage	V _{NO}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =40mA, 10Hz f 10kHz	-	100	-	µVrms
Short Circuit Current Limit	I _{SC}	V _{IN} =V _{OUT} +2.0V	1.1	-	-	A
Ripple Rejection	R · R	I _{OUT} =40mA, f=120Hz, Vripple=1Vp-p V _{IN} =V _{OUT} +3V	60	80	-	dB
Dropout Voltage	V _D	I _{OUT} =1A	-	1.1	1.4	V
Temperature Stability	TCV _O	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, T _j =-40~150	-	0.5	-	%

ELECTRICAL CHARACTERISTICS

KIA1117BS/BF12 (Unless otherwise specified, Tj=25)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT1}	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, Tj=25	V _{OUT} × 0.98	V _{OUT}	V _{OUT} × 1.02	V
	V _{OUT2}	10mA I _{OUT} 1A, V _{OUT} +1.5V V _{IN} 10V	V _{OUT} × 0.97	V _{OUT}	V _{OUT} × 1.03	
Line Regulation	Reg Line	V _{OUT} +1.5V V _{IN} 10V, I _{OUT} =10mA	-	1	10	mV
Load Regulation	Reg Load	10mA I _{OUT} 1A, V _{IN} =V _{OUT} +2.0V	-	0.5	1	%
Quiescent Current	I _{B1}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =0A	-	5	10	mA
	I _{B2}	V _{IN} =10V, I _{OUT} =0A	-	5	10	
Output Noise Voltage	V _{NO}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =40mA, 10Hz f 10kHz	-	100	-	µVrms
Short Circuit Current Limit	I _{SC}	V _{IN} =V _{OUT} +2.0V	1.1	-	-	A
Ripple Rejection	R · R	I _{OUT} =40mA, f=120Hz, Vripple=1Vp-p V _{IN} =V _{OUT} +3V	60	80	-	dB
Dropout Voltage	V _D	I _{OUT} =1A	-	1.1	1.4	V
Temperature Stability	TCV _O	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, T _j =-40~150	-	0.5	-	%

KIA1117BS/BF00 ~ KIA1117BS/BF50

ELECTRICAL CHARACTERISTICS

KIA1117BS/BF15 (Unless otherwise specified, Tj=25)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT1}	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, Tj=25	V _{OUT} × 0.98	V _{OUT}	V _{OUT} × 1.02	V
	V _{OUT2}	10mA I _{OUT} 1A, V _{OUT} +1.5V V _{IN} 10V	V _{OUT} × 0.97	V _{OUT}	V _{OUT} × 1.03	
Line Regulation	Reg Line	V _{OUT} +1.5V V _{IN} 10V, I _{OUT} =10mA	-	1	10	mV
Load Regulation	Reg Load	10mA I _{OUT} 1A, V _{IN} =V _{OUT} +2.0V	-	0.5	1	%
Quiescent Current	I _{B1}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =0A	-	5	10	mA
	I _{B2}	V _{IN} =10V, I _{OUT} =0A	-	5	10	
Output Noise Voltage	V _{NO}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =40mA, 10Hz f 10kHz	-	100	-	µVrms
Short Circuit Current Limit	I _{SC}	V _{IN} =V _{OUT} +2.0V	1.1	-	-	A
Ripple Rejection	R · R	I _{OUT} =40mA, f=120Hz, Vripple=1Vp-p V _{IN} =V _{OUT} +3V	60	80	-	dB
Dropout Voltage	V _D	I _{OUT} =1A	-	1.1	1.4	V
Temperature Stability	TCV _O	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, T _j =-40~150	-	0.5	-	%

ELECTRICAL CHARACTERISTICS

KIA1117BS/BF18 (Unless otherwise specified, Tj=25)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT1}	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, Tj=25	V _{OUT} × 0.98	V _{OUT}	V _{OUT} × 1.02	V
	V _{OUT2}	10mA I _{OUT} 1A, V _{OUT} +1.5V V _{IN} 10V	V _{OUT} × 0.97	V _{OUT}	V _{OUT} × 1.03	
Line Regulation	Reg Line	V _{OUT} +1.5V V _{IN} 10V, I _{OUT} =10mA	-	1	10	mV
Load Regulation	Reg Load	10mA I _{OUT} 1A, V _{IN} =V _{OUT} +2.0V	-	0.5	1	%
Quiescent Current	I _{B1}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =0A	-	5	10	mA
	I _{B2}	V _{IN} =10V, I _{OUT} =0A	-	5	10	
Output Noise Voltage	V _{NO}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =40mA, 10Hz f 10kHz	-	100	-	µVrms
Short Circuit Current Limit	I _{SC}	V _{IN} =V _{OUT} +2.0V	1.1	-	-	A
Ripple Rejection	R · R	I _{OUT} =40mA, f=120Hz, Vripple=1Vp-p V _{IN} =V _{OUT} +3V	60	80	-	dB
Dropout Voltage	V _D	I _{OUT} =1A	-	1.1	1.4	V
Temperature Stability	TCV _O	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, T _j =-40~150	-	0.5	-	%

KIA1117BS/BF00 ~ KIA1117BS/BF50

ELECTRICAL CHARACTERISTICS

KIA1117BS/BF25 (Unless otherwise specified, Tj=25)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT1}	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, T _j =25	V _{OUT} × 0.98	V _{OUT}	V _{OUT} × 1.02	V
	V _{OUT2}	10mA I _{OUT} 1A, V _{OUT} +1.5V V _{IN} 10V	V _{OUT} × 0.97	V _{OUT}	V _{OUT} × 1.03	
Line Regulation	Reg Line	V _{OUT} +1.5V V _{IN} 10V, I _{OUT} =10mA	-	1	10	mV
Load Regulation	Reg Load	10mA I _{OUT} 1A, V _{IN} =V _{OUT} +2.0V	-	0.5	1	%
Quiescent Current	I _{B1}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =0A	-	5	10	mA
	I _{B2}	V _{IN} =10V, I _{OUT} =0A	-	5	10	
Output Noise Voltage	V _{NO}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =40mA, 10Hz f 10kHz	-	100	-	µVrms
Short Circuit Current Limit	I _{SC}	V _{IN} =V _{OUT} +2.0V	1.1	-	-	A
Ripple Rejection	R · R	I _{OUT} =40mA, f=120Hz, Vripple=1Vp-p V _{IN} =V _{OUT} +3V	60	80	-	dB
Dropout Voltage	V _D	I _{OUT} =1A	-	1.1	1.4	V
Temperature Stability	TCV _O	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, T _j =-40~150	-	0.5	-	%

ELECTRICAL CHARACTERISTICS

KIA1117BS/BF28 (Unless otherwise specified, Tj=25)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT1}	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, T _j =25	V _{OUT} × 0.98	V _{OUT}	V _{OUT} × 1.02	V
	V _{OUT2}	10mA I _{OUT} 1A, V _{OUT} +1.5V V _{IN} 10V	V _{OUT} × 0.97	V _{OUT}	V _{OUT} × 1.03	
Line Regulation	Reg Line	V _{OUT} +1.5V V _{IN} 10V, I _{OUT} =10mA	-	1	10	mV
Load Regulation	Reg Load	10mA I _{OUT} 1A, V _{IN} =V _{OUT} +2.0V	-	0.5	1	%
Quiescent Current	I _{B1}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =0A	-	5	10	mA
	I _{B2}	V _{IN} =10V, I _{OUT} =0A	-	5	10	
Output Noise Voltage	V _{NO}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =40mA, 10Hz f 10kHz	-	100	-	µVrms
Short Circuit Current Limit	I _{SC}	V _{IN} =V _{OUT} +2.0V	1.1	-	-	A
Ripple Rejection	R · R	I _{OUT} =40mA, f=120Hz, Vripple=1Vp-p V _{IN} =V _{OUT} +3V	60	80	-	dB
Dropout Voltage	V _D	I _{OUT} =1A	-	1.1	1.4	V
Temperature Stability	TCV _O	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, T _j =-40~150	-	0.5	-	%

KIA1117BS/BF00 ~ KIA1117BS/BF50

ELECTRICAL CHARACTERISTICS

KIA1117BS/BF33 (Unless otherwise specified, Tj=25)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT1}	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, T _j =25	V _{OUT} × 0.98	V _{OUT}	V _{OUT} × 1.02	V
	V _{OUT2}	10mA I _{OUT} 1A, V _{OUT} +1.5V V _{IN} 10V	V _{OUT} × 0.97	V _{OUT}	V _{OUT} × 1.03	
Line Regulation	Reg Line	V _{OUT} +1.5V V _{IN} 10V, I _{OUT} =10mA	-	1	10	mV
Load Regulation	Reg Load	10mA I _{OUT} 1A, V _{IN} =V _{OUT} +2.0V	-	0.5	1	%
Quiescent Current	I _{B1}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =0A	-	5	10	mA
	I _{B2}	V _{IN} =10V, I _{OUT} =0A	-	5	10	
Output Noise Voltage	V _{NO}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =40mA, 10Hz f 10kHz	-	100	-	µVrms
Short Circuit Current Limit	I _{SC}	V _{IN} =V _{OUT} +2.0V	1.1	-	-	A
Ripple Rejection	R · R	I _{OUT} =40mA, f=120Hz, Vripple=1Vp-p V _{IN} =V _{OUT} +3V	60	80	-	dB
Dropout Voltage	V _D	I _{OUT} =1A	-	1.1	1.4	V
Temperature Stability	TCV _O	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, T _j =-40~150	-	0.5	-	%

ELECTRICAL CHARACTERISTICS

KIA1117BS/BF50 (Unless otherwise specified, Tj=25)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT1}	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, T _j =25	V _{OUT} × 0.98	V _{OUT}	V _{OUT} × 1.02	V
	V _{OUT2}	10mA I _{OUT} 1A, V _{OUT} +1.5V V _{IN} 10V	V _{OUT} × 0.97	V _{OUT}	V _{OUT} × 1.03	
Line Regulation	Reg Line	V _{OUT} +1.5V V _{IN} 10V, I _{OUT} =10mA	-	1	10	mV
Load Regulation	Reg Load	10mA I _{OUT} 1A, V _{IN} =V _{OUT} +2.0V	-	0.5	1	%
Quiescent Current	I _{B1}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =0A	-	5	10	mA
	I _{B2}	V _{IN} =10V, I _{OUT} =0A	-	5	10	
Output Noise Voltage	V _{NO}	V _{IN} =V _{OUT} +1.25V, I _{OUT} =40mA, 10Hz f 10kHz	-	100	-	µVrms
Short Circuit Current Limit	I _{SC}	V _{IN} =V _{OUT} +2.0V	1.1	-	-	A
Ripple Rejection	R · R	I _{OUT} =40mA, f=120Hz, Vripple=1Vp-p V _{IN} =V _{OUT} +3V	60	80	-	dB
Dropout Voltage	V _D	I _{OUT} =1A	-	1.1	1.4	V
Temperature Stability	TCV _O	V _{IN} =V _{OUT} +1.5V, I _{OUT} =10mA, T _j =-40~150	-	0.5	-	%

KIA1117BS/BF00 ~ KIA1117BS/BF50

Fig.1 Application Circuit-1 (Fixed-Type)

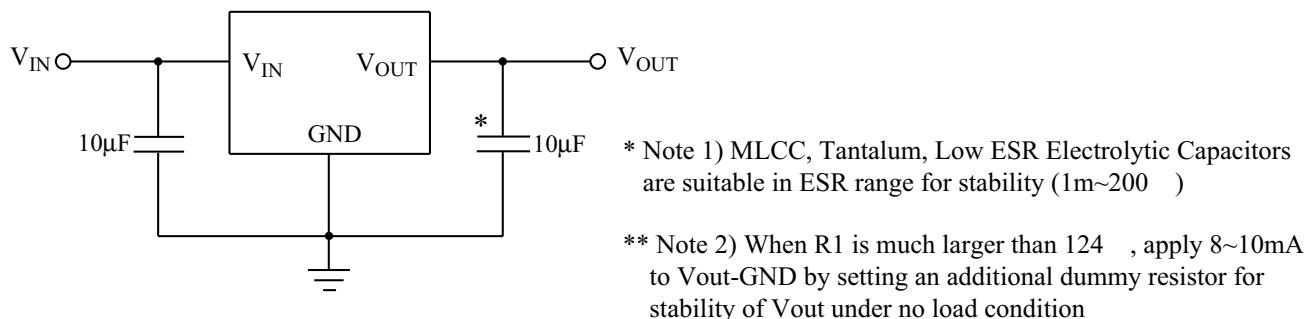
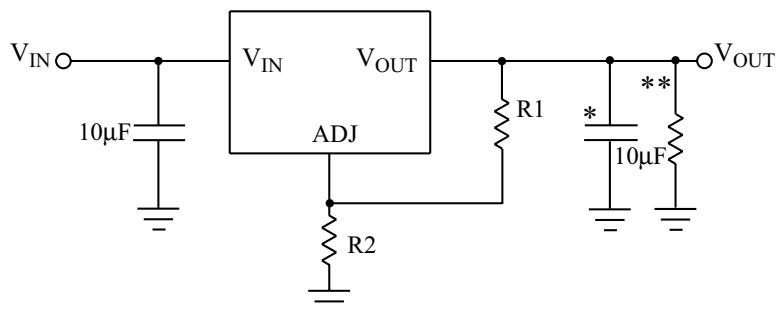


Fig.2 Application Circuit-2 (Adjustable-Type)



$$V_{OUT} = V_{REF} (1 + R2/R1) + I_{ADJ} \cdot R2$$

KIA1117BS/BF00 ~ KIA1117BS/BF50

Fig. 3 V_D - I_{OUT}

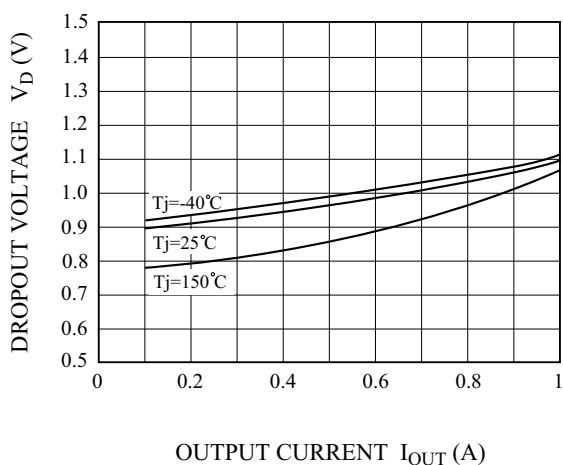


Fig. 4 $V_{OUT}(\text{CHANGE})$ - T_j

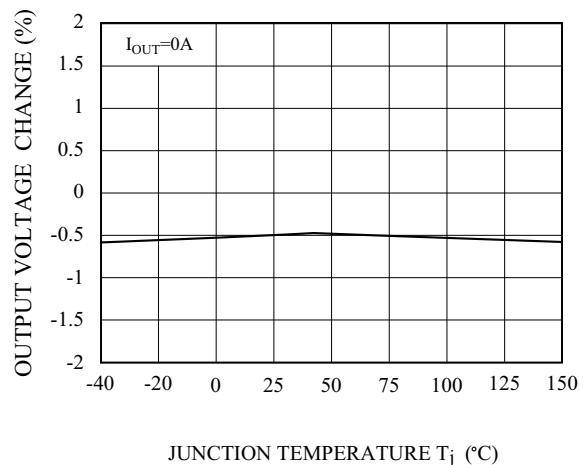


Fig. 5 LINE REGULATION

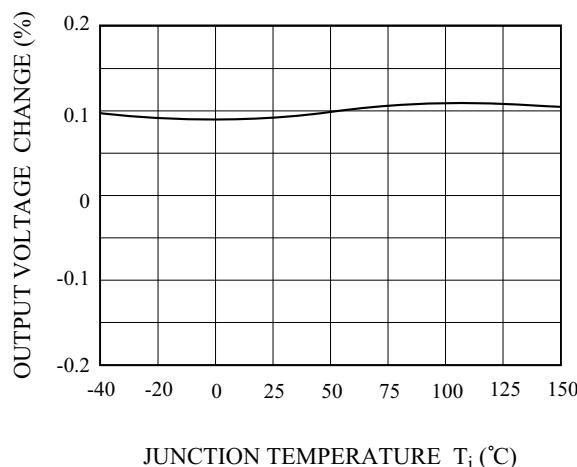


Fig. 6 LOAD REGULATION

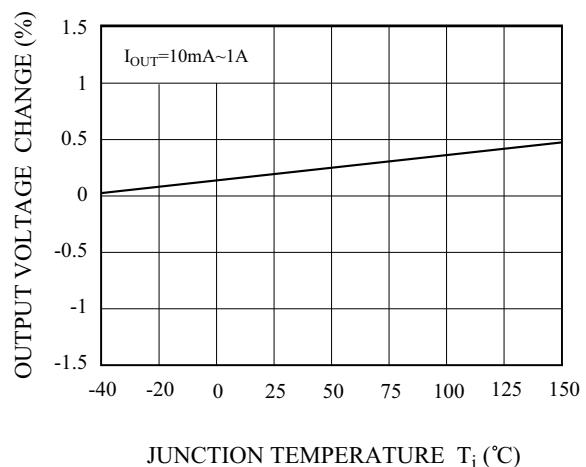


Fig. 7 I_Q - T_j

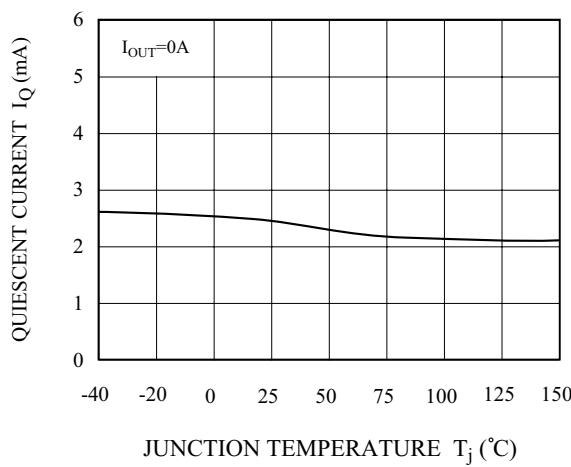
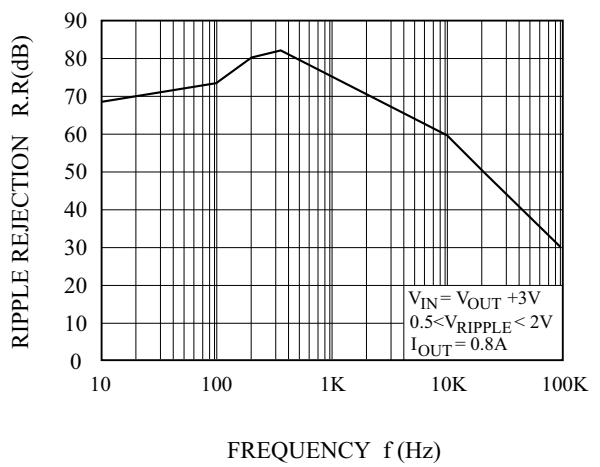


Fig. 8 R.R - f



KIA1117BS/BF00 ~ KIA1117BS/BF50

Fig.9 P_D - T_a (S-Type : SOT-223)

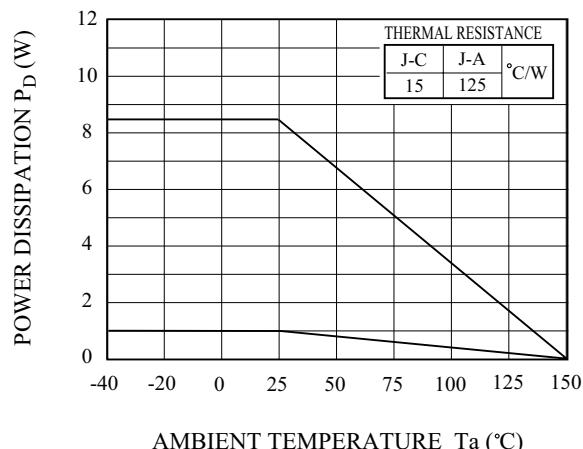


Fig.10 P_D - T_a (F-Type : DPAK)

