

Low-Power CMOS Ionization Smoke Detector IC

The MC14467-1, when used with an ionization chamber and a small number of external components, will detect smoke. When smoke is sensed, an alarm is sounded via an external piezoelectric transducer and internal drivers. This circuit is designed to operate in smoke detector systems that comply with UL217 and UL268 specifications.

Features

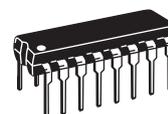
- Ionization Type with On-Chip FET Input Comparator
- Piezoelectric Horn Driver
- Guard Outputs on Both Sides of Detect Input
- Input-Production Diodes on the Detect Input
- Low-Battery Trip Point, Internally Set, can be Altered Via External Resistor
- Detect Threshold, Internally Set, can be Altered Via External Resistor
- Pulse Testing for Low Battery Uses LED for Battery Loading
- Comparator Outputs for Detect and Low Battery
- Internal Reverse Battery Protection
- Pb-Free Packaging Designated by Suffix Code ED and EG

ORDERING INFORMATION

Device	Case No.	Package
MC14467P1	648-08	Plastic Dip
MC14467ED1		
MCZ14467EG/R2	751G-04	SOICW

MC14467-1

LOW-POWER CMOS IONIZATION SMOKE DETECTOR IC



**P SUFFIX
 ED SUFFIX
 (PB-FREE)
 16-LEAD
 PLASTIC DIP
 CASE 648-08**



**EG SUFFIX
 (PB-FREE)
 16-LEAD
 SOICW
 CASE 751G-04**

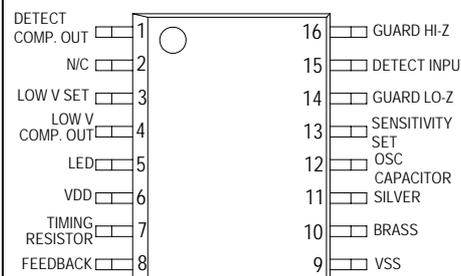
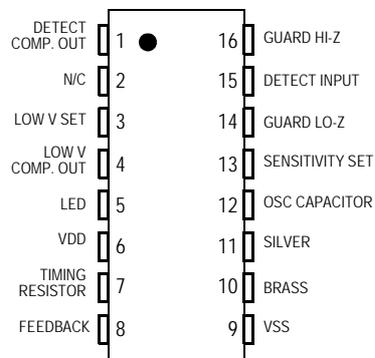


Figure 1. Pin Connections

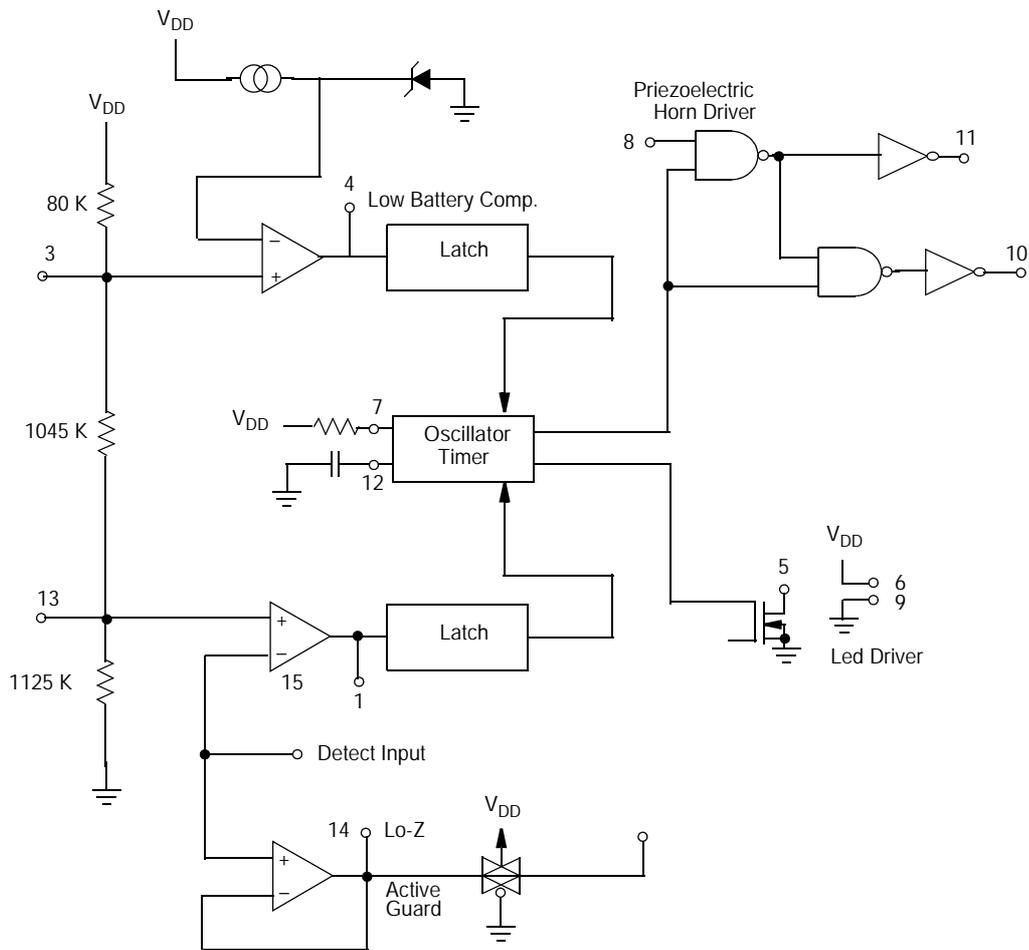


Figure 2. Block Diagram

Table 1. Maximum Ratings⁽¹⁾
(Voltages referenced to V_{SS})

Rating	Symbol	Value	Unit
DC Supply Voltage	V_{DD}	-0.5 to +15	V
Input Voltage, All Inputs Except Pin 8	V_{IN}	-0.25 to $V_{DD} + 0.25$	V
DC Current Drain per Input Pin, Except Pin 15 = 1 mA	I	10	mA
DC Current Drain per Output Pin	I	30	mA
Operating Temperature Range	T_A	-10 to +60	°C
Storage Temperature Range	T_{STG}	-55 to +125	°C
Reverse Battery Time	t_{RB}	5.0	s

1. Maximum Ratings are those values beyond which damage to the device may occur. This device contains circuitry to protect the inputs against damage due to high static voltages or electric fields; however, it is advised that normal precautions be taken to avoid application of any voltage higher than maximum rated voltages to this high impedance circuit. For proper operation it is recommended that except for pin 8, V_{IN} and V_{OUT} be constrained to the range $V_{SS} \leq (V_{IN} \text{ or } V_{OUT}) \leq V_{DD}$. For pin 8, refer to the Electrical Characteristics.

Table 2. Recommended Operating Conditions
(Voltages referenced to V_{SS})

Parameter	Symbol	Value	Unit
Supply Voltage	V_{DD}	9.0	V
Timing Capacitor	—	0.1	μF
Timing Resistor	—	8.2	$\text{M}\Omega$
Battery Load (Resistor or LED)	—	10	mA

Table 3. Electrical Characteristics
(Voltages referenced to V_{SS} , $T_A = 25^\circ\text{C}$)

Characteristic	Symbol	V_{DD} V_{DC}	Min	Typ ⁽¹⁾	Max	Unit
Operating Voltage	V_{DD}	—	6.0	—	12	V
Output Voltage Piezoelectric Horn Drivers ($I_{OH} = -16\text{ mA}$) Comparators ($I_{OH} = -30\ \mu\text{A}$)	V_{OH}	7.2 9.0	6.3 8.5	— 8.8	— —	V
Output Voltage Piezoelectric Horn Drivers ($I_{OL} = +16\text{ mA}$) Comparators ($I_{OL} = +30\ \mu\text{A}$)	V_{OL}	7.2 9.0	— —	— 0.1	0.9 0.5	V
Output Voltage - LED Driver, $I_{OL} = 10\text{ mA}$	V_{OL}	7.2	—	—	3.0	V
Output Impedance, Active Guard Pin 14 Pin 16	LO-Z HI-Z	9.0 9.0	— —	— —	10 1000	$\text{k}\Omega$
Operating Current ($R_{bias} = 8.2\ \text{M}\Omega$)	I_{DD}	9.0 12.0	— —	5.0 —	9.0 12.0	μA
Input Current - Detect (40% R.H.)	I_{IN}	9.0	—	—	± 1.0	pA
Internal Set Voltage Low Battery Sensitivity	V_{LOW} V_{SET}	9.0 —	7.2 47	— 50	7.8 53	V $\%V_{DD}$
Hysteresis	V_{HYS}	9.0	75	100	150	mV
Offset Voltage (measured at $V_{in} = V_{DD}/2$) Active Guard Detect Comparator	V_{OS}	9.0 9.0	— —	— —	± 100 ± 50	mV
Input Voltage Range, Pin 8	V_{IN}	—	$V_{SS} - 10$	—	$V_{DD} + 10$	V
Input Capacitance	C_{IN}	—	—	5.0	—	pF
Common Mode Voltage Range, Pin 15	V_{CM}	—	0.6	—	$V_{DD} - 2$	V

1. Data labelled "Typ" is not to be used for design purposes but is intended as an indication of the IC's potential performance.

Table 4. Timing Parameters

(C = 0.1 μ F, R_{bias} = 8.2 M Ω , V_{DD} = 9.0 V, T_A = 25°C, See Figure 7)

Characteristics		Symbol	Min	Typ ⁽¹⁾	Max	Units
Oscillator Period	No Smoke	t_{CI}	1.34	1.67	2.0	s
	Smoke		32	40	48	ms
Oscillator Rise Time		t_R	8.0	10	12	ms
Horn Output (During Smoke)	On Time	PW _{ON}	120	160	208	ms
	Off Time	PW _{OFF}	60	80	104	ms
LED Output	Between Pulses	t_{LED}	32	40	48	s
	On Time		PW _{ON}	8.0	10	12
Horn Output (During Low Battery)	On Time	t_{ON}	8.0	10	12	ms
	Between Pulses		t_{OFF}	32	40	48

1. Data labelled "Typ" is not to be used for design purposes but is intended as an indication of the IC's potential performance.

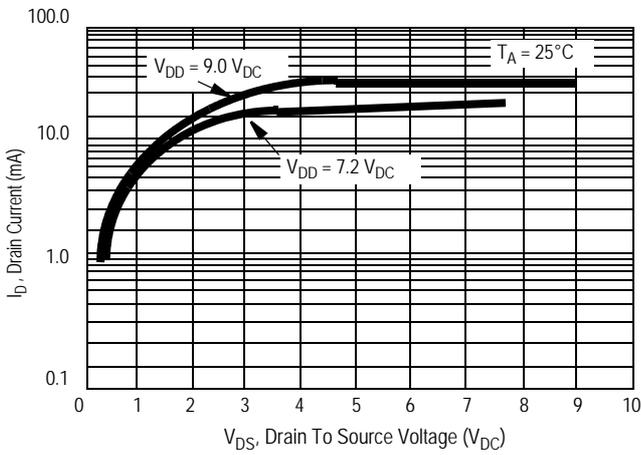


Figure 3. Typical LED Output I-V Characteristic

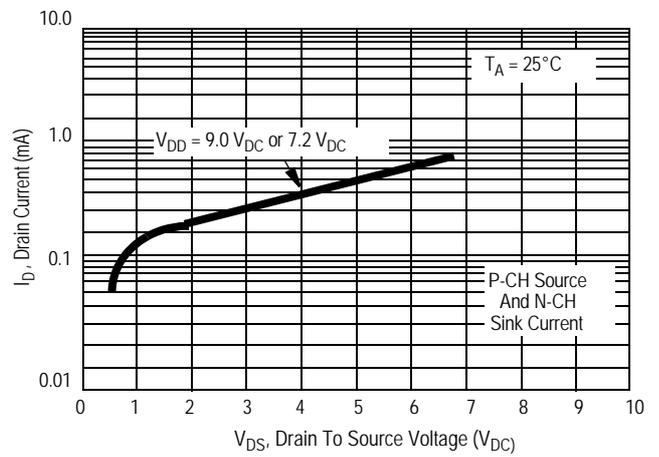


Figure 4. Typical Comparator Output I-V Characteristic

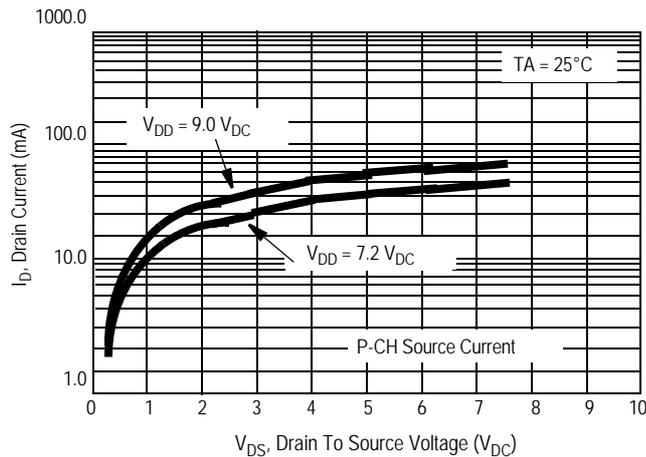
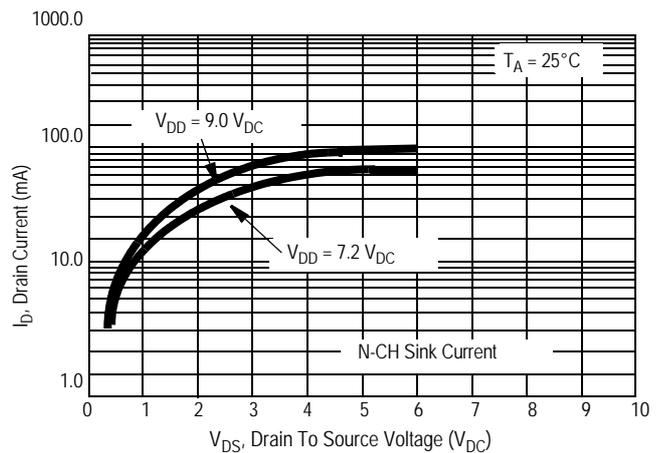


Figure 5. Typical P Horn Driver Output I-V Characteristic



DEVICE OPERATION

Timing

The internal oscillator of the MC14467-1 operates with a period of 1.67 seconds during no-smoke conditions. Each 1.67 seconds, internal power is applied to the entire IC and a check is made for smoke, except during LED pulse, Low Battery Alarm Chirp, or Horn Modulation (in smoke). Every 24 clock cycles a check is made for low battery by comparing V_{DD} to an internal zener voltage. Since very small currents are used in the oscillator, the oscillator capacitor should be of a low leakage type.

Detect Circuitry

If smoke is detected, the oscillator period becomes 40 ms and the piezoelectric horn oscillator circuit is enabled. The horn output is modulated 160 ms on, 80 ms off. During the off time, smoke is again checked and will inhibit further horn output if no smoke is sensed. During smoke conditions the low battery alarm is inhibited, but the LED pulses at a 1.0 Hz rate.

An active guard is provided on both pins adjacent to the detect input. The voltage at these pins will be within 100 mV of the input signal. This will keep surface leakage currents to a minimum and provide a method of measuring the input voltage without loading the ionization chamber. The active guard op amp is not power strobed and thus gives constant protection from surface leakage currents. Pin 15 (the Detect input) has internal diode protection against static damage.

Sensitivity/Low Battery Thresholds

Both the sensitivity threshold and the low battery voltage levels are set internally by a common voltage divider (please

see Figure 2) connected between V_{DD} and V_{SS} . These voltages can be altered by external resistors connected from pins 3 or 13 to either V_{DD} or V_{SS} . There will be a slight interaction here due to the common voltage divider network. The sensitivity threshold can also be be set by adjusting the smoke chamber ionization source.

Test Mode

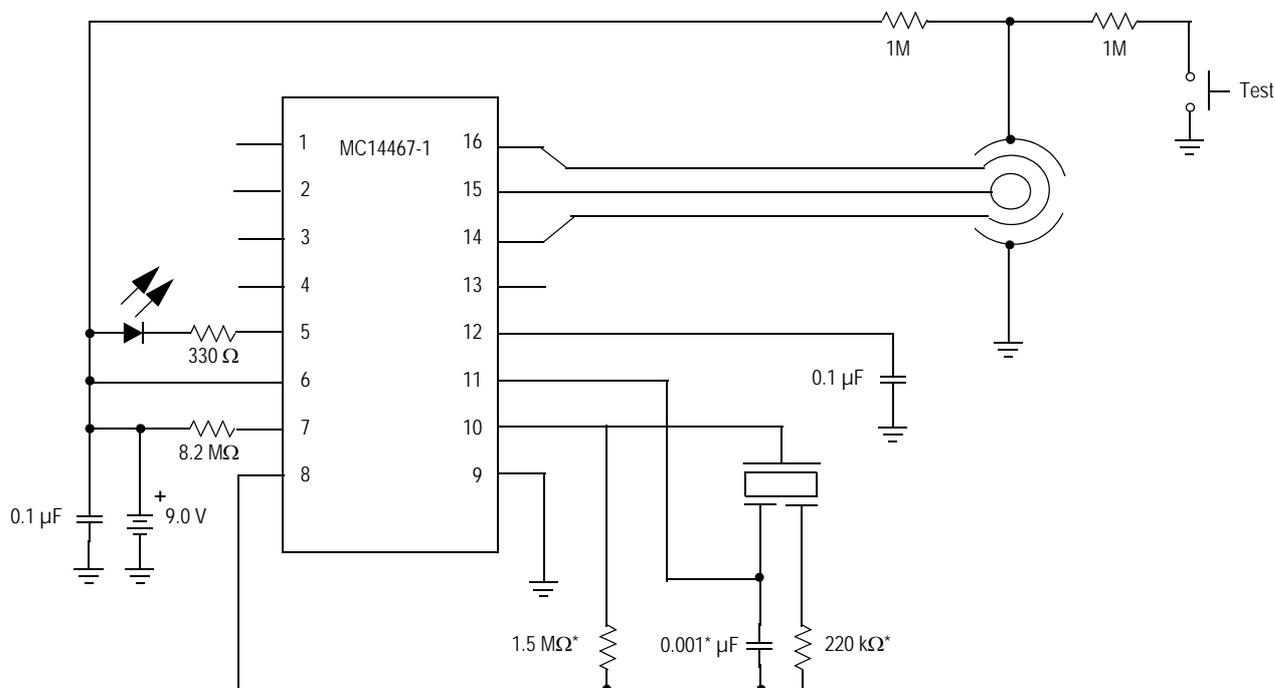
Since the internal op amps and comparators are power strobed, adjustments for sensitivity or low battery level could be difficult and/or time-consuming. By forcing Pin 12 to V_{SS} , the power strobing is bypassed and the outputs, Pins 1 and 4, constantly show smoke/no smoke and good battery/low battery, respectively. Pin 1 = V_{DD} for smoke and Pin 4 = V_{DD} for low battery. In this mode and during the 10 ms power strobe, chip current rises to approximately 50 μA .

LED Pulse

The 9-volt battery level is checked every 40 seconds during the LED pulse. The battery is loaded via a 10 mA pulse for 10 ms. If the LED is not used, it should be replaced with an equivalent resistor such that the battery loading remains at 10 mA.

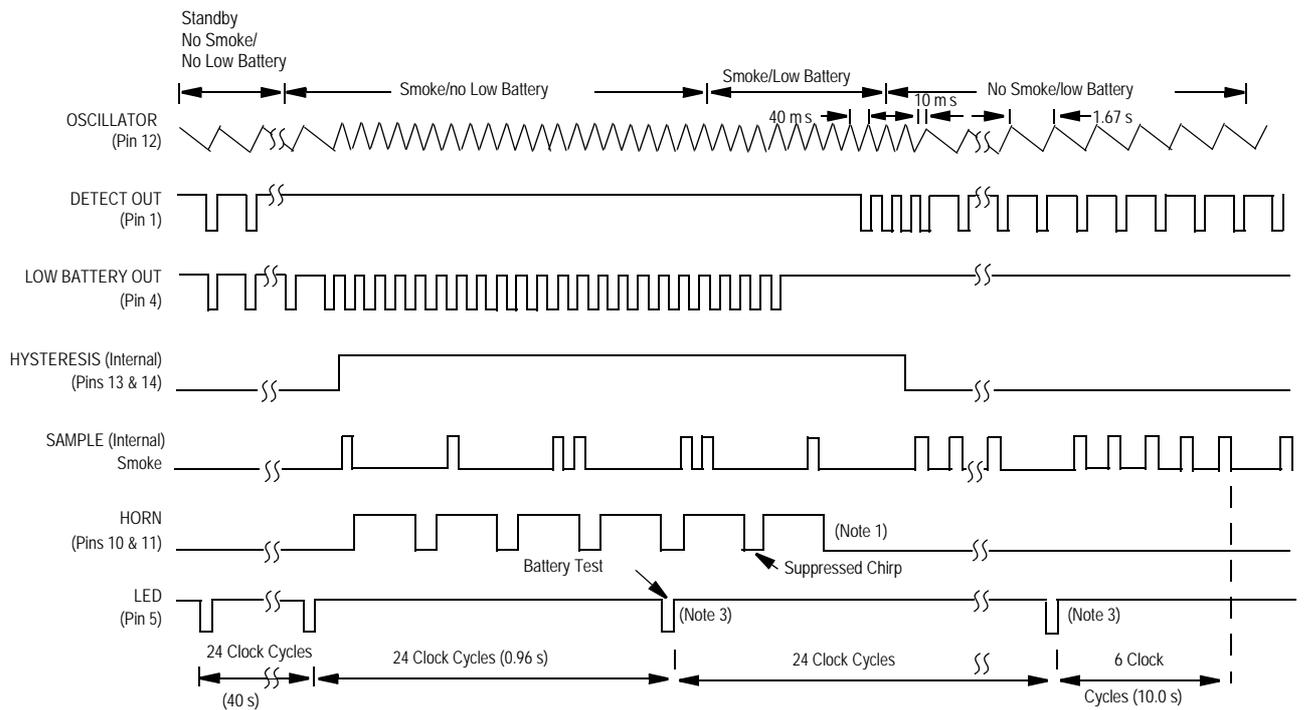
Hysteresis

When smoke is detected, the resistor/divider network that sets sensitivity is altered to increase sensitivity. This yields approximately 100 mV of hysteresis and reduces false triggering.



*NOTE: Component values may change depending on type of piezoelectric horn used.

Figure 6. Typical Application as Ionization Smoke Detector

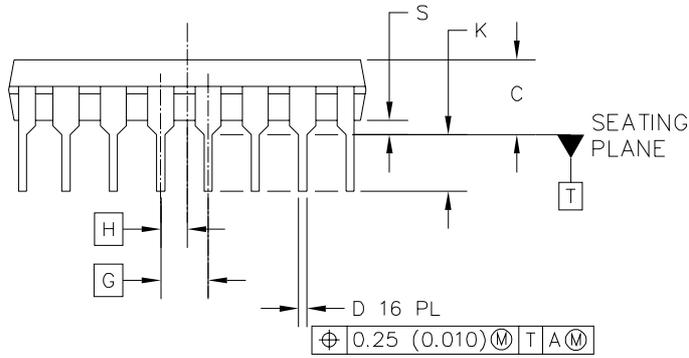
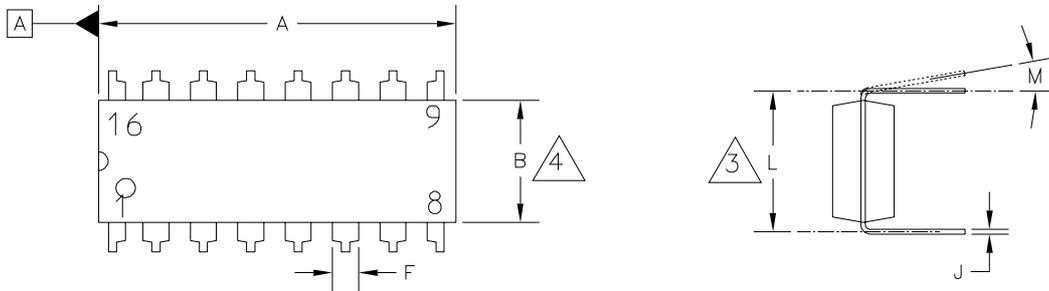


Notes:

1. Horn modulation is self-completing. When going from smoke to no smoke, the alarm condition will terminate only when horn is off.
2. Comparators are strobed on once per clock cycle (1.67 s for no smoke, 40 ms for smoke).
3. Low battery comparator information is latched only during LED pulse.
4. ~ 100 mVp-p swing.

Figure 7. Timing Diagram

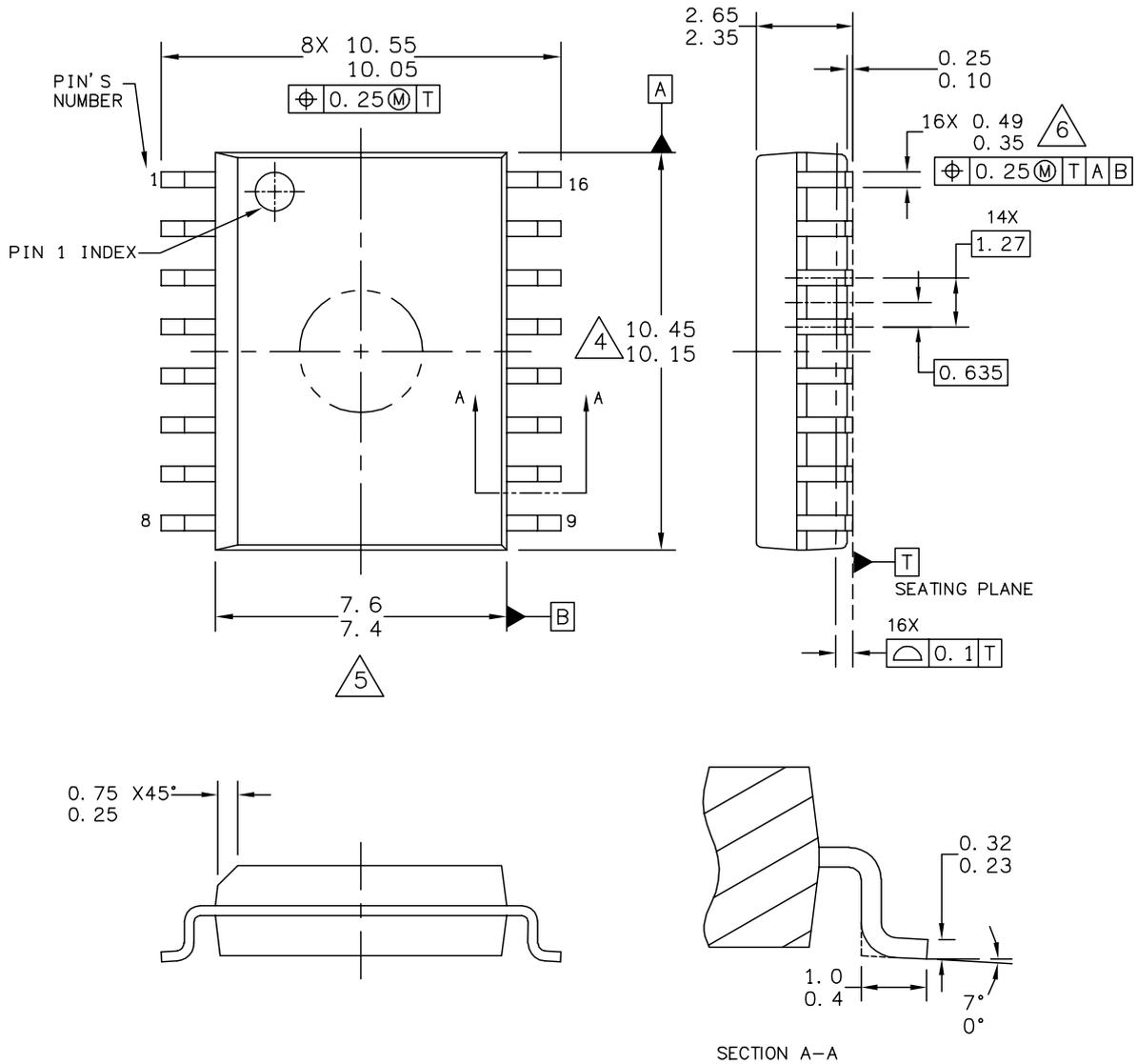
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	CASE NUMBER: 648-08	19 MAY 2005
	STANDARD: NON-JEDEC	

CASE 648-08
ISSUE T
16-LEAD PLASTIC DIP

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	CASE NUMBER: 751G-04	02 JUN 2005
	STANDARD: JEDEC MS-013AA	

CASE 751G-04
ISSUE F
16-LEAD SOICW

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Japan:

Freescale Semiconductor Japan Ltd.
Headquarters
ARCO Tower 15F
1-8-1, Shimo-Meguro, Meguro-ku,
Tokyo 153-0064
Japan
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support.japan@freescale.com

Asia/Pacific:

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