

# DCR1674SA

FDS5647-2.1 January 2004

## **Phase Control Thyristor**

Replaces issue August 2002 version, FDS5647-1.0

#### **FEATURES**

- Double Side Cooling
- High Surge Capability
- High Mean Current
- Fatigue Free

#### **APPLICATIONS**

- High Power Drives
- High Voltage Power Supplies
- DC Motor Control

#### **VOLTAGE RATINGS**

Type Number	Repetitive Peak Voltages V <sub>DRM</sub> V <sub>RRM</sub> V	Conditions
DCR1674SA42	4200	$T_{vi} = 0^{\circ} \text{ to } 125^{\circ}\text{C},$
DCR1674SA41	4100	$I_{\text{DRM}}^{\text{v}} = I_{\text{RRM}} = 500 \text{mA},$
DCR1674SA40	4000	$V_{\text{DRM}}$ , $V_{\text{RBM}}$ $t_{p}$ = 10ms,
DCR1674SA39	3900	$V_{\text{DSM}}^{\text{DIM}} \& V_{\text{RSM}}^{\text{HIM}} =$
DCR1674SA38	3800	$V_{\text{DRM}}^{\text{NOM}} \& V_{\text{RRM}}^{\text{NOM}} + 100V$
		respectively

Lower voltage grades available.

#### **ORDERING INFORMATION**

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

#### DCR1674SA42

Note: Please use the complete part number when ordering and quote this number in any future correspondance relating to your order. **KEY PARAMETERS** 

$\mathbf{V}_{DRM}$	4200V
I <sub>t(AV)</sub>	3940A
I <sub>tsm</sub>	67000A
dVdt*	<b>1000V/μs</b>
dl/dt	<b>200Α/</b> μs

\*Higher dV/dt selections available

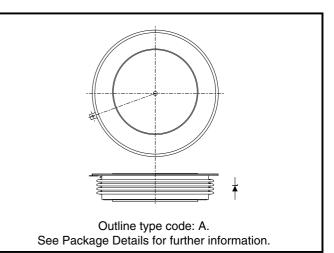


Fig. 1 Package outline



## **CURRENT RATINGS**

 $T_{case} = 60^{\circ}C$  unless stated otherwise.

Symbol	Parameter	Conditions	Max.	Units			
Double Sid	Double Side Cooled						
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	3940	А			
I <sub>T(RMS)</sub>	RMS value	-	6190	А			
Ι <sub>τ</sub>	Continuous (direct) on-state current	-	5740	Α			
Single Side	Single Side Cooled (Anode side)						
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	2590	Α			
I <sub>T(RMS)</sub>	RMS value	-	4070	Α			
Ι <sub>τ</sub>	Continuous (direct) on-state current	-	3490	А			

#### **CURRENT RATINGS**

T<sub>case</sub> = 80°C unless stated otherwise.

Symbol	Parameter	Conditions	Max.	Units			
Double Sid	Double Side Cooled						
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	3110	А			
I <sub>T(RMS)</sub>	RMS value	-	4880	А			
Ι <sub>τ</sub>	Continuous (direct) on-state current	-	4430	Α			
Single Side	Single Side Cooled (Anode side)						
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	2010	А			
I <sub>T(RMS)</sub>	RMS value	-	3150	Α			
Ι <sub>τ</sub>	Continuous (direct) on-state current	-	2630	Α			



#### SURGE RATINGS

Symbol	Parameter	Conditions		Units
I <sub>TSM</sub>	Surge (non-repetitive) on-state current	10ms half sine; $T_{case} = 125^{\circ}C$		kA
l²t	I <sup>2</sup> t for fusing	$V_{_{ m R}} = 50\% V_{_{ m RRM}} - 1/4 \text{ sine}$	14.0 x 10 <sup>6</sup>	A <sup>2</sup> s
I <sub>TSM</sub>	Surge (non-repetitive) on-state current	10ms half sine; T <sub>case</sub> = 125°C	67.0	kA
l²t	I <sup>2</sup> t for fusing	V <sub>R</sub> = 0	22.4 x 10 <sup>6</sup>	A <sup>2</sup> s

## THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions		Min.	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance - junction to case	Double side cooled	dc	-	0.0065	°C/W
		Single side cooled	Anode dc	-	0.013	°C/W
			Cathode dc	-	0.013	°C/W
	Thermal resistance - case to heatsink	Clamping force 83.0kN with mounting compound	Double side	-	0.001	°C/W
R <sub>th(c-h)</sub>			Single side	-	0.002	°C/W
	Virtual junction temperature	On-state (conducting)		-	135	°C
T <sub>vj</sub>		Reverse (blocking)		-	125	°C
T <sub>stg</sub>	Storage temperature range			-55	125	°C
-	Clamping force			74.0	91.0	kN



#### **DYNAMIC CHARACTERISTICS**

Symbol	Parameter	Conditions		Тур.	Max.	Units
I <sub>RRM</sub> /I <sub>DRM</sub>	Peak reverse and off-state current	At V <sub>RRM</sub> /V <sub>DRM</sub> , T <sub>case</sub> = 125°C		-	500	mA
dV/dt	Maximum linear rate of rise of off-state voltage	To 67% $V_{DRM} T_j = 125^{\circ}C$ , gate	open circuit.	-	1000	V/µs
		Gate source 2A	Repetitive 50Hz	-	200	A/μs
dl/dt	Rate of rise of on-state current		Non-repetitive	-	400	A/μs
V <sub>T(TO)</sub>	Threshold voltage	At $T_{v_j} = 125^{\circ}C$		-	0.95	v
r <sub>T</sub>	On-state slope resistance	At $T_{v_j} = 125^{\circ}C$		-	0.138	mΩ
t <sub>gd</sub>	Delay time	$V_{_{D}}$ = 67% $V_{_{DRM}}$ , Gate source 20V, 10 $\Omega$ t <sub>r</sub> = 0.5µs, T <sub>j</sub> = 25°C		-	2.5	μs
t <sub>q</sub>	Turn-off time	$ \begin{array}{l} I_{_{T}} = 5000 \text{A},  t_{_{P}} = 3 \text{ms},  T_{_{I}} = 125 ^{\circ} \text{C}, \\ V_{_{R}} = 200 \text{V},  \text{dI}_{_{RR}} / \text{dt} = -5 \text{A} / \mu \text{s}, \\ V_{_{DR}} = 67 ^{\circ} \text{V}_{_{DRM}},  \text{dV}_{_{DR}} / \text{dt} = 20 \text{V} / \mu \text{s}  \text{linear} \end{array} $		-	900	μs
I <sub>L</sub>	Latching current	$T_{j} = 25^{\circ}C, V_{D} = 5V$		-	650	mA
I <sub>H</sub>	Holding current	$T_j = 25^{\circ}C, R_{g,k} = \infty$		-	220	mA

## GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Conditions		Units
V <sub>GT</sub>	Gate trigger voltage	$V_{\text{DRM}} = 5V, T_{\text{case}} = 25^{\circ}\text{C}$	3.5	V
I <sub>GT</sub>	Gate trigger current	$V_{\text{DRM}} = 5V, T_{\text{case}} = 25^{\circ}\text{C}$	500	mA
V <sub>GD</sub>	Gate non-trigger voltage	At $V_{DRM} T_{case} = 125^{\circ}C$	0.25	V
V <sub>FGM</sub>	Peak forward gate voltage	Anode positive with respect to cathode	30	V
V <sub>FGN</sub>	Peak forward gate voltage	Anode negative with respect to cathode	0.25	V
V <sub>RGM</sub>	Peak reverse gate voltage		5	V
I <sub>FGM</sub>	Peak forward gate current	Anode positive with respect to cathode	30	А
P <sub>GM</sub>	Peak gate power	See table, gate characteristics curve	150	w
P <sub>G(AV)</sub>	Mean gate power		10	w



**CURVES** 

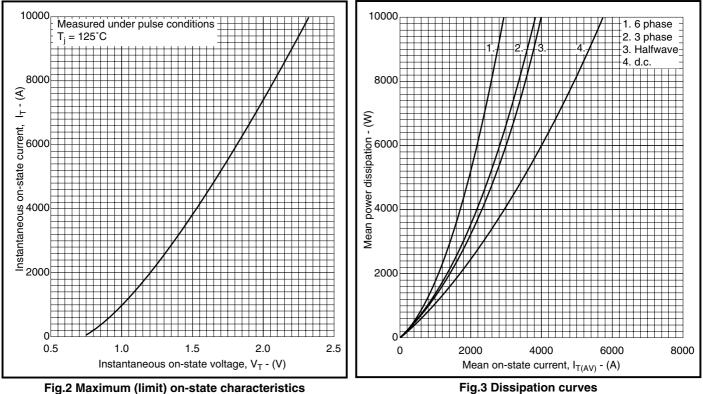


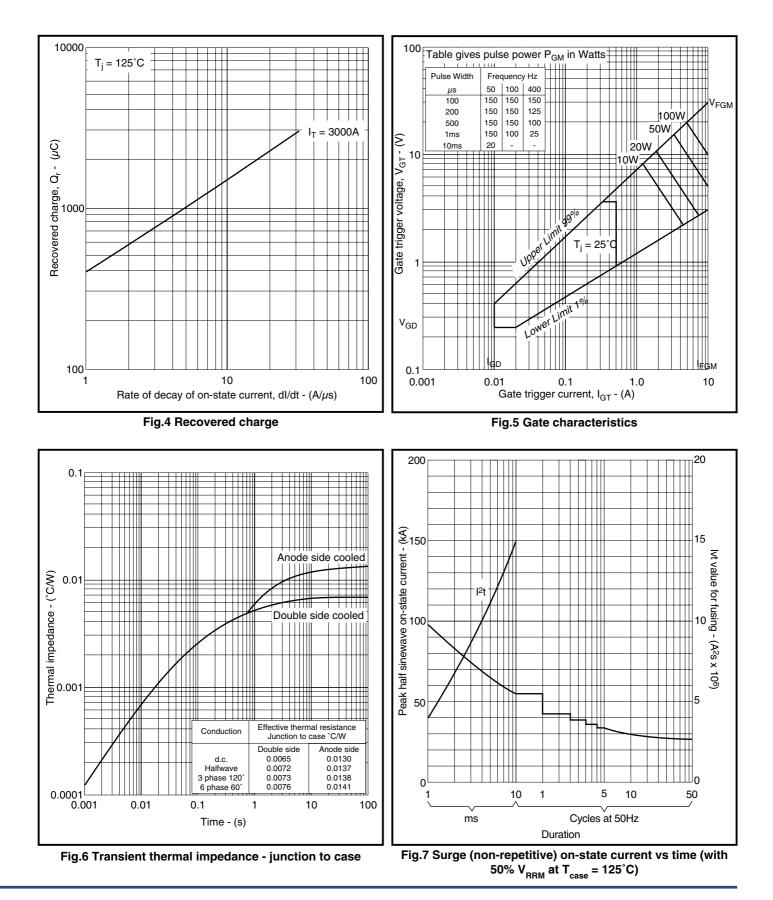
Fig.2 Maximum (limit) on-state characteristics

 $V_{TM}$  Equation:-

 $V_{TM} = A + Bln (I_T) + C.I_T + D.\sqrt{I_T}$ 

Where A = 0.6844942B = -0.0108645C = 7.203702 x 10<sup>-5</sup> D = 0.01015201 these values are valid for  $T_i = 125^{\circ}C$  for  $I_T 500A$  to 10000A

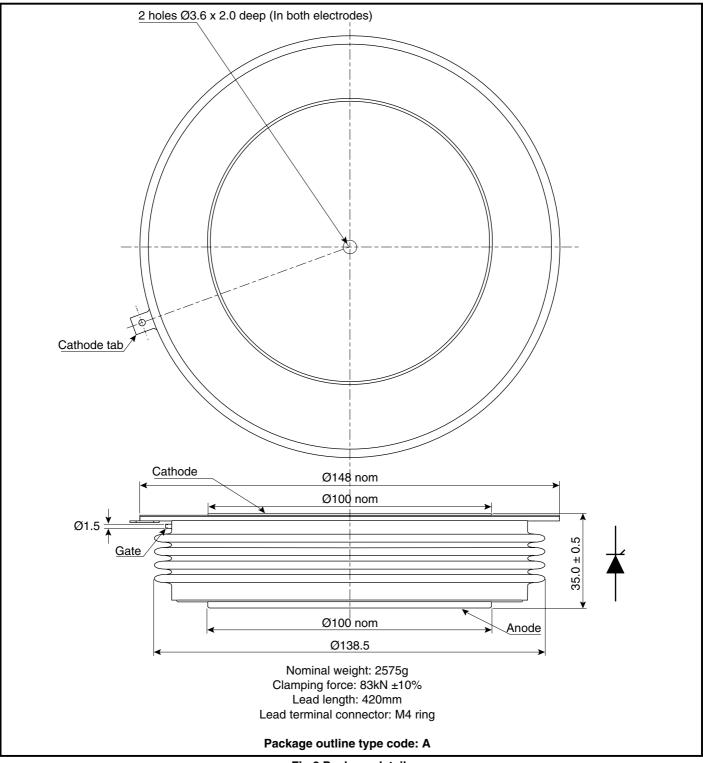






#### **PACKAGE DETAILS**

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





#### POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

#### HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.



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