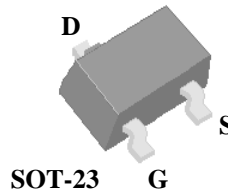
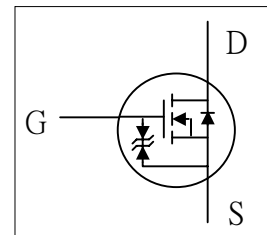




- ▼ Simple Drive Requirement
- ▼ Small Package Outline
- ▼ Surface Mount Device
- ▼ Halogen Free & RoHS Compliant Product



$BV_{DSS}$	600V
$R_{DS(ON)}$	72 $\Omega$
$I_D$	51mA



### Description

Advanced Power MOSFETs utilized advanced processing techniques to achieve the lowest possible on-resistance, extremely efficient and cost-effectiveness device.

The special design SOT-23 package with good thermal performance is widely preferred for all commercial-industrial surface mount applications using infrared reflow technique and suited for voltage conversion or switch applications.

### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	600	V
$V_{GS}$	Gate-Source Voltage	$\pm 32$	V
$I_D @ T_A=25^\circ C$	Continuous Drain Current <sup>3</sup> , $V_{GS}$ @ 10V	51	mA
$I_D @ T_A=70^\circ C$	Continuous Drain Current <sup>3</sup> , $V_{GS}$ @ 10V	41	mA
$I_D @ T_A=70^\circ C$	Continuous Drain Current <sup>4</sup> , $V_{GS}$ @ 10V	68	mA
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	300	mA
$P_D @ T_A=25^\circ C$	Total Power Dissipation	0.5	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

### Thermal Data

Symbol	Parameter	Value	Unit
Rthj-a	Maximum Thermal Resistance, Junction-ambient <sup>3</sup>	250	$^\circ C/W$



# AP2332GEN-HF

## Electrical Characteristics @T<sub>j</sub>=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	600	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=10V, I_D=60mA$	-	-	72	$\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2	-	5	V
$g_{fs}$	Forward Transconductance	$V_{DS}=10V, I_D=60mA$	-	110	-	mS
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=480V, V_{GS}=0V$	-	-	25	$\mu A$
$I_{GSS}$	Gate-Source Leakage	$V_{GS}=\pm 32V, V_{DS}=0V$	-	-	$\pm 30$	$\mu A$
$Q_g$	Total Gate Charge <sup>2</sup>	$I_D=0.1A$	-	2	3.2	nC
$Q_{gs}$	Gate-Source Charge	$V_{DS}=200V$	-	1	-	nC
$Q_{gd}$	Gate-Drain ("Miller") Charge	$V_{GS}=10V$	-	0.3	-	nC
$t_{d(on)}$	Turn-on Delay Time <sup>2</sup>	$V_{DS}=300V$	-	10	-	ns
$t_r$	Rise Time	$I_D=60mA$	-	7	-	ns
$t_{d(off)}$	Turn-off Delay Time	$R_G=3.3\Omega$	-	15	-	ns
$t_f$	Fall Time	$V_{GS}=10V$	-	70	-	ns
$C_{iss}$	Input Capacitance	$V_{GS}=0V$	-	40	64	pF
$C_{oss}$	Output Capacitance	$V_{DS}=25V$	-	13.5	-	pF
$C_{riss}$	Reverse Transfer Capacitance	$f=1.0MHz$	-	3.5	-	pF

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_{SD}$	Forward On Voltage <sup>2</sup>	$I_S=0.05A, V_{GS}=0V$	-	-	1.5	V

### Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Mounted on min. copper pad FR4 board
- 4.Mounted on 1 in<sup>2</sup> copper pad FR4 board  $t \leq 10s$  thermal resistance.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

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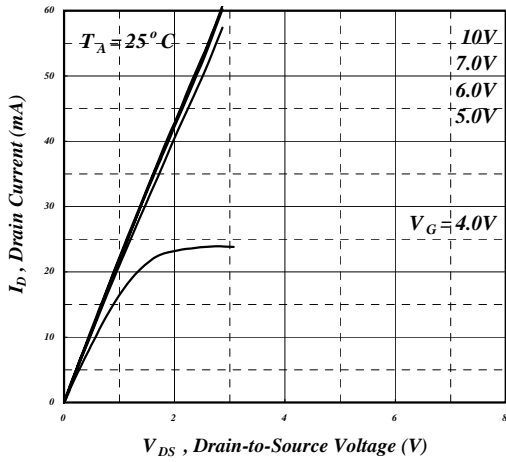


Fig 1. Typical Output Characteristics

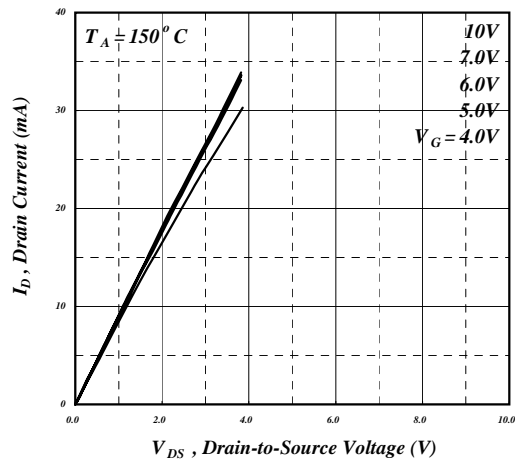


Fig 2. Typical Output Characteristics

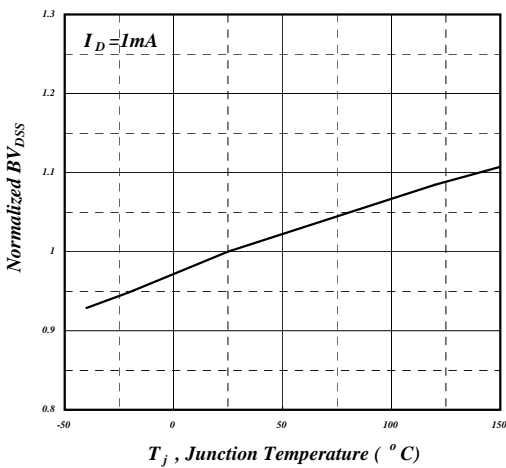


Fig 3. Normalized  $BV_{DSS}$  v.s. Junction Temperature

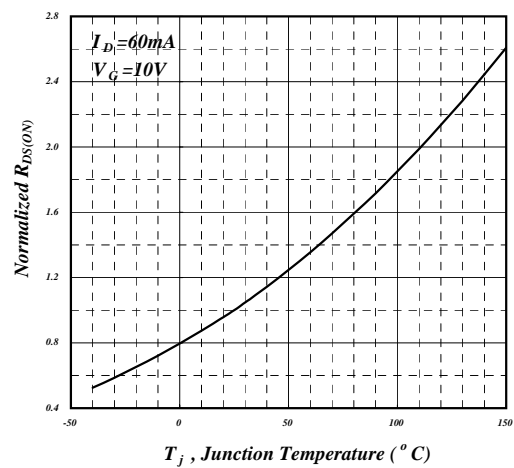


Fig 4. Normalized On-Resistance v.s. Junction Temperature

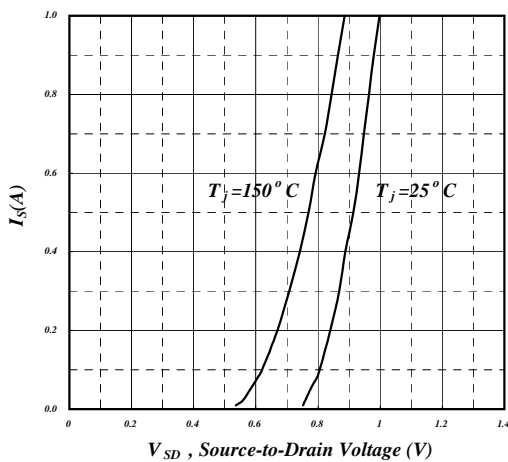


Fig 5. Forward Characteristic of Reverse Diode

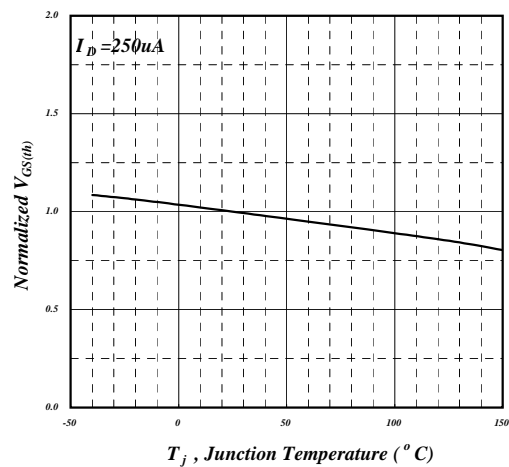


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

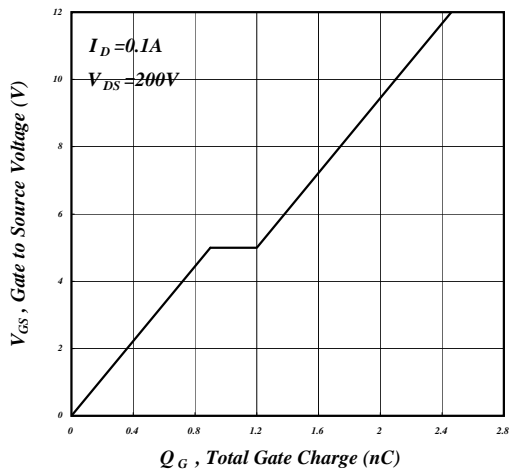


Fig 7. Gate Charge Characteristics

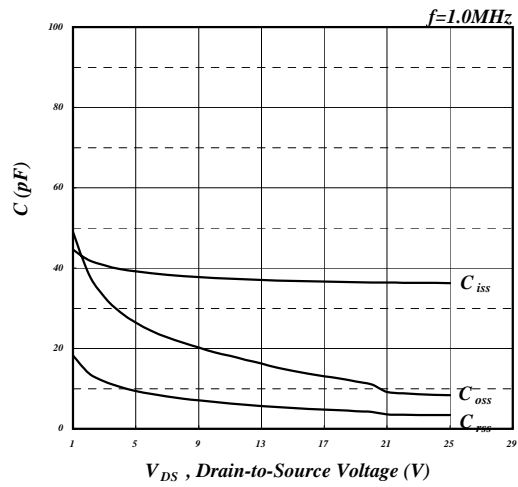


Fig 8. Typical Capacitance Characteristics

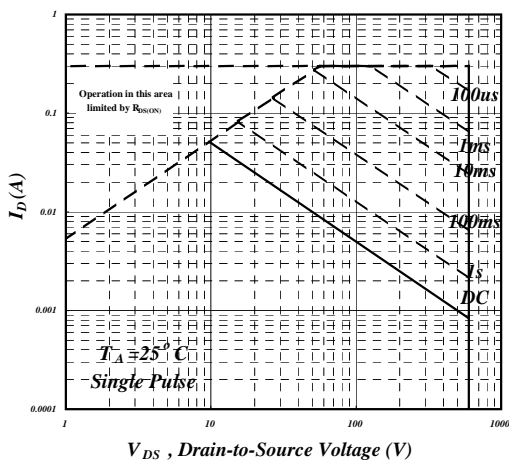


Fig 9. Maximum Safe Operating Area

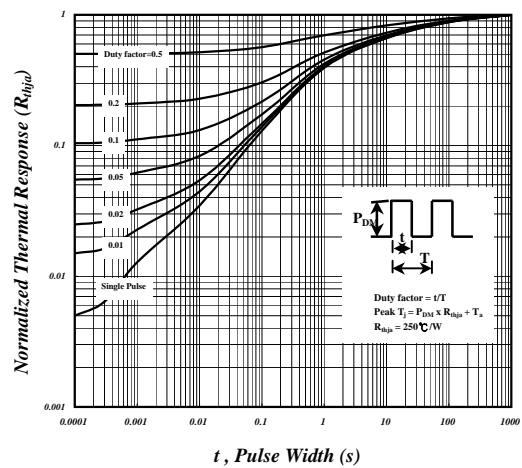


Fig 10. Effective Transient Thermal Impedance

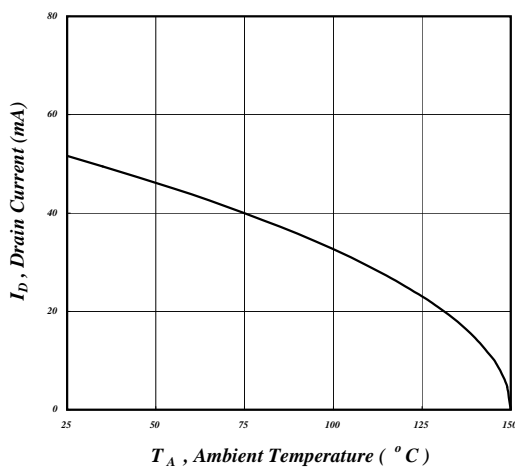


Fig 11. Maximum Continuous Drain Current v.s. Ambient Temperature

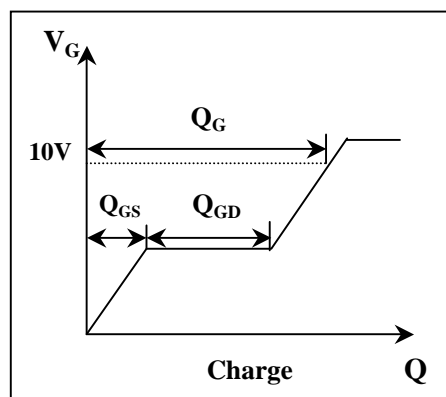


Fig 12. Gate Charge Circuit