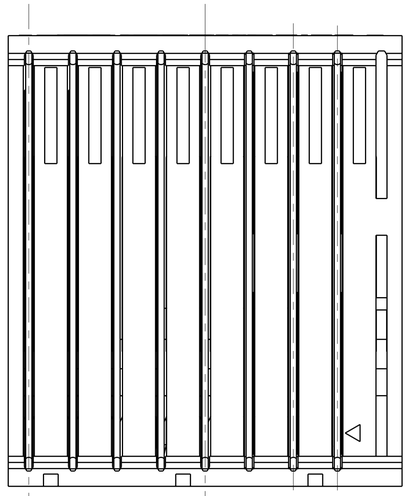
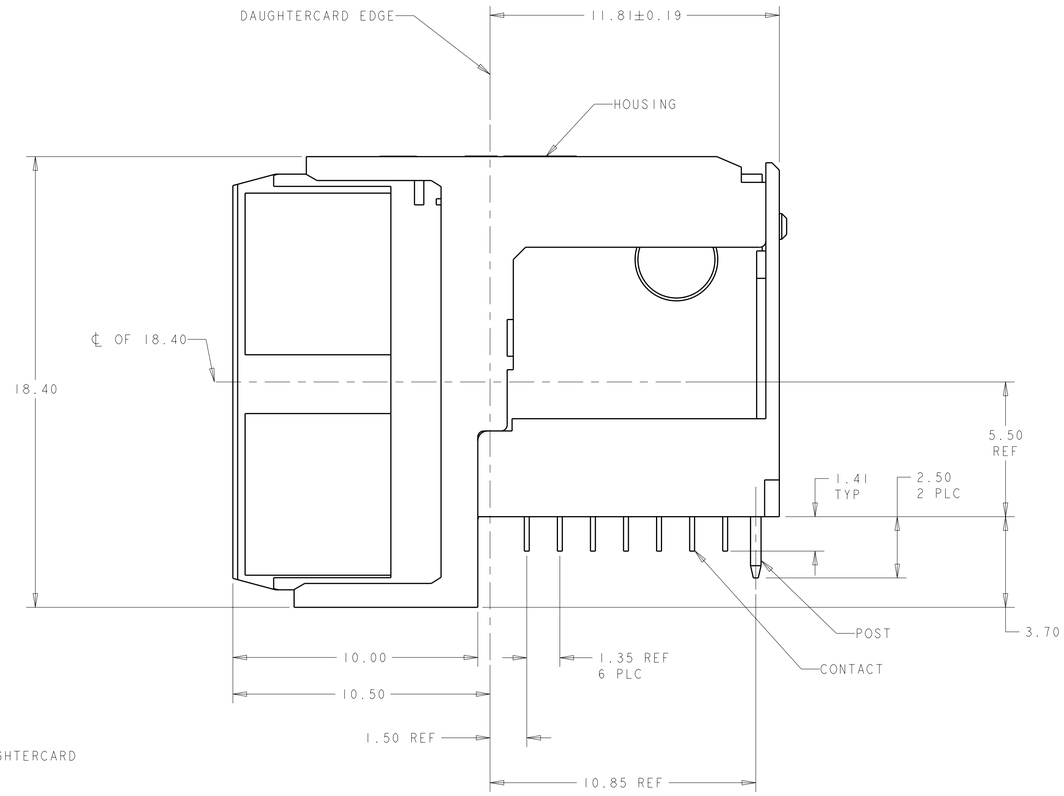
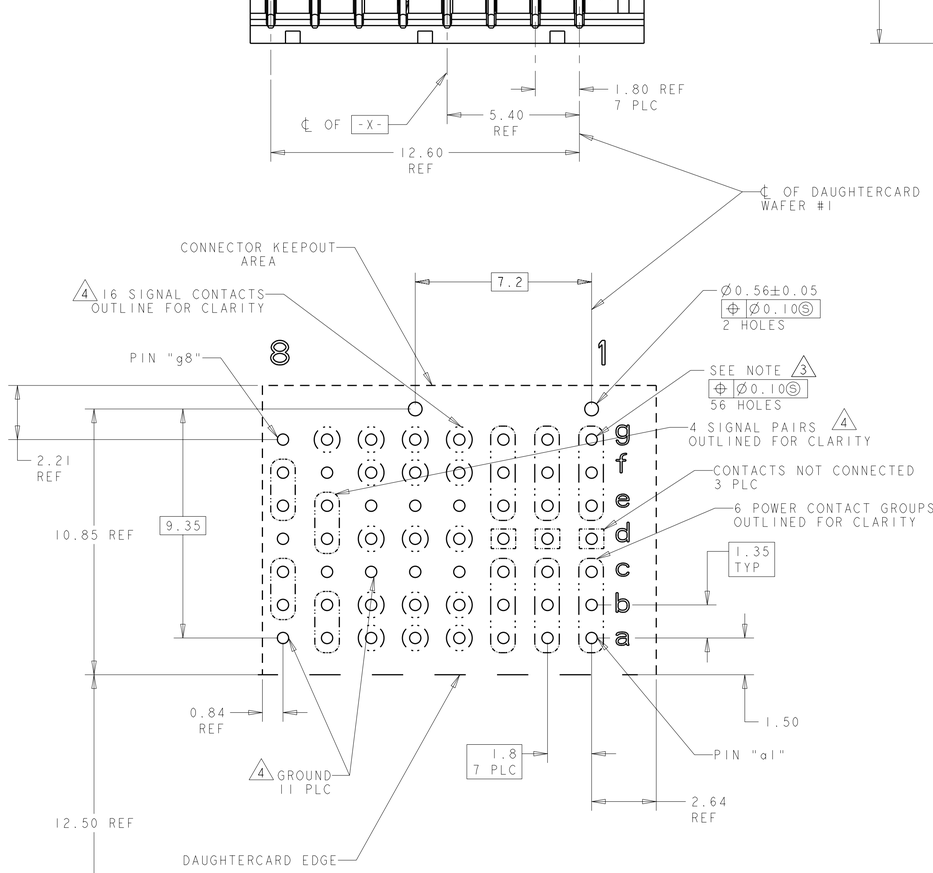
LOC	DIST
GP	00

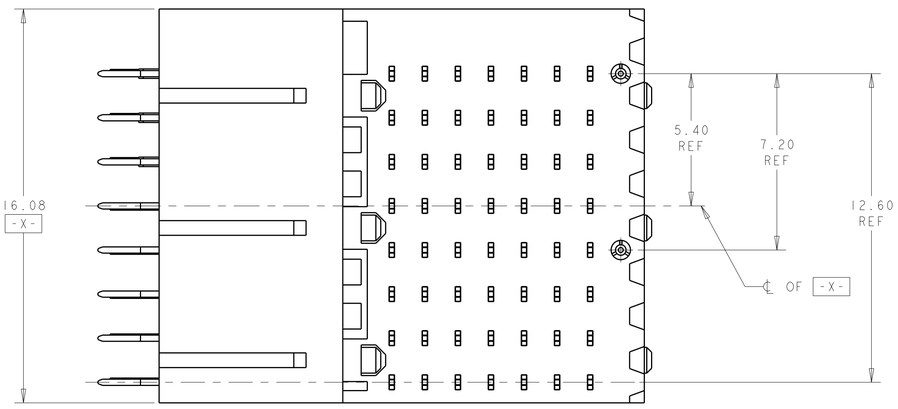
REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	A2	REVISED PER ECO-12-008364	04MAY2012	KH	AS









1. HOUSING: LCP, UL94V0, COLOR: BLACK.
   CONTACT: PHOSPHOR BRONZE.
   POST: BRASS WIRE
2. CONTACT: 0.76µm MIN GOLD IN PAD CONTACT AREA, 1.27µm MIN TIN-LEAD ON PCB TAIL OVER 1.27µm MIN. NICKEL OVER ALL.
   POST: 1.27µm MIN NICKEL PLATED.
3. MANUFACTURING TOLERANCE FOR Ø0.46± 0.05
   DIAMETER FINISHED HOLE WITH SnPB PLATING:

   DRILLED HOLE = Ø0.55±0.02
   COPPER PLATING = 0.025-0.050
   Sn PB PLATING = 0.0038-0.0124
   OR MANUFACTURING TOLERANCE FOR Ø0.475± 0.05
   DIAMETER FINISHED HOLE WITHOUT SnPB PLATING:

   DRILLED HOLE = Ø0.55±0.02
   COPPER PLATING = 0.025-0.050
4. SEE TABLE I FOR INTERCONNECTIONS TO BACKPLANE CONNECTOR.
5. CONTACT: 0.76µm MIN GOLD IN PAD CONTACT AREA, 1.27µm MIN TIN ON PCB TAIL OVER 1.27µm MIN. NICKEL OVER ALL.
   POST: 1.27µm MIN NICKEL PLATED.
6. CONTACT: 1.27µm MIN GOLD IN PAD CONTACT AREA, 1.27µm MIN TIN-LEAD ON PCB TAIL OVER 1.27µm MIN. NICKEL OVER ALL.
   POST: 1.27µm MIN NICKEL PLATED.

TABLE I

INTERCONNECTIONS WITH BACKPLANE CONNECTOR 1410186

TYPICAL INTERCONNECTIONS FOR COLUMN (WAFER): 8		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	bx	cx
	cx	dx
SIGNAL PAIR	ex	gx
	fx	hx
GROUNDS	ax, dx, gx, (ALL COMMONED)	ax, bx, ex, fx, ix

TYPICAL INTERCONNECTIONS FOR COLUMN (WAFER): 7		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	ax	ax
	bx	bx
SIGNAL PAIR	dx	ex
	ex	fx
SIGNAL	gx	ix
GROUNDS	cx, fx (ALL COMMONED)	cx, dx, gx, hx

TYPICAL INTERCONNECTIONS FOR EACH COLUMN (WAFER): 4,5,6		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL	ax	bx
SIGNAL	bx	cx
SIGNAL	dx	ex
SIGNAL	fx	gx
SIGNAL	gx	hx
GROUNDS	cx, ex, (ALL COMMONED)	ax, dx, fx, ix

TYPICAL INTERCONNECTIONS FOR EACH COLUMN (WAFER): 1,2,3		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
POWER	ax, bx, cx	ax, bx, cx, dx
POWER	ex, fx, gx	fx, gx, hx, ix
NOT CONNECTED	dx	ex

NOTE: "x" DESIGNATES THE COLUMN NUMBER

7. CONTACT: 1.27µm MIN GOLD IN PAD CONTACT AREA, 1.27µm MIN TIN ON PCB TAIL OVER 1.27µm MIN. NICKEL OVER ALL.
   POST: 1.27µm MIN NICKEL PLATED.

7	1410189-4
6	1410189-3
5	1410189-2
2	1410189-1
PLATING	PART NO

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS: mm

TOLERANCES UNLESS OTHERWISE SPECIFIED:
0 PLC ±-
1 PLC ±0.5
2 PLC ±0.13
3 PLC ±-
4 PLC ±-
ANGLES ±1°0'

MATERIAL -1

FINISH - SEE TABLE

DWN L SIPE 13FEB2004
CHK J CONSOLI 13FEB2004
APVD G GRIFFITH 14MAR2006
PRODUCT SPEC 108-2072
APPLICATION SPEC 114-13056
WEIGHT -

TE Connectivity

NAME RIGHT-ANGLE PLUG ASSEMBLY, 7 ROW HALF LEFT END, 20.3mm, MultiGig RT2, DAUGHTERCARD CONNECTOR, VITA 46

SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO
A1	00779	C-1410189	-

CUSTOMER DRAWING | SCALE 8:1 | SHEET 1 OF 1 | REV A2

4805 (3/11)

Pro/ENGINEER DRAWING