

ELECTRICAL SPECIFICATIONS:

- |     |                           |                              |  |
|-----|---------------------------|------------------------------|--|
| 1.0 | URNS RATIO:               | (P4-P5-P6) : (J3-J6)         | : 1CT : 1CT ± 3%                         |
|     |                           | (P3-P2-P1) : (J1-J2)         | : 1CT : 1CT ± 3%                         |
| 2.0 | INDUCTANCE:               | (P6-P4)                      | : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias |
|     |                           | (J3-J6)                      | : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias |
| 3.0 | LEAKAGE INDUCTANCE:       | J3-J6 (WITH P6 AND P4 SHORT) | : 0.3 MAX. @ 1MHz                        |
|     |                           | P3-P1 (WITH J2 AND J1 SHORT) | : 0.3 MAX. @ 1MHz                        |
| 4.0 | INTERWINDING CAPACITANCE: | (P6,P5,P4) TO (J6,J3)        | : 30pf MAX @ 1MHz                        |
|     |                           | (P3,P2,P1) TO (J2,J1)        | : 30pf MAX. @ 1MHz                       |
| 5.0 | DC RESISTANCE:            | (P4-P6)=(J2-J1)              | : 1.2 ohms Max.                          |

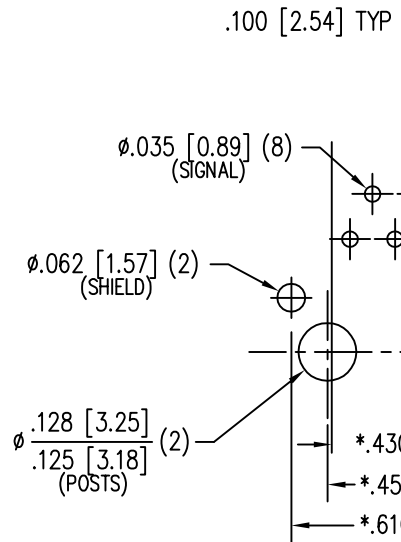
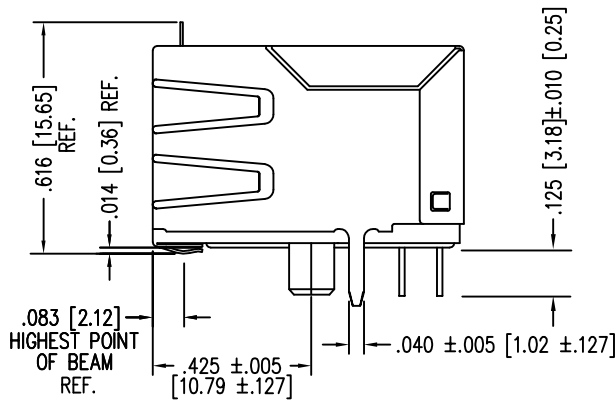
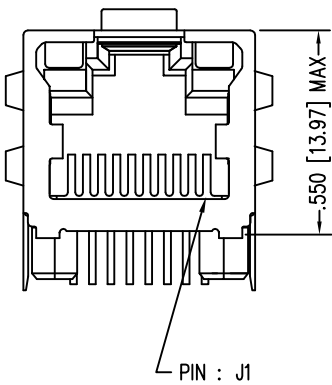
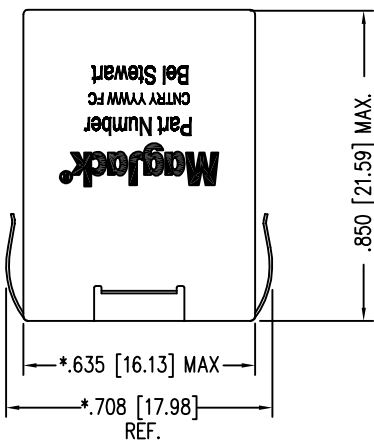
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1.0  
CON

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RECEIVE

- 6.0 RETURN LOSS: (P6-P4)=100 OHMS AND (P1-P3)=100 OHM REF.  
1MHz TO 30MHz : 18dB MIN.  
30MHz TO 80MHz : 12dB MIN.  
NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J6-J3).
- 7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3) : 1500 VAC  
(J3, J6) TO (P4,P6) : 1500 VAC
- 8.0 INSERTION LOSS: RS=RL=100 ohms : 1.1 dB TYP  
100KHz TO 100MHz
- 9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS : 3.0 nS MAX  
OUTPUT VOLTAGE = 1 V peak : 3.0 nS MAX  
PULSE WIDTH= 112nS
- 10.0 CROSS TALK: 1-100 MHz : 30 dB TYP
- 11.0 COMMON TO COMMON MODE ATTENUATION: 1MHz TO 100MHz : 35dB TYP



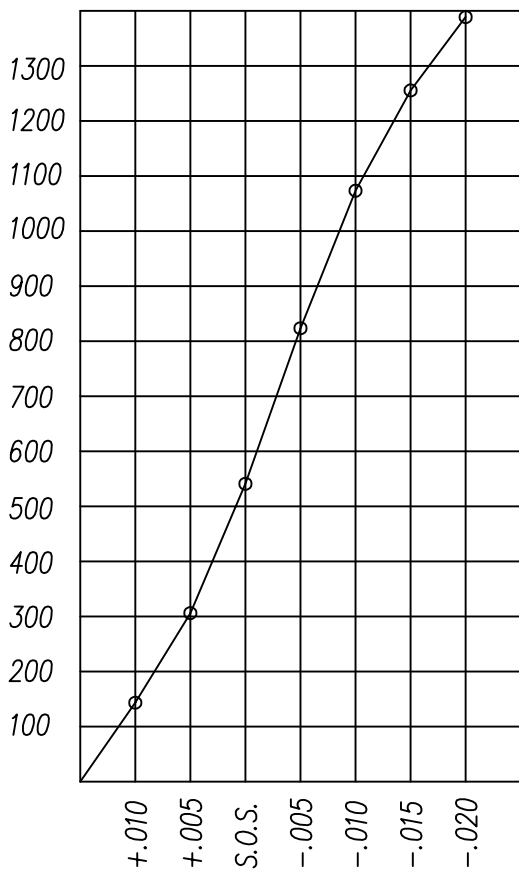
- NOTES:
- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS
  - DIMENSIONS SHOWN WITH "\*" TO BE CENTRAL ABOUT CENTER LINE
  - DIMENSIONS SHOWN ARE SUBJECT TO CHANGE WITHOUT NOTICE.
  - PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
  - STANDARD 50 MICRO-INCH SELECTIVE GOLD PLATING

CT750006

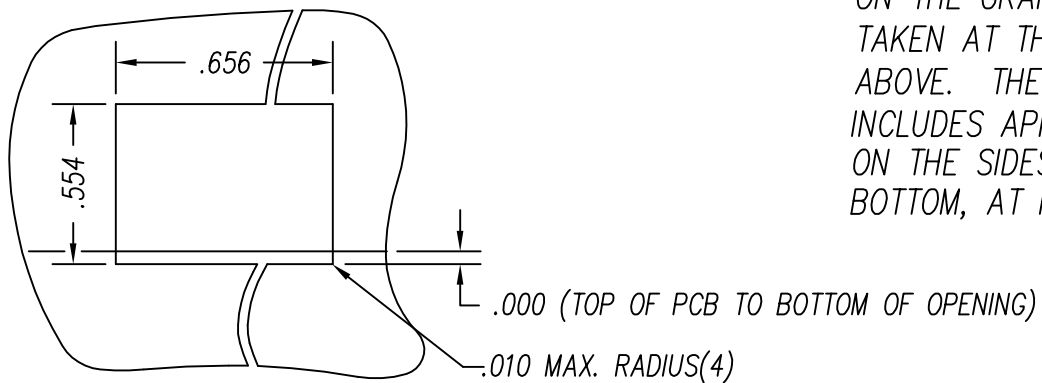
P.C.B. RECOMMENDED  
SEEN FROM  
TOLERANCE  $\pm .003 [0.08]$

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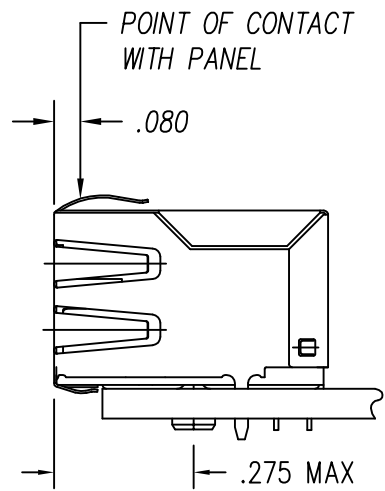
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PANEL GROUNDING BEAM DEFLECTION  
S.O.S. = SUGGESTED OPENING SIZE



SUGGESTED PANEL OPENING



THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO ADJUST THE JACK / PANEL CONTACT POINT TO YET MAINTAIN RELIABLE GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION AND MAY CARRY SOME CONSEQUENCES SUCH AS LOWER MATING FORCES OR INCREASED ASSEMBLY TOLERANCES. FOR THE DATA ON THE GRAPH ARE GENERAL GUIDELINES TAKEN AT THE POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 INCH ON THE SIDES AND TOP AND BOTTOM, AT PANEL OPENING.

CT720034X1/24-001302

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SHEET  
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