

Silicon PNP Power Transistors

2SB1315

DESCRIPTION

- With TO-3PML package
- Low collector saturation voltage

APPLICATIONS

- For use in low frequency power amplifier applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

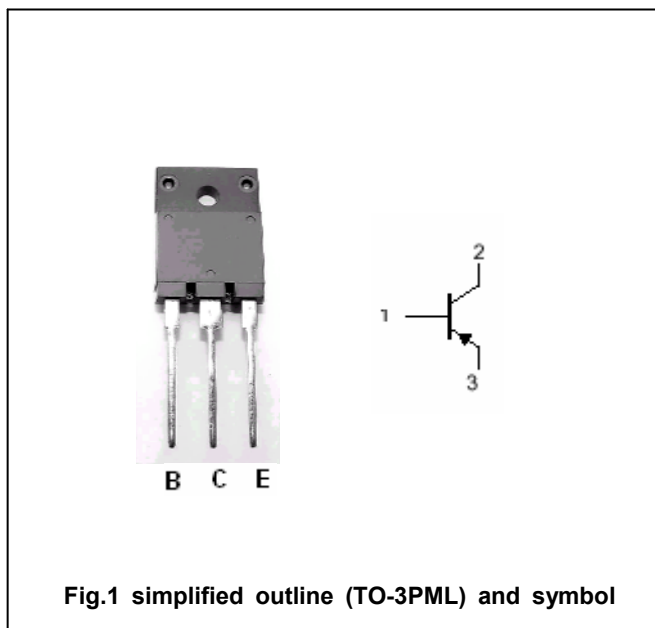


Fig.1 simplified outline (TO-3PML) and symbol

Absolute maximum ratings (Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	MAX	UNIT
V <sub>CBO</sub>	Collector-base voltage	Open emitter	-120	V
V <sub>CEO</sub>	Collector-emitter voltage	Open base	-120	V
V <sub>EBO</sub>	Emitter-base voltage	Open collector	-5	V
I <sub>C</sub>	Collector current		-8	A
P <sub>C</sub>	Collector dissipation	T <sub>a</sub> =25°C	3.5	W
		T <sub>C</sub> =25°C	65	
T <sub>j</sub>	Junction temperature		150	°C
T <sub>stg</sub>	Storage temperature		-55~150	°C

## Silicon PNP Power Transistors

## 2SB1315

## CHARACTERISTICS

T<sub>j</sub>=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =-50mA ; I <sub>B</sub> =0	-120			V
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =-1mA ; I <sub>C</sub> =0	-5			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =-5A ; I <sub>B</sub> =-0.5A			-1.5	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =-5A ; I <sub>B</sub> =-0.5A			-2.0	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =-120V ; I <sub>E</sub> =0			-50	μA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =-5V ; I <sub>C</sub> =0			-50	μA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =-1A ; V <sub>CE</sub> =-5V	60		320	
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =-1A ; V <sub>CE</sub> =-5V		65		MHz
C <sub>OB</sub>	Collector output capacitance	f=1MHz ; V <sub>CB</sub> =-10V		200		pF

Silicon PNP Power Transistors

2SB1315

PACKAGE OUTLINE

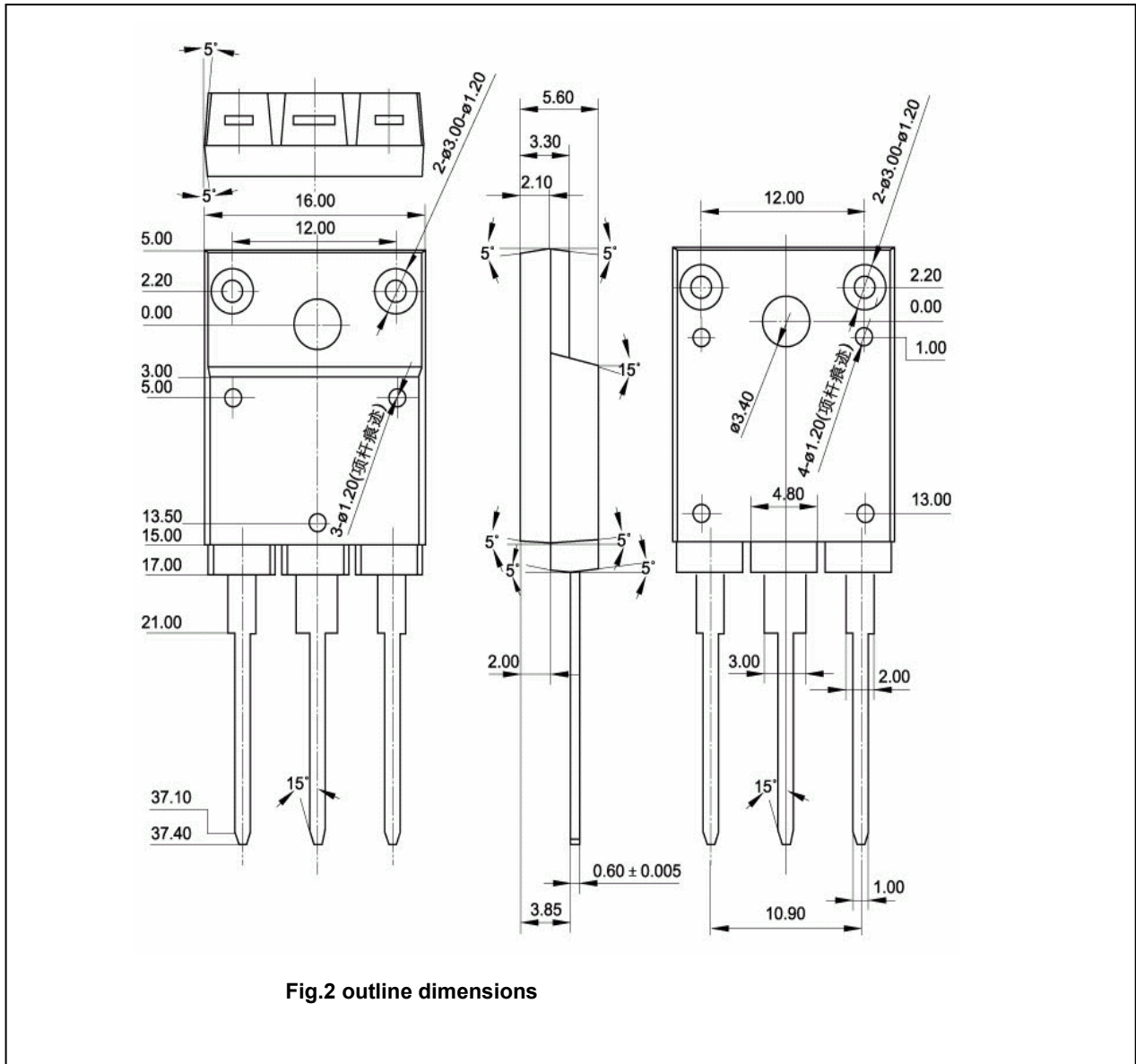


Fig.2 outline dimensions