

# NL17SZ00

## Single 2-Input NAND Gate

The NL17SZ00 is a single 2-input NAND Gate in a tiny footprint package. The SC70-5/SC-88A occupies a very small board area. The device performs much as LCX multi-gate products in speed and drive.

- Tiny SC70-5/SC-88A Package
- 2.7 ns  $T_{PD}$  at 5 Volts (typ)
- Source/Sink 24 mA at 3.0 Volts
- Over-Voltage Tolerant Inputs and Outputs
- Pin For Pin with NC7SZ00
- Chip Complexity: FETs = TBD

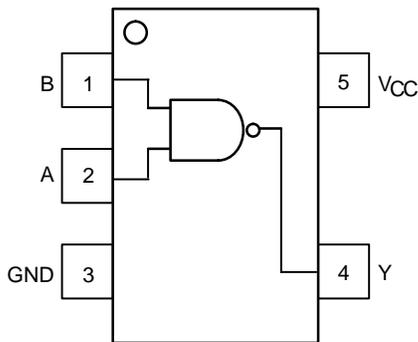


Figure 1. Pinout (Top View)

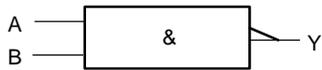


Figure 2. Logic Symbol

### PIN ASSIGNMENT

Pin	Function
1	A
2	B
3	GND
4	Y
5	V <sub>CC</sub>



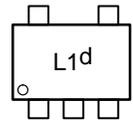
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### MARKING DIAGRAMS



SC70-5/SC-88A/SOT-353  
DF SUFFIX  
CASE 419A



Pin 1

d = Date Code

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

### FUNCTION TABLE

Input		Output Y = AB
A	B	Y
L	L	H
L	H	H
H	L	H
H	H	L

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## MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	DC Supply Voltage	-0.5 to +7.0	V
V <sub>IN</sub>	DC Input Voltage	-0.5 to +7.0	V
V <sub>OUT</sub>	DC Output Voltage	-0.5 to +7.0	V
I <sub>IK</sub>	DC Input Diode Current	-50	mA
I <sub>OK</sub>	DC Output Diode Current	-50	mA
I <sub>OUT</sub>	DC Output Sink Current	±50	mA
I <sub>CC</sub>	DC Supply Current per Supply Pin	±100	mA
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C
T <sub>L</sub>	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
T <sub>J</sub>	Junction Temperature Under Bias	+150	°C
θ <sub>JA</sub>	Thermal Resistance	SC-70/SC-88A (NOTE ) TSOP-5 350 230	°C/W
P <sub>D</sub>	Power Dissipation in Still Air at 85°C	SC-70/SC-88A TSOP-5 150 200	mW
MSL	Moisture Sensitivity	Level 1	
F <sub>R</sub>	Flammability Rating	Oxygen Index: 28 to 34 UL 94 V-0 @ 0.125 in	
V <sub>ESD</sub>	ESD Withstand Voltage	Human Body Model (Note 2) Machine Model (Note 3) Charged Device Model (Note 4) > 2000 > 200 N/A	V

Maximum Ratings are those values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation under absolute maximum-rated conditions is not implied. Functional operation should be restricted to the Recommended Operating Conditions.

1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2-ounce copper trace with no air flow.
2. Tested to EIA/JESD22-A114-A.
3. Tested to EIA/JESD22-A115-A.
4. Tested to JESD22-C101-A.

## RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V <sub>CC</sub>	DC Supply Voltage	2.0	5.5	V
V <sub>IN</sub>	DC Input Voltage	0	5.5	V
V <sub>OUT</sub>	DC Output Voltage	0	5.5	V
T <sub>A</sub>	Operating Temperature Range	-40	+85	°C
t <sub>r</sub> , t <sub>f</sub>	Input Rise and Fall Time	V <sub>CC</sub> = 3.0 V ± 0.3 V V <sub>CC</sub> = 5.0 V ± 0.5 V	0 100 20	ns/V

## DEVICE JUNCTION TEMPERATURE VERSUS TIME TO 0.1% BOND FAILURES

Junction Temperature °C	Time, Hours	Time, Years
80	1,032,200	117.8
90	419,300	47.9
100	178,700	20.4
110	79,600	9.4
120	37,000	4.2
130	17,800	2.0
140	8,900	1.0

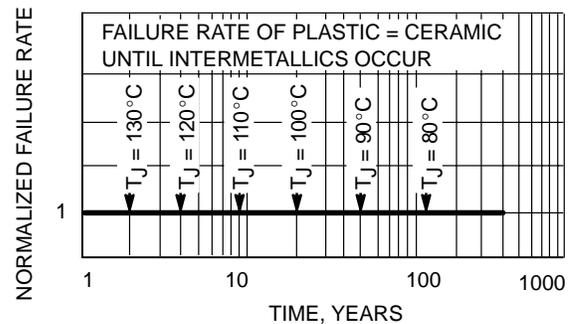


Figure 3. Failure Rate vs. Time Junction Temperature

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## DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			-40°C ≤ T <sub>A</sub> ≤ 85°C		Unit
				Min	Typ	Max	Min	Max	
V <sub>IH</sub>	High-Level Input Voltage		2.3 to 5.5	0.7 V <sub>CC</sub>			0.7 V <sub>CC</sub>		V
V <sub>IL</sub>	Low-Level Input Voltage		2.3 to 5.5			0.3 V <sub>CC</sub>		0.3 V <sub>CC</sub>	V
V <sub>OH</sub>	High-Level Output Voltage V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub>	I <sub>OH</sub> = 100 μA	2.3 to 5.5	V <sub>CC</sub> - 0.1	V <sub>CC</sub>		V <sub>CC</sub> - 0.1		V
		I <sub>OH</sub> = -8 mA	2.3	1.9	2.1		1.9		
		I <sub>OH</sub> = -12 mA	2.7	2.2	2.4		2.2		
		I <sub>OH</sub> = -16 mA	3.0	2.4	2.7		2.4		
		I <sub>OH</sub> = -24 mA	3.0	2.3	2.5		2.3		
		I <sub>OH</sub> = -32 mA	4.5	3.8	4.0		3.8		
V <sub>OL</sub>	Low-Level Output Voltage V <sub>IN</sub> = V <sub>IH</sub> or V <sub>OH</sub>	I <sub>OL</sub> = 100 μA	2.3 to 5.5			0.1		0.1	V
		I <sub>OL</sub> = 8 mA	2.3		0.20	0.3		0.3	
		I <sub>OL</sub> = 12 mA	2.7		0.22	0.4		0.4	
		I <sub>OL</sub> = 16 mA	3.0		0.28	0.4		0.4	
		I <sub>OL</sub> = 24 mA	3.0		0.38	0.55		0.55	
		I <sub>OL</sub> = 32 mA	4.5		0.42	0.55		0.55	
I <sub>IN</sub>	Input Leakage Current	V <sub>IN</sub> = V <sub>CC</sub> or GND	0 to 5.5			±0.1		±1.0	μA
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = V <sub>CC</sub> or GND	5.5			1		10	μA

## AC ELECTRICAL CHARACTERISTICS t<sub>R</sub> = t<sub>F</sub> = 3.0 ns

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			-40°C ≤ T <sub>A</sub> ≤ 85°C		Unit
				Min	Typ	Max	Min	Max	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay (Figure 4 and 5)	R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 15 pF	2.5 ± 0.2	0.8	3.0	6.5	0.8	7.0	ns
		R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 15 pF	3.3 ± 0.3	0.5	2.4	4.5	0.5	4.7	
		R <sub>L</sub> = 500 Ω, C <sub>L</sub> = 50 pF		1.5	2.4	5.0	1.5	5.2	
		R <sub>L</sub> = 1 MΩ, C <sub>L</sub> = 15 pF	5.0 ± 0.5	0.5	2.0	3.9	0.5	4.1	
		R <sub>L</sub> = 500 Ω, C <sub>L</sub> = 50 pF		0.8	2.4	4.3	0.8	4.5	

## CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Condition	Typical	Unit
C <sub>IN</sub>	Input Capacitance	V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	4	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 5)	10 MHz, V <sub>CC</sub> = 3.3 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	25	pF
		10 MHz, V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	30	

5. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I<sub>CC(OPR)</sub> = C<sub>PD</sub> • V<sub>CC</sub> • f<sub>in</sub> + I<sub>CC</sub>. C<sub>PD</sub> is used to determine the no-load dynamic power consumption; P<sub>D</sub> = C<sub>PD</sub> • V<sub>CC</sub><sup>2</sup> • f<sub>in</sub> + I<sub>CC</sub> • V<sub>CC</sub>.

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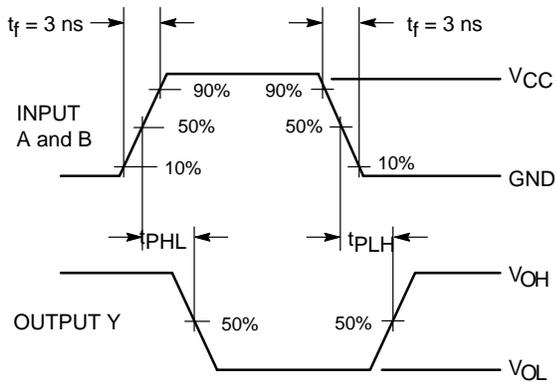
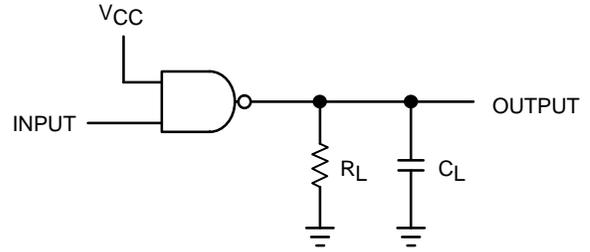


Figure 4. Switching Waveform



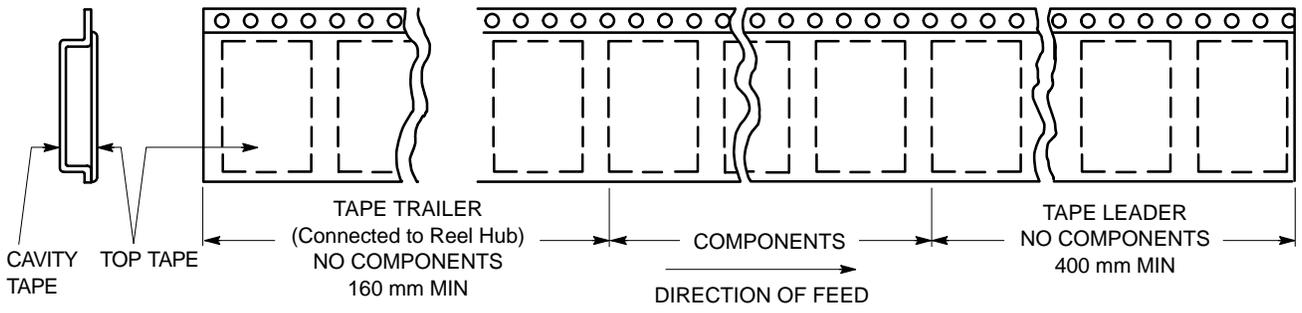
A 1-MHz square input wave is recommended for propagation delay tests.

Figure 5. Test Circuit

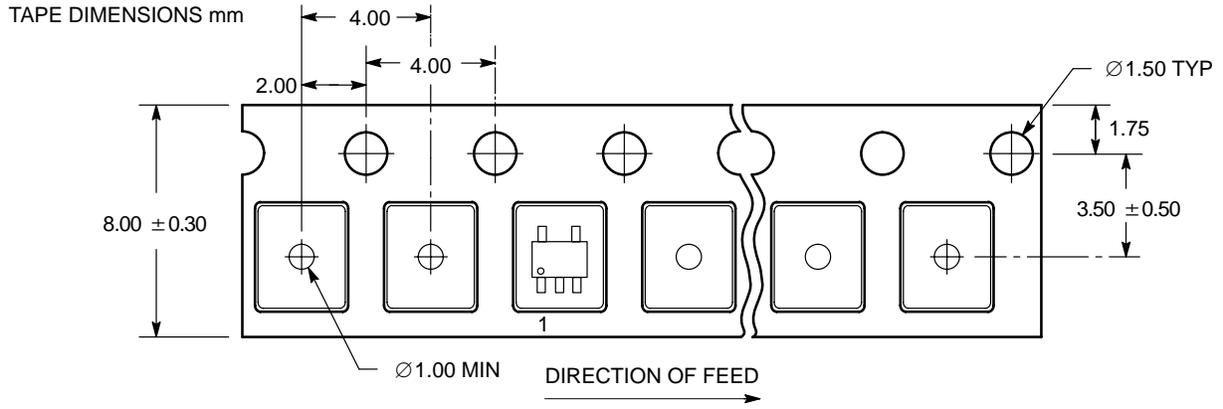
## DEVICE ORDERING INFORMATION

Device Order Number	Device Nomenclature							Package Type	Tape and Reel Size
	Logic Circuit Indicator	No. of Gates per Package	Temp Range Identifier	Technology	Device Function	Package Suffix	Tape and Reel Suffix		
NL17SZ00DFT2	NL	1	7	SZ	00	DF	T2	SC70-5/ SC-88A/ SOT-353	178 mm, 3000 Units

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**Figure 6. Tape Ends for Finished Goods**



**Figure 7. SC-70/SC-88A/SOT-353 DFT2 Reel Configuration/Orientation**

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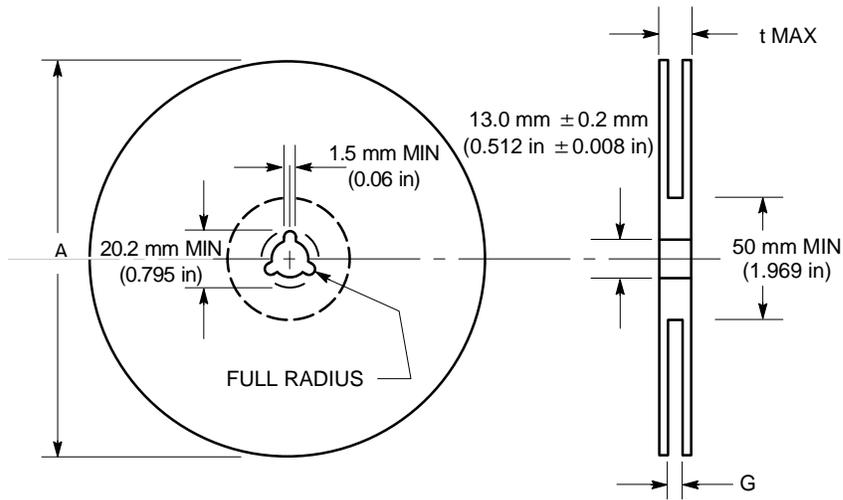


Figure 8. Reel Dimensions

## REEL DIMENSIONS

Tape Size	T and R Suffix	A Max	G	t Max
8 mm	T1, T2	178 mm (7 in)	8.4 mm, + 1.5 mm, -0.0 (0.33 in + 0.059 in, -0.00)	14.4 mm (0.56 in)

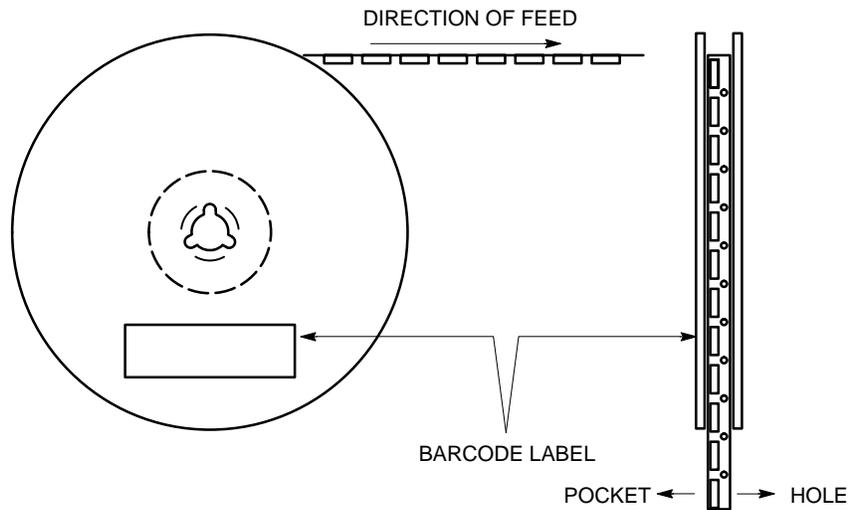
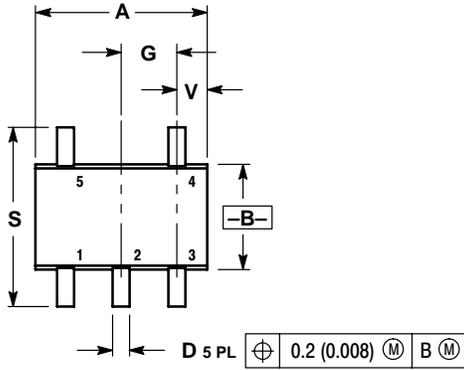


Figure 9. Reel Winding Direction

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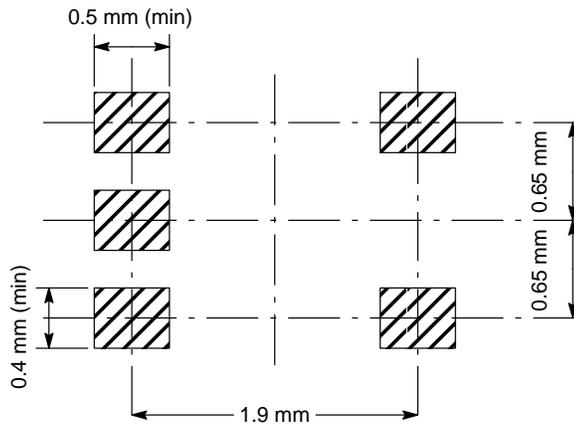
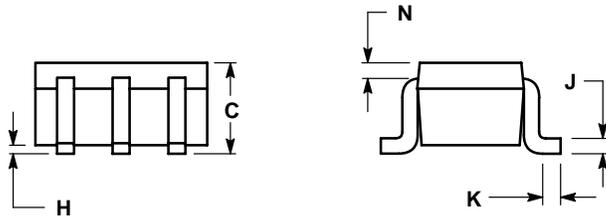
## PACKAGE DIMENSIONS

SC70-5/SC-88A/SOT-353  
 DF SUFFIX  
 5-LEAD PACKAGE  
 CASE 419A-01  
 ISSUE E



- NOTES:  
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.  
 2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.071	0.087	1.80	2.20
B	0.045	0.053	1.15	1.35
C	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026 BSC		0.65 BSC	
H	---	0.004	---	0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008 REF		0.20 REF	
S	0.079	0.087	2.00	2.20
V	0.012	0.016	0.30	0.40



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