





EH47 00

Series

RoHS Compliant (Pb-free) 2.5V 4 Pad 2.5mm x 3.2mm
Ceramic SMD LVCMOS Oscillator

Frequency Tolerance/Stability ______ ±100ppm Maximum

Operating Temperature Range – 0°C to +70°C

T TS -12.000M

Nominal Frequency 12.000MHz

Pin 1 Connection
Tri-State (High Impedance)

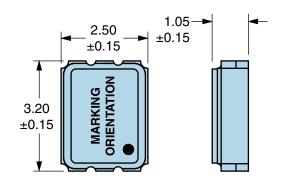
Duty Cycle 50 ±5(%)

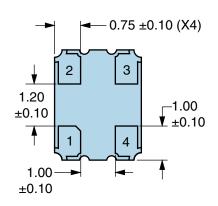
Operation 260°C ging at 25°C ±5ppm perating Temperature Range 0°C to upply Voltage 2.5Vdc put Current 6mA M utput Voltage Logic High (Voh) 90% o utput Voltage Logic Low (Vol) 10% of ise/Fall Time 6nSec uty Cycle 50 ±5(DMHz pm Maximum (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the
Operating 260°C ging at 25°C perating Temperature Range o°C to upply Voltage put Current utput Voltage Logic High (Voh) utput Voltage Logic Low (Vol) ise/Fall Time Operating Above fixed provided to the second provided t	om Maximum (Inclusive of all conditions: Calibration Tolerance at 25°C. Frequency Stability over the
perating Temperature Range 0°C to upply Voltage 2.5Vdc put Current 6mA Mutput Voltage Logic High (Voh) 90% of utput Voltage Logic Low (Vol) 10% of ise/Fall Time 6nSec uty Cycle 50 ±5(ting Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°, Reflow, Shock, and Vibration)
put Current 6mA N utput Voltage Logic High (Voh) 90% o utput Voltage Logic Low (Vol) 10% o ise/Fall Time 6nSec uty Cycle 50 ±5(n/Year Maximum
put Current 6mA M utput Voltage Logic High (Voh) 90% o utput Voltage Logic Low (Vol) 10% o ise/Fall Time 6nSec uty Cycle 50 ±5(+70°C
utput Voltage Logic High (Voh) utput Voltage Logic Low (Vol) ise/Fall Time 6nSec tuty Cycle 50 ±5(c ±5%
utput Voltage Logic Low (Vol) 10% or ise/Fall Time 6nSec uty Cycle 50 ±5(Maximum (No Load)
ise/Fall Time 6nSec uty Cycle 50 ±5(f Vdd Minimum (IOH = -8mA)
uty Cycle 50 ±5(f Vdd Maximum (IOL = +8mA)
,	Maximum (Measured at 20% to 80% of waveform)
	(%) (Measured at 50% of waveform)
pad Drive Capability 15pF N	Maximum
utput Logic Type CMOS	
in 1 Connection Tri-Sta	ate (High Impedance)
ri-State Input Voltage (Vih and Vil) 90% of Imped	of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High ance)
tandby Current 10µA M	Maximum (Pin 1 = Ground)
bsolute Clock Jitter ±100p	Sec Maximum
tart Up Time 10mSe	ec Maximum
torage Temperature Range -55°C	to +125°C

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS		
ESD Susceptibility	MIL-STD-883, Method 3015, Class 1, HBM: 1500Vdc	
Fine Leak Test	MIL-STD-883, Method 1014, Condition A	
Flammability	UL94-V0	
Gross Leak Test	MIL-STD-883, Method 1014, Condition C	
Mechanical Shock	MIL-STD-883, Method 2002, Condition B	
Moisture Resistance	MIL-STD-883, Method 1004	
Moisture Sensitivity	J-STD-020, MSL 1	
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K	
Resistance to Solvents	MIL-STD-202, Method 215	
Solderability	MIL-STD-883, Method 2003	
Temperature Cycling	MIL-STD-883, Method 1010, Condition B	
Vibration	MIL-STD-883, Method 2007, Condition A	



MECHANICAL DIMENSIONS (all dimensions in millimeters)



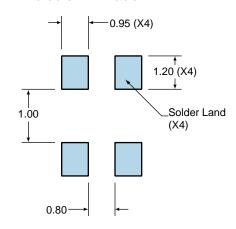


PIN	CONNECTION
1	Tri-State
2	Case Ground
3	Output
4	Supply Voltage

LINE	MARKING
1	EPO
	XXXXX XXXXX=Ecliptek Manufacturing Identifier

Suggested Solder Pad Layout

All Dimensions in Millimeters



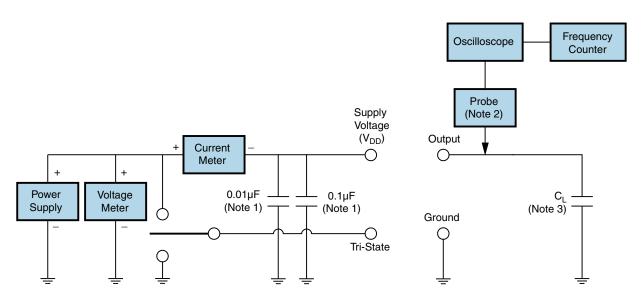
All Tolerances are ±0.1



OUTPUT WAVEFORM & TIMING DIAGRAM



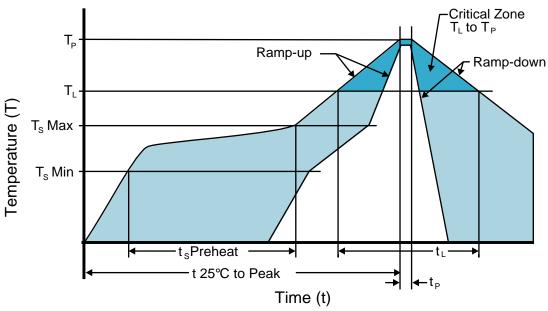
Test Circuit for CMOS Output



- Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.
- Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.
- Note 3: Capacitance value C₁ includes sum of all probe and fixture capacitance.



Recommended Solder Reflow Methods



High Temperature Infrared/Convection

T _s MAX to T _∟ (Ramp-up Rate)	3°C/second Maximum
Preheat	
- Temperature Minimum (Ts MIN)	150°C
- Temperature Typical (T _s TYP)	175°C
- Temperature Maximum (T _s MAX)	200°C
- Time (t _s MIN)	60 - 180 Seconds
Ramp-up Rate (T _L to T _P)	3°C/second Maximum
Time Maintained Above:	
- Temperature (T∟)	217°C
- Time (t∟)	60 - 150 Seconds
Peak Temperature (T _P)	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (T _P Target)	250°C +0/-5°C
Time within 5°C of actual peak (tp)	20 - 40 seconds
Ramp-down Rate	6°C/second Maximum
Time 25°C to Peak Temperature (t)	8 minutes Maximum
Moisture Sensitivity Level	Level 1



Recommended Solder Reflow Methods



Low Temperature Infrared/Convection 240°C

T _s MAX to T _L (Ramp-up Rate)	5°C/second Maximum
Preheat	
- Temperature Minimum (T _s MIN)	N/A
- Temperature Typical (T _s TYP)	150°C
- Temperature Maximum (T _s MAX)	N/A
- Time (t _s MIN)	60 - 120 Seconds
Ramp-up Rate (T _L to T _P)	5°C/second Maximum
Time Maintained Above:	
- Temperature (T _L)	150°C
- Time (t∟)	200 Seconds Maximum
Peak Temperature (T _P)	240°C Maximum
Target Peak Temperature (T _P Target)	240°C Maximum 1 Time / 230°C Maximum 2 Times
Time within 5°C of actual peak (tp)	10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time
Ramp-down Rate	5°C/second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1

Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum.

High Temperature Manual Soldering

260°C Maximum for 5 seconds Maximum, 2 times Maximum.