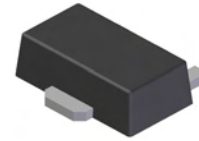


**Features**

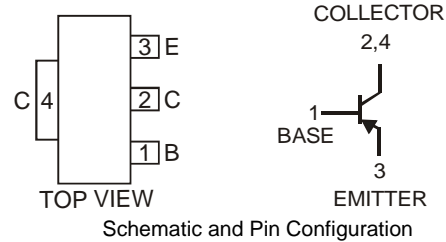
- Epitaxial Planar Die Construction
- Complementary NPN Type Available (2DD2150)
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- **Lead Free By Design/RoHS Compliant (Note 1)**
- **"Green" Device (Note 2)**



SOT89-3L

**Mechanical Data**

- Case: SOT89-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish — Matte Tin annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.072 grams (approximate)



**Maximum Ratings** @ $T_A = 25^\circ\text{C}$  unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	$V_{CB0}$	-20	V
Collector-Emitter Voltage	$V_{CEO}$	-20	V
Emitter-Base Voltage	$V_{EBO}$	-6	V
Peak Pulse Current	$I_{CM}$	-5	A
Continuous Collector Current	$I_C$	-3	A

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ $T_A = 25^\circ\text{C}$	$P_D$	1	W
Thermal Resistance, Junction to Ambient Air (Note 3) @ $T_A = 25^\circ\text{C}$	$R_{\theta JA}$	125	$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** @ $T_A = 25^\circ\text{C}$  unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Conditions
<b>OFF CHARACTERISTICS (Note 4)</b>						
Collector-Base Breakdown Voltage	$V_{(BR)CBO}$	-20	—	—	V	$I_C = -50\mu\text{A}, I_E = 0$
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	-20	—	—	V	$I_C = -1\text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	-6	—	—	V	$I_E = -50\mu\text{A}, I_C = 0$
Collector Cut-Off Current	$I_{CBO}$	—	—	-0.1	$\mu\text{A}$	$V_{CB} = -20\text{V}, I_E = 0$
Emitter Cut-Off Current	$I_{EBO}$	—	—	-0.1	$\mu\text{A}$	$V_{EB} = -5\text{V}, I_C = 0$
<b>ON CHARACTERISTICS (Note 4)</b>						
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$	—	-0.18	-0.5	V	$I_C = -2\text{A}, I_B = -0.1\text{A}$
DC Current Gain	$h_{FE}$	180	—	390	—	$V_{CE} = -2\text{V}, I_C = -0.1\text{A}$
<b>SMALL SIGNAL CHARACTERISTICS</b>						
Output Capacitance	$C_{obo}$	—	28	—	pF	$V_{CB} = -10\text{V}, I_E = 0, f = 1\text{MHz}$
Current Gain-Bandwidth Product	$f_T$	—	220	—	MHz	$V_{CE} = -2\text{V}, I_E = 0.1\text{A}, f = 100\text{MHz}$

- Notes:
1. No purposefully added lead.
  2. Diodes Inc.'s "Green" policy can be found on our website at [http://www.diodes.com/products/lead\\_free/index.php](http://www.diodes.com/products/lead_free/index.php).
  3. Device mounted on FR-4 PCB; pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.
  4. Measured under pulsed conditions. Pulse width = 300 $\mu\text{s}$ . Duty cycle  $\leq 2\%$ .

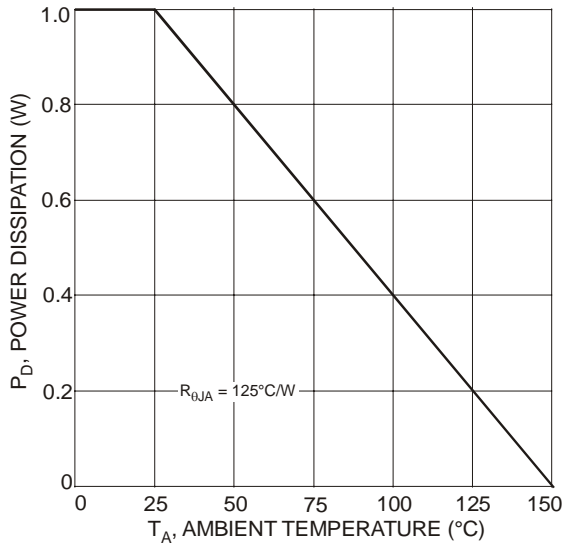


Fig. 1 Power Dissipation vs. Ambient Temperature (Note 3)

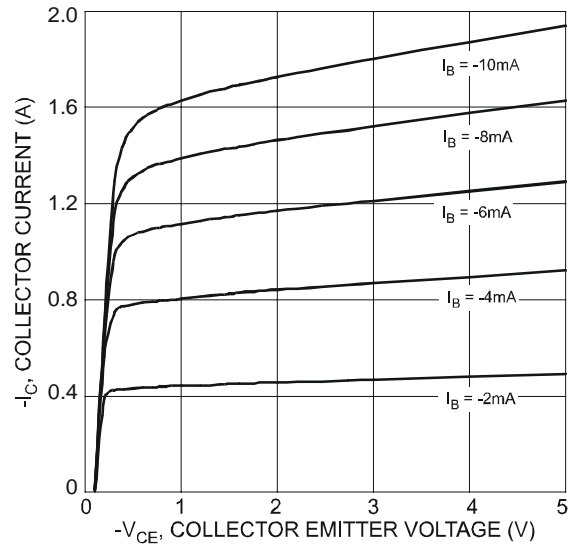


Fig. 2 Typical Collector Current vs. Collector-Emitter Voltage

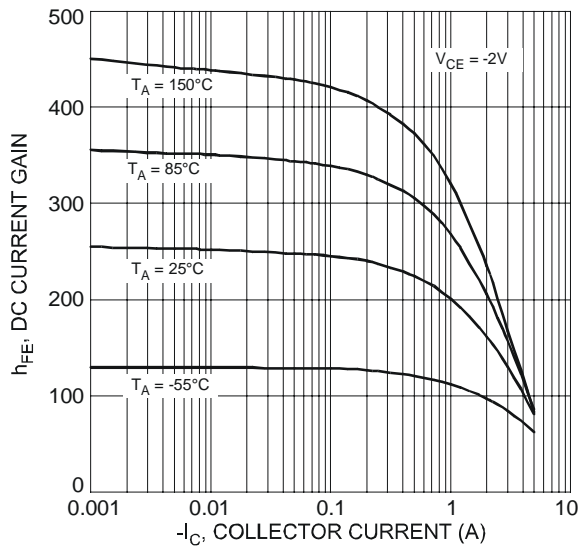


Fig. 3 Typical DC Current Gain vs. Collector Current

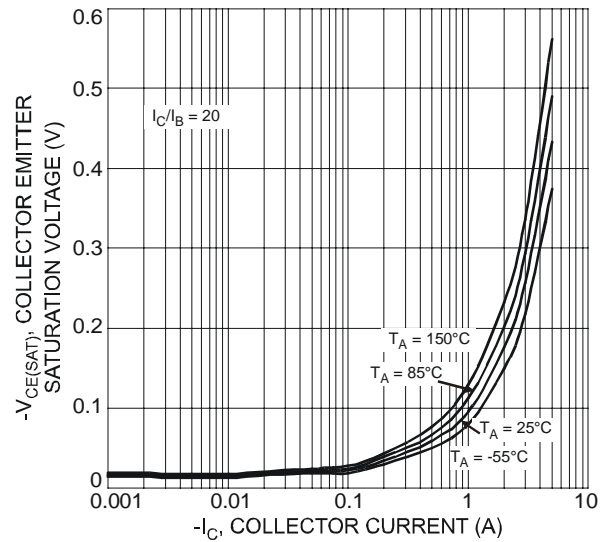


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

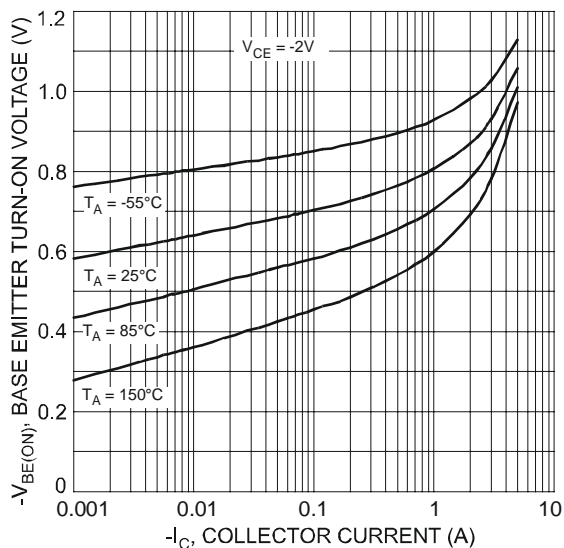


Fig. 5 Typical Base-Emitter Turn-On Voltage vs. Collector Current

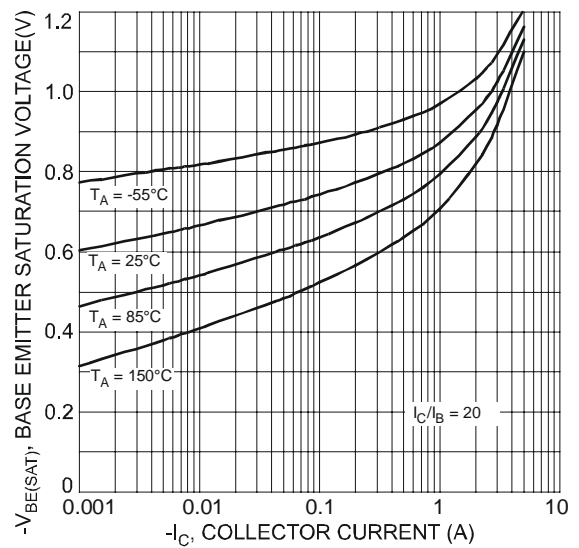
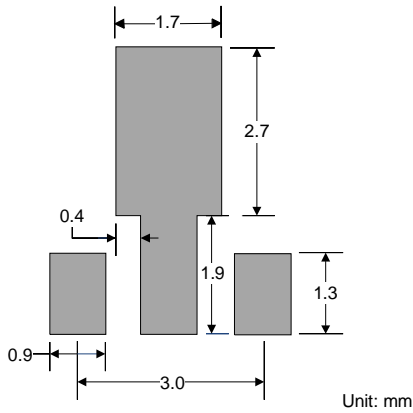


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current



## Suggested Pad Layout



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