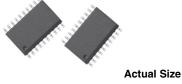


Vishay Thin Film

# Molded, 50 Mil Pitch, Dual-In-Line Resistor Networks



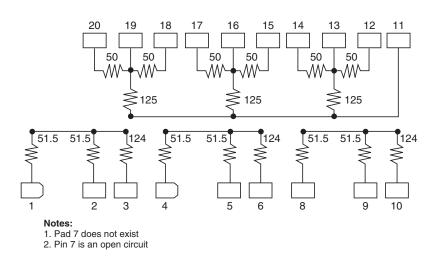
V.35 Termination Network used to insure signal integrity between transmitter and receiver sections of V.35 protocol.

## FEATURES

- Lead (Pb)-free available
- Rugged, molded case construction
- Reduces total assembly costs
- Saves board space
- Compatible with surface mounting equipment
- Uniform performance characteristics

### **TYPICAL PERFORMANCE**

•	ABS	TRACKING
TCR	100	10
	ABS	RATIO
TOL	1, 2	0.5



STANDARD ELECTRICAL SPECIFICATIONS				
TEST		SPECIFICATIONS	CONDITIONS	
Resistance Rang	e	per schematic		
Tolerance:	Absolute	± 1.0 %	51.5 $\Omega$ and 124 $\Omega$	
	Absolute	± 2.0 %	50.0 $\Omega$ and 125 $\Omega$	
TCR:	Absolute	± 100 ppm/°C	- 55 °C to + 125 °C	
	Tracking	± 10 ppm/°C	- 55 °C to + 125 °C	
Power Rating:	Package	1.6 W	- 55 °C to + 125 °C	
Breakdown Volta	ge	50 V		

\* Pb containing terminations are not RoHS compliant, exemptions may apply

RoHS\*





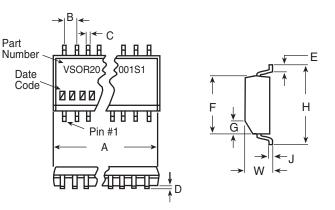
**SCHEMATIC** 

# VSOR2000S1

## Vishay Thin Film Molded, 50 Mil Pitch, Dual-In-Line Resistor Networks



## **DIMENSIONS** in inches and millimeters



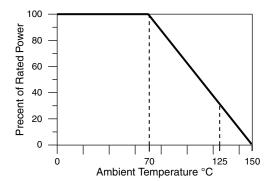
	INCHES	ММ
А	0.500 ± 0.010	12.7 ± 0.25
B (Ref.)	0.050	1.27
C (Ref.)	0.016	0.41
D	0.008	0.20
Е (Тур.)	0.030	0.75
F	$0.293 \pm 0.003$	7.44 ± 0.08
G	0.025 × 45°	$0.64  imes 45^{\circ}$
Н	$0.406 \pm 0.005$	10.31 ± 0.13
J (Ref.)	0.010	0.25
W	0.100 ± 0.005	1.54 ± 0.13

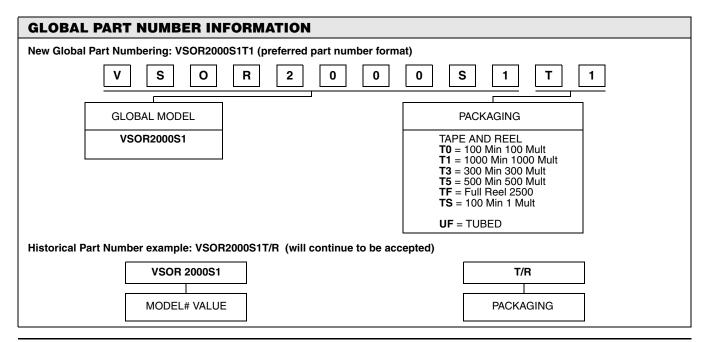
#### IMPRINTING

VSOR2000S1 Date Code

MECHANICAL SPECIFICATIONS				
Resistive Material	Tantalum Nitride			
Substrate Material	Silicon			
Body	Molded Epoxy			
Terminals	Copper Alloy			
Plating	Tin Lead solder			
Lead coplanarity	0.0005"			
Marking Resistance to Solvents	Permanency testing per MIL-STD-202, Method 215			
Lead (Pb)-free Option	100 % Sn Matte			
Lead (Pb)-free Option	Plated			

### **DERATING CURVE**







Vishay

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