MITSUBISHI ELECTRIC

<TRANSISTOR ARRAY>

M54562FP

8-UNIT 500mA DARLINGTON TRANSISTOR ARRAY WITH CLAMP DIODE SOURCE TYPE

DESCRIPTION

M54562FP is an eight-circuit output-sourcing darlington transistor array. The circuits are made of PNP and NPN transistors. This semiconductor integrated circuit performs high current driving with extremely low input-current supply.

FEATURES

- High breakdown voltage (BVCEO ≥ 50V)
- High-current driving (Io(max) = –500mA)
- With clamping diodes
- Driving available with PMOS IC output of 6 ~ 16V or with TTL output
- Output current-sourcing type

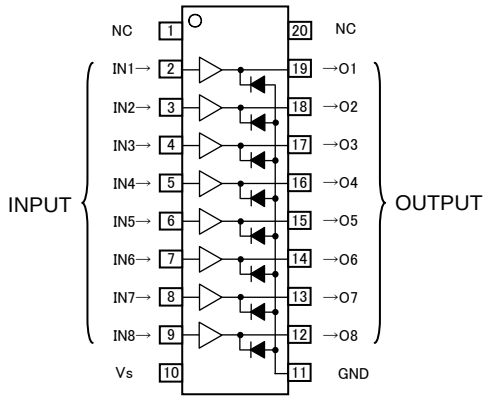
APPLICATIONS

Drives of relays, printers, LEDs, fluorescent display tubes and lamps, and interfaces between MOS-bipolar logic systems and relays, solenoids, or small motors.

FUNCTION

The M54562FP each have eight circuits, which are made of input inverters and current-sourcing outputs.
The outputs are made of PNP transistors and NPN Darlington transistors. The PNP transistor base current is constant. A clamping diode is provided between each output and GND. VS and GND are used commonly among the eight circuits.
The inputs have resistance of 8.5kΩ, and voltage of up to 30V is applicable. Output current is 500 mA maximum. Supply voltage VS is 50V maximum.

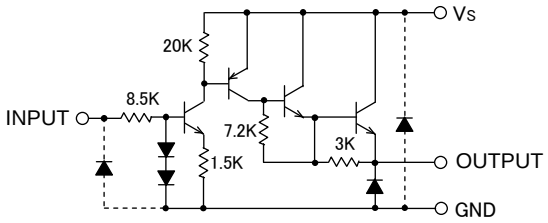
PIN CONFIGURATION



Package type 20P2N-A

NC : No connection

CIRCUIT DIAGRAM



The eight circuits share the VS and GND.

The diode, indicated with the dotted line, is parasitic, and cannot be used.

Unit :Ω

ABSOLUTE MAXIMUM RATINGS (Unless otherwise noted, Ta = –20 ~ +75° C)

Symbol	Parameter	Conditions	Ratings	Unit
V_{CEO} #	Collector-emitter voltage	Output , L	–0.5 ～ +50	V
V_S	Supply voltage		50	V
V_I	Input voltage		–0.5 ～ +30	V
I_O	Output current	Current per circuit output, H	– 500	mA
I_F	Clamping diode forward current		– 500	mA
V_R #	Clamping diode reverse voltage		50	V
Pd	Power dissipation	Ta = 25℃, when mounted on board	1.10	W
Topr	Operating temperature		–20 ～ +75	℃
Tstg	Storage temperature		–55 ～ +125	℃

: Unused Input pins must be connected to GND.

RECOMMENDED OPERATING (Unless otherwise noted, Ta = –20 ~ +75℃)

Symbol	Parameter		Limits			Unit
			min	typ	max	
V_S	Supply voltage		0	—	50	V
I_O	Output current (Current per 1 circuit when 8 circuits are coming on simultaneously)	Duty Cycle no more than 5%	0	—	–350	mA
		Duty Cycle no more than 30%	0	—	–100	
V_{IH}	"H" input voltage		2.4	5.0	30	V
V_{IL}	"L" input voltage		0	—	0.2	V

ELECTRICAL CHARACTERISTICS (Unless otherwise noted, Ta = －20～＋75℃)

Symbol	Parameter	Test conditions	Limits			Unit
			min	typ*	max	
$I_{S(leak)}$ #	Supply leak current	V_S = 50V, V_I = 0.2V	—	—	100	μA
$V_{CE(sat)}$	Collector-emitter saturation voltage	V_S = 10V, V_I = 2.4V, I_O = −350mA	—	1.75	2.4	V
		V_S = 10V, V_I = 2.4V, I_O = −100mA	—	1.5	2.0	
I_I	Input current	V_I = 5V	—	0.48	0.75	mA
		V_I = 25V	—	2.8	4.7	
I_S	Supply current	V_S = 50V, V_I = 5V (all input)	—	5.6	15.0	mA
V_F #	Clamping diode forward voltage	I_F = −350mA	—	−1.2	−2.4	V
I_R	Clamping diode reverse current	V_R = 50V	—	—	100	μA

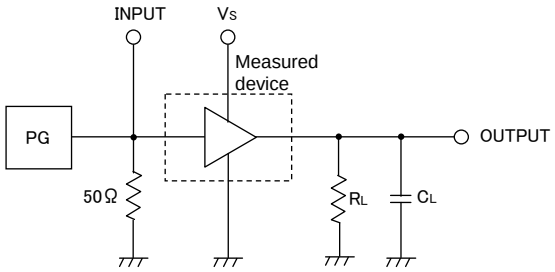
* : The typical values are those measured under ambient temperature (Ta) of 25℃. There is no guarantee that these values are obtained under any conditions.

: Unused Input pins must be connected to GND.

SWITCHING CHARACTERISTICS (Unless otherwise noted, Ta = 25℃)

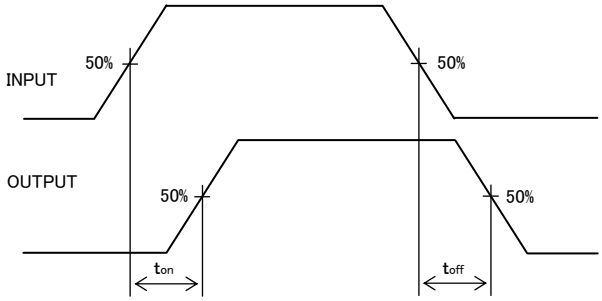
Symbol	Parameter	Test conditions	Limits			Unit
			min	typ	max	
ton	Turn-on time	C_L = 15pF (note 1)	—	110	—	ns
toff	Turn-off time		—	5200	—	ns

NOTE 1 TEST CIRCUIT



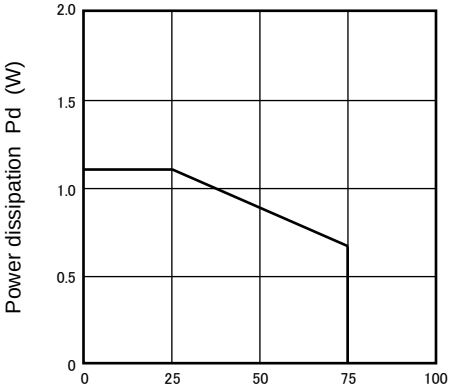
(1) Pulse generator (PG) characteristics: PRR = 1kHz, tw = 10ms, tr = 6ns, tf = 6ns, Z_O = 50Ω, V_I = 0 to 2.4V
(2) Input-output conditions : R_L = 30Ω, V_S = 10V
(3) Electrostatic capacity CL includes floating capacitance at connections and input capacitance at probes

TIMING DIAGRAM

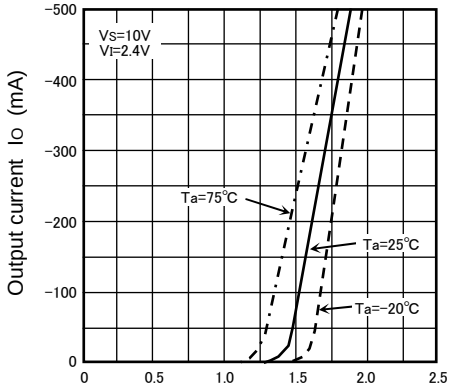


TYPICAL CHARACTERISTICS

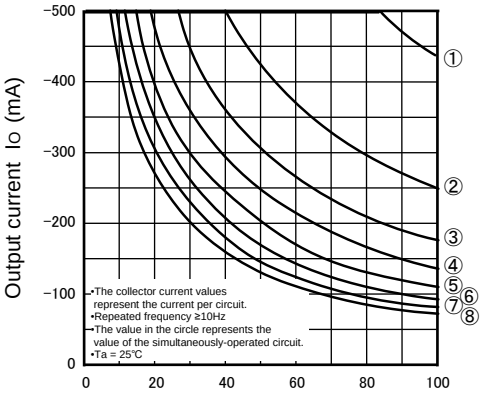
Thermal Derating Factor Characteristics



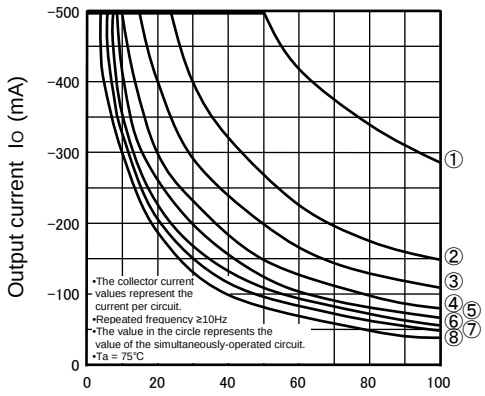
Output Saturation Voltage Collector Current Characteristics



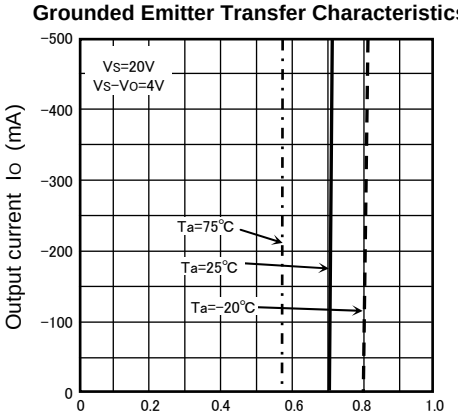
Duty-Cycle-Output current Characteristics



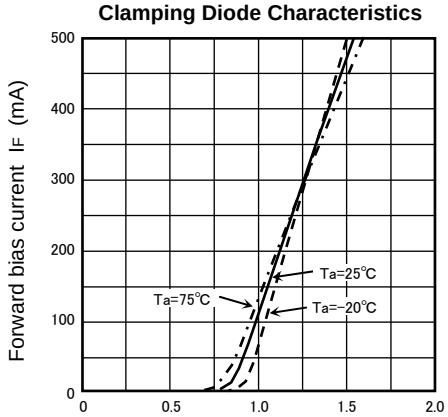
Duty-Cycle-Output current Characteristics



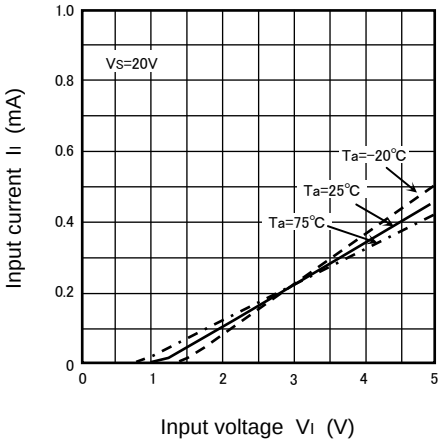
Grounded Emitter Transfer Characteristics



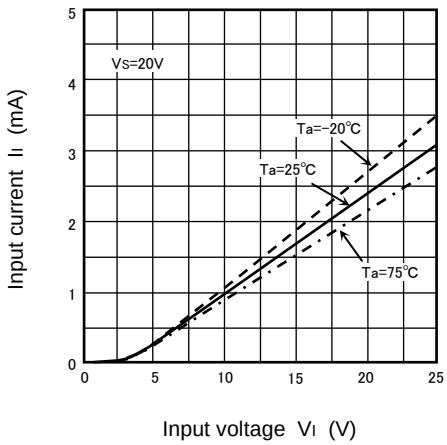
Clamping Diode Characteristics



Input Characteristics



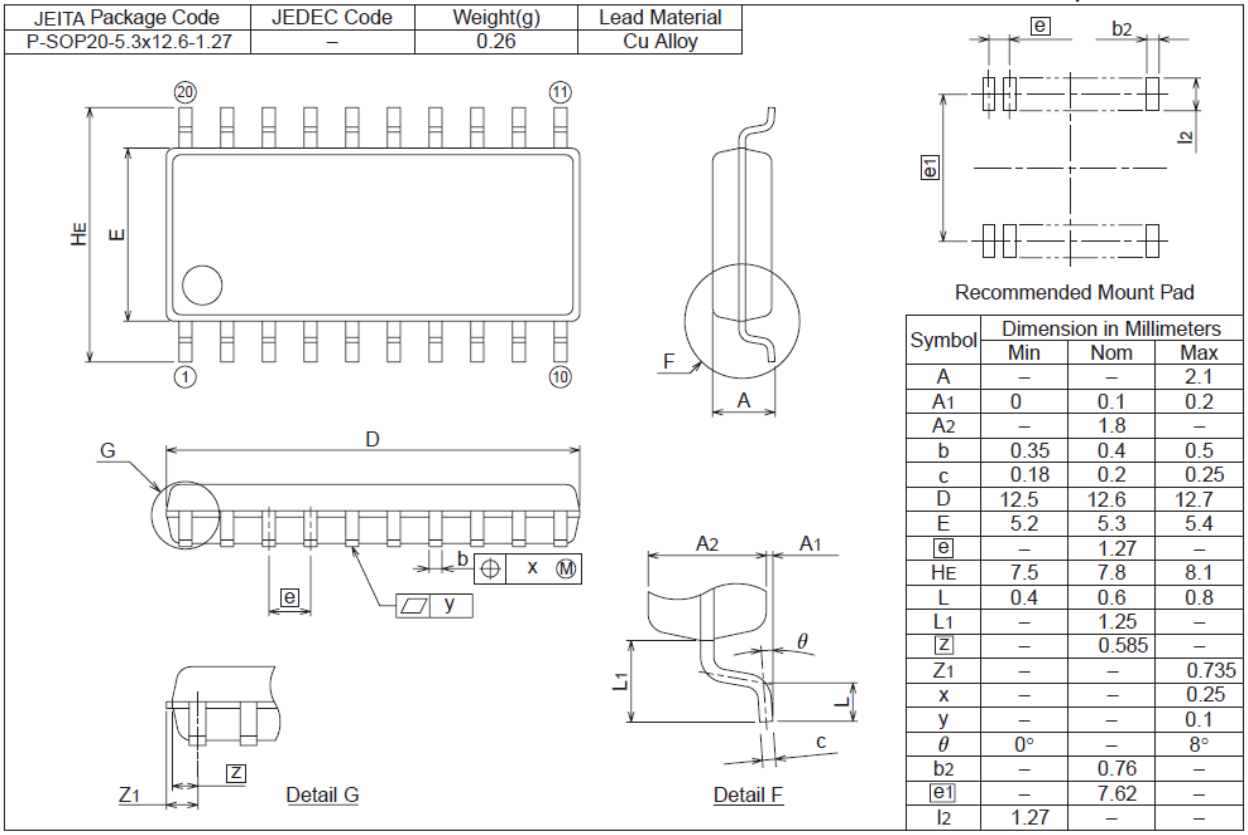
Input Characteristics



PACKAGE OUTLINE

20P2N-A — **Plastic 20pin 300mil SOP**

JEITA Package Code	JEDEC Code	Weight(g)	Lead Material
P-SOP20-5.3x12.6-1.27	–	0.26	Cu Alloy



Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	–	–	2.1
A1	0	0.1	0.2
A2	–	1.8	–
b	0.35	0.4	0.5
c	0.18	0.2	0.25
D	12.5	12.6	12.7
E	5.2	5.3	5.4
e	–	1.27	–
HE	7.5	7.8	8.1
L	0.4	0.6	0.8
L1	–	1.25	–
Z	–	0.585	–
Z1	–	–	0.735
x	–	–	0.25
y	–	–	0.1
θ	0°	–	8°
b2	–	0.76	–
e1	–	7.62	–
l2	1.27	–	–

Keep safety first in your circuit designs!

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