

FEATURES

- 10 μ s Short Circuit Withstand
- High Thermal Cycling Capability
- Non Punch Through Silicon
- Isolated AISiC Base With AlN Substrates
- Lead Free Construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 3300V and currents up to 2400A.

The DIM800DCM17-A000 is a 1700V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10 μ s short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM800DCM17-A000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V_{CES}	1700V
$V_{CE(sat)}$ * (typ)	2.7V
I_C (max)	800A
$I_{C(PK)}$ (max)	1600A

* Measured at the power busbars, not the auxiliary terminals

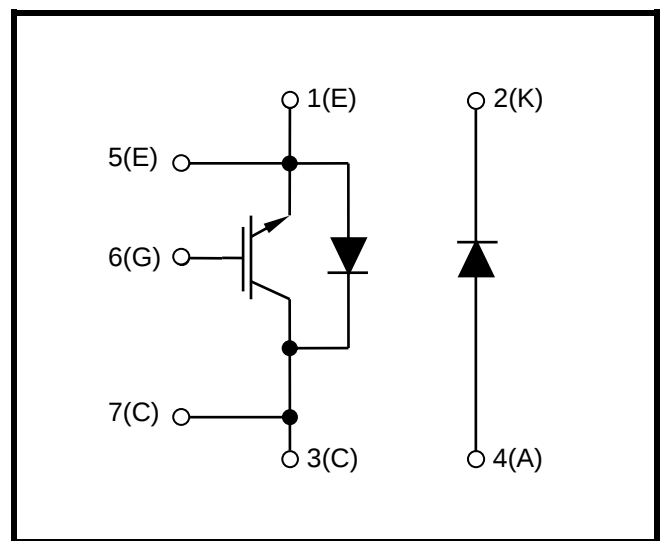
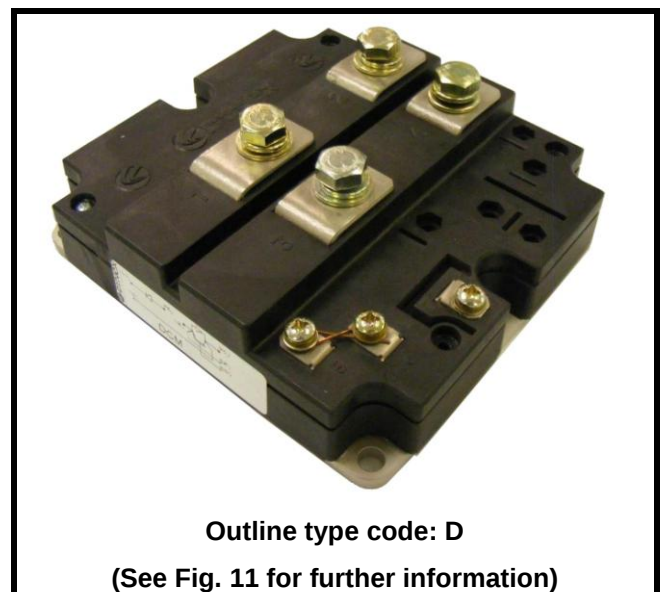


Fig. 1 Circuit configuration



Outline type code: D

(See Fig. 11 for further information)

Fig. 2 Package

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

$T_{\text{case}} = 25^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V_{CES}	Collector-emitter voltage	$V_{\text{GE}} = 0\text{V}$	1700	V
V_{GES}	Gate-emitter voltage		± 20	V
I_{C}	Continuous collector current	$T_{\text{case}} = 75^{\circ}\text{C}$	800	A
$I_{\text{C(PK)}}$	Peak collector current	1ms, $T_{\text{case}} = 110^{\circ}\text{C}$	1600	A
P_{max}	Max. transistor power dissipation	$T_{\text{case}} = 25^{\circ}\text{C}$, $T_{\text{j}} = 150^{\circ}\text{C}$	6940	W
I^2t	Diode I^2t value (IGBT arm)	$V_{\text{R}} = 0$, $t_{\text{p}} = 10\text{ms}$, $T_{\text{j}} = 125^{\circ}\text{C}$	120	kA^2s
	Diode I^2t value (Diode arm)		270	kA^2s
V_{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	4000	V
Q_{PD}	Partial discharge – per module	IEC1287, $V_1 = 1800\text{V}$, $V_2 = 1300\text{V}$, 50Hz RMS	10	pC

THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AlN
Baseplate material:	AlSiC
Creepage distance:	20mm
Clearance:	10mm
CTI (Comparative Tracking Index):	350

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$R_{\text{th(j-c)}}$	Thermal resistance – transistor (per arm)	Continuous dissipation – junction to case	-	-	18	$^{\circ}\text{C/kW}$
$R_{\text{th(j-c)}}$	Thermal resistance – diode (IGBT arm)	Continuous dissipation – junction to case	-	-	40	$^{\circ}\text{C/kW}$
	Thermal resistance – diode (Diode arm)				26.7	$^{\circ}\text{C/kW}$
$R_{\text{th(c-h)}}$	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	$^{\circ}\text{C/kW}$
T_{j}	Junction temperature	Transistor	-	-	150	$^{\circ}\text{C}$
		Diode	-	-	125	$^{\circ}\text{C}$
T_{stg}	Storage temperature range	-	-40	-	125	$^{\circ}\text{C}$
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}C$ unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I_{CES}	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}$			1	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ}C$			25	mA
I_{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			4	μA
$V_{GE(TH)}$	Gate threshold voltage	$I_C = 40mA, V_{GE} = V_{CE}$	4.5	5.5	6.5	V
$V_{CE(sat)}^{\dagger}$	Collector-emitter saturation voltage	$V_{GE} = 15V, I_C = 800A$		2.7	3.2	V
		$V_{GE} = 15V, I_C = 800A, T_j = 125^{\circ}C$		3.4	4.0	V
I_F	Diode forward current	DC		800		A
I_{FM}	Diode maximum forward current	$t_p = 1ms$		1600		A
$V_F^{\dagger}$	Diode forward voltage (IGBT arm)	$I_F = 800A$		2.2	2.5	V
	Diode forward voltage (Diode arm)			2.0	2.3	V
	Diode forward voltage (IGBT arm)	$I_F = 800A, T_j = 125^{\circ}C$		2.3	2.6	V
	Diode forward voltage (Diode arm)			2.0	2.3	V
C_{ies}	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		60		nF
Q_g	Gate charge	$\pm 15V$		9		μC
C_{res}	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$				nF
L_M	Module inductance – per arm			20		nH
R_{INT}	Internal transistor resistance – per arm			270		$\mu\Omega$
SC_{Data}	Short circuit current, I_{SC}	$T_j = 125^{\circ}C, V_{CC} = 1000V$ $t_p \leq 10\mu s, V_{GE} \leq 15V$ $V_{CE(max)} = V_{CES} - L^* \times di/dt$ IEC 60747-9		3200		A

Note:

† Measured at the power busbars, not the auxiliary terminals

* L is the circuit inductance + L_M

ELECTRICAL CHARACTERISTICS

 $T_{\text{case}} = 25^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(\text{off})}$	Turn-off delay time	$I_C = 800\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 900\text{V}$ $R_{G(\text{ON})} = 2.2\Omega$ $R_{G(\text{OFF})} = 2.2\Omega$ $L_S \sim 100\text{nH}$		1250		ns
t_f	Fall time			170		ns
E_{OFF}	Turn-off energy loss			230		mJ
$t_{d(\text{on})}$	Turn-on delay time			250		ns
t_r	Rise time			250		ns
E_{ON}	Turn-on energy loss			275		mJ
Q_{rr}	Diode reverse recovery charge	IGBT arm $I_F = 800\text{A}$ $V_{CE} = 900\text{V}$ $di_F/dt = 4000\text{A}/\mu\text{s}$		200		μC
I_{rr}	Diode reverse recovery current			460		A
E_{rec}	Diode reverse recovery energy			130		mJ
Q_{rr}	Diode reverse recovery charge	Diode arm $I_F = 800\text{A}$ $V_{CE} = 900\text{V}$ $di_F/dt = 4000\text{A}/\mu\text{s}$		250		μC
I_{rr}	Diode reverse recovery current			530		A
E_{rec}	Diode reverse recovery energy			160		mJ

 $T_{\text{case}} = 125^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(\text{off})}$	Turn-off delay time	$I_C = 800\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 900\text{V}$ $R_{G(\text{ON})} = 2.2\Omega$ $R_{G(\text{OFF})} = 2.2\Omega$ $L_S \sim 100\text{nH}$		1500		ns
t_f	Fall time			200		ns
E_{OFF}	Turn-off energy loss			360		mJ
$t_{d(\text{on})}$	Turn-on delay time			400		ns
t_r	Rise time			250		ns
E_{ON}	Turn-on energy loss			425		mJ
Q_{rr}	Diode reverse recovery charge	IGBT arm $I_F = 800\text{A}$ $V_{CE} = 900\text{V}$ $di_F/dt = 4000\text{A}/\mu\text{s}$		330		μC
I_{rr}	Diode reverse recovery current			530		A
E_{rec}	Diode reverse recovery energy			200		mJ
Q_{rr}	Diode reverse recovery charge	Diode arm $I_F = 800\text{A}$ $V_{CE} = 900\text{V}$ $di_F/dt = 4000\text{A}/\mu\text{s}$		425		μC
I_{rr}	Diode reverse recovery current			600		A
E_{rec}	Diode reverse recovery energy			250		mJ

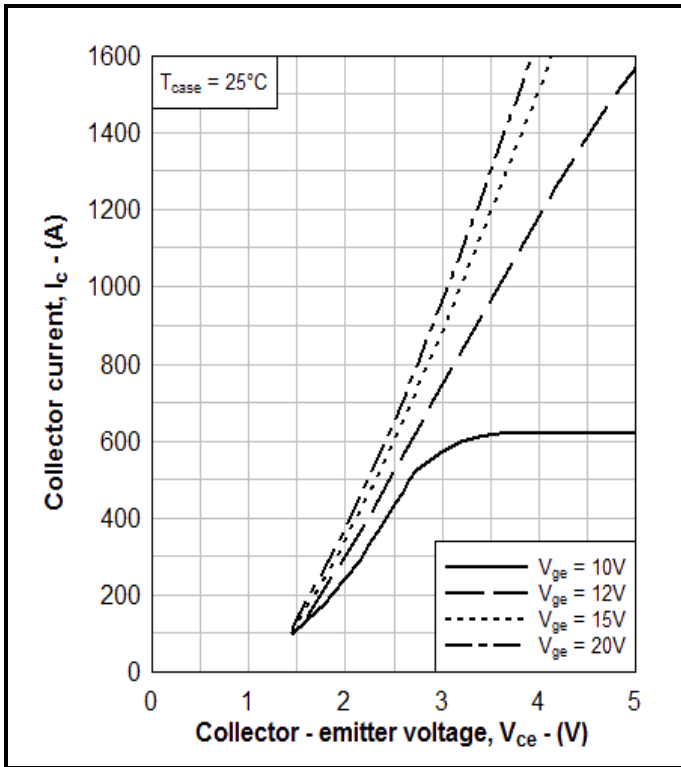


Fig. 3 Typical output characteristics

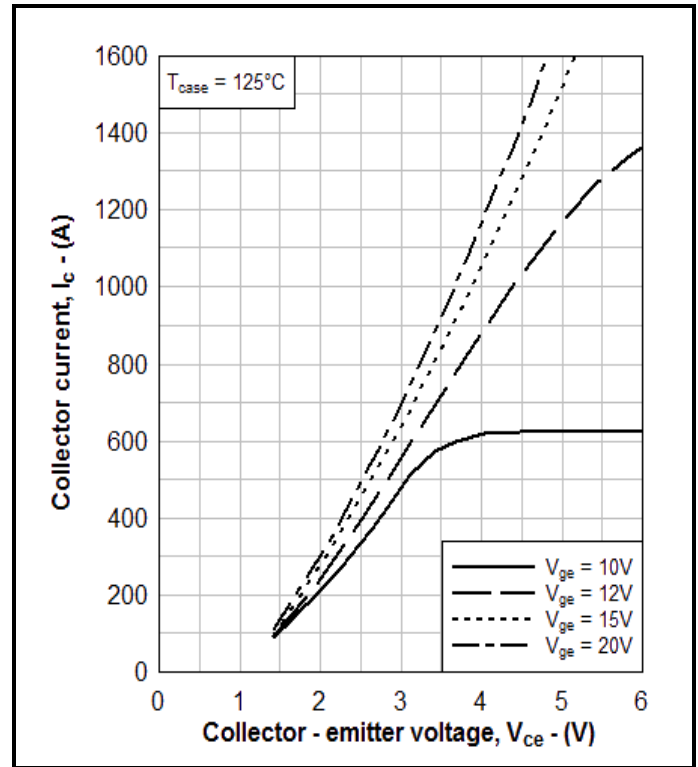


Fig. 4 Typical output characteristics

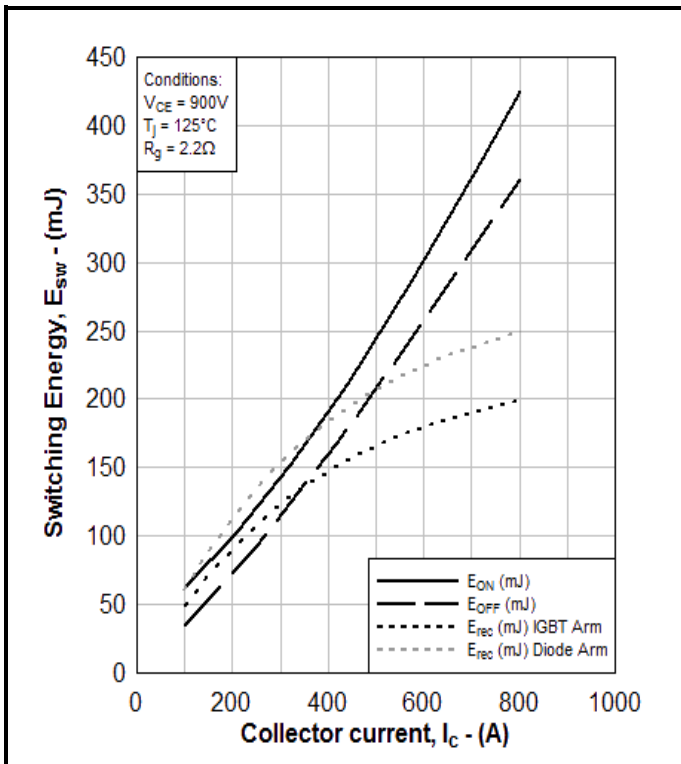


Fig. 5 Typical switching energy vs collector current

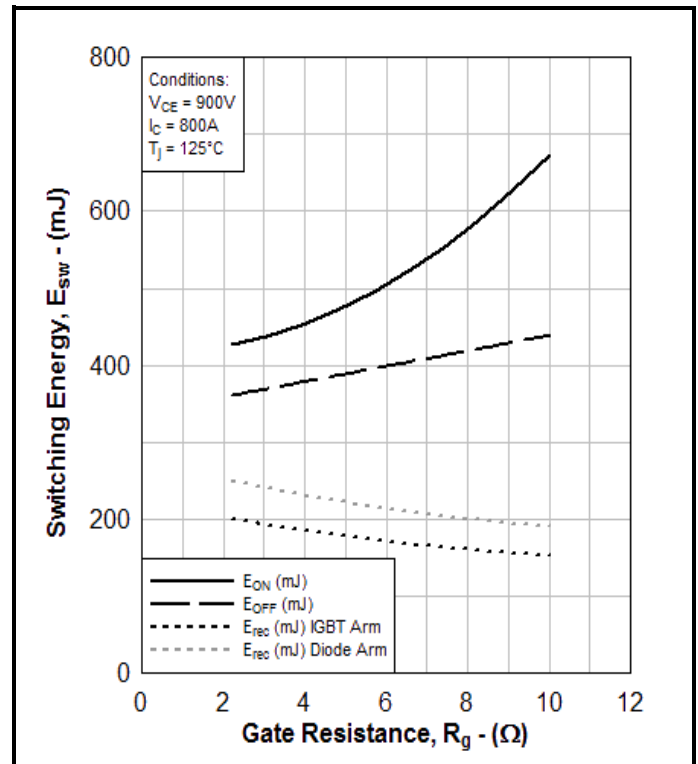


Fig. 6 Typical switching energy vs gate resistance

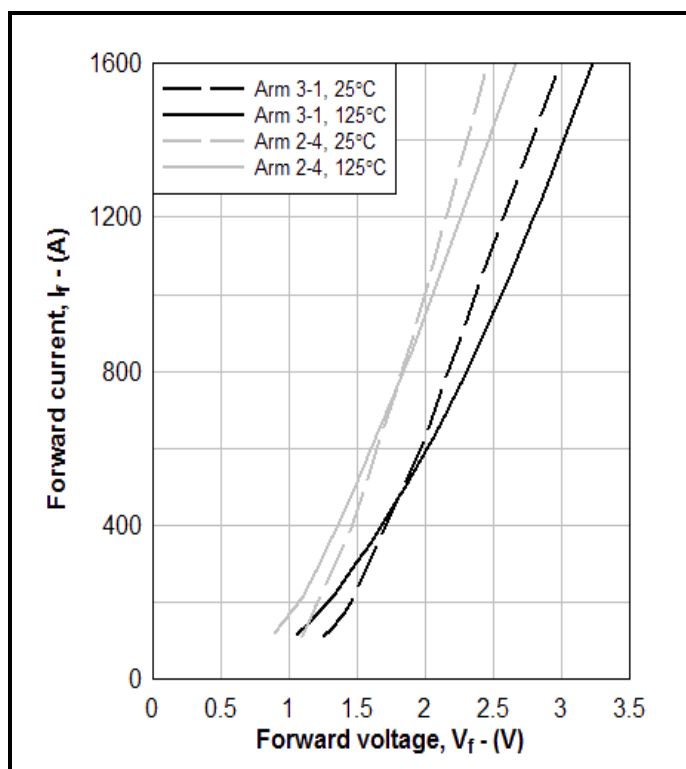


Fig. 7 Diode typical forward characteristics

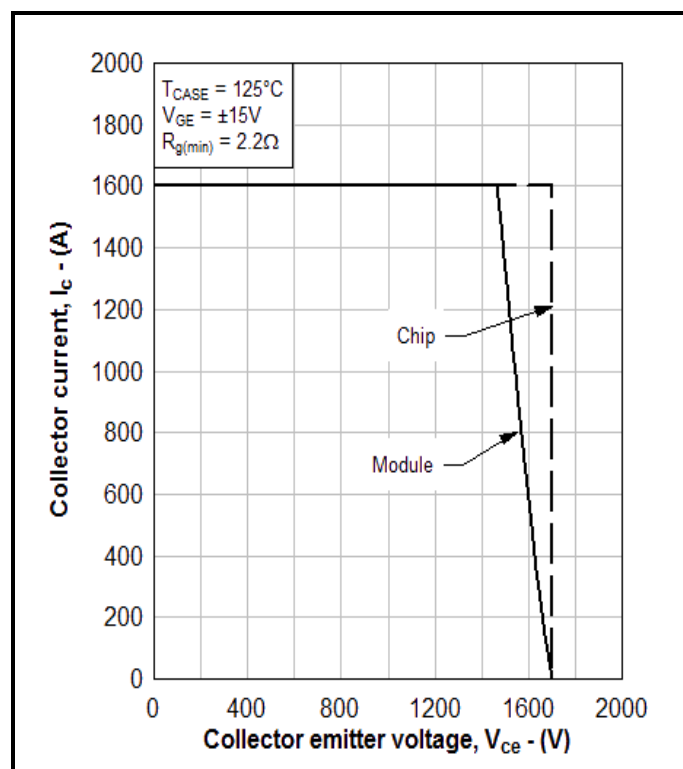


Fig. 8 Reverse bias safe operating area

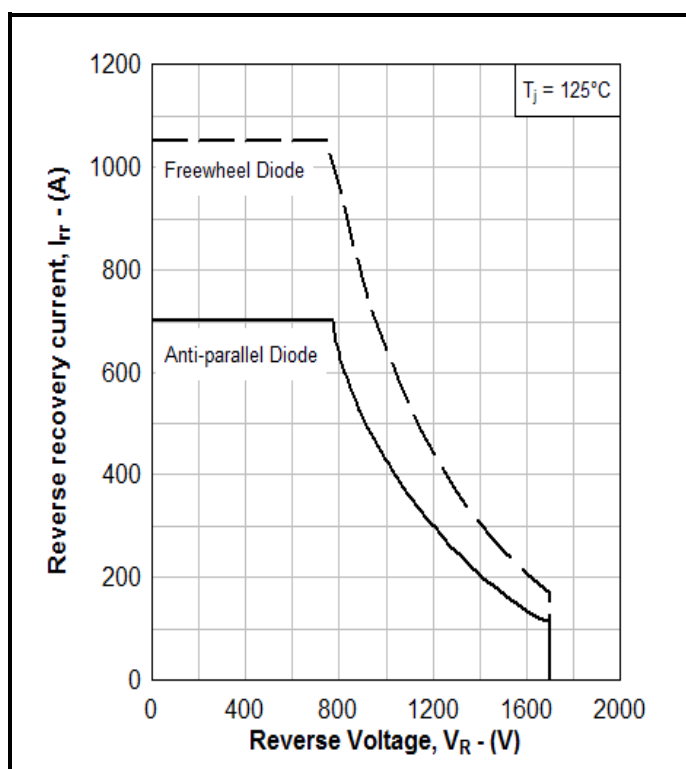


Fig. 9 Diode reverse bias safe operating area

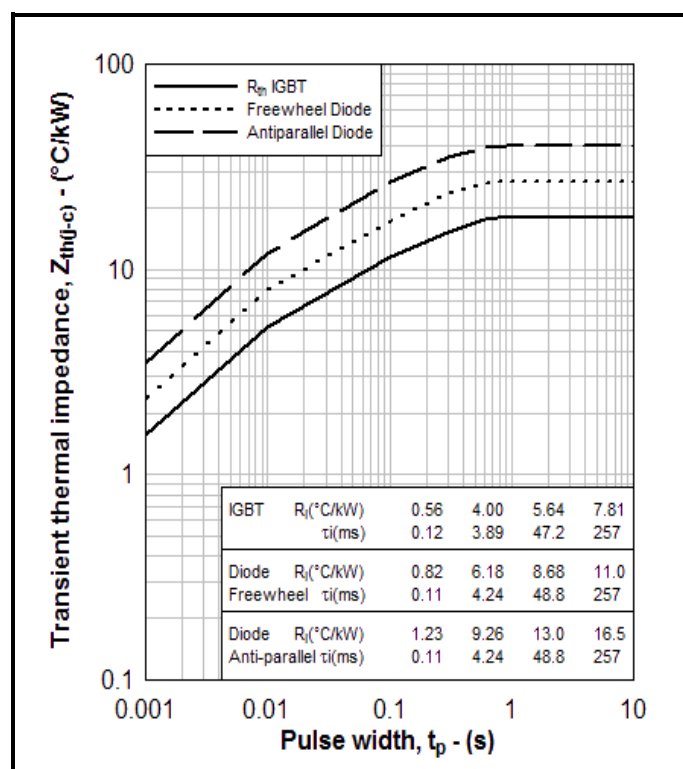


Fig. 10 Transient thermal impedance

For further package information, please visit our website or contact Customer Services.
All dimensions in mm, unless stated otherwise.
DO NOT SCALE.

Fig. 11 Module outline drawing

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