

# DIM600DCM17-A000

# **IGBT Chopper Module**

Replaces DS5491-4.2 DS5491-5 March 2011 (LN26753)

#### **FEATURES**

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Non Punch Through Silicon
- Isolated AISiC Base With AIN Substrates
- Lead Free Construction

#### **APPLICATIONS**

- High Reliability Inverters
- Motor Controllers
- Traction Drives

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 600V to 3300V and currents up to 2400A.

The DIM600DCM17-A000 is a 1700V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

### **ORDERING INFORMATION**

Order As:

## DIM600DCM17-A000

Note: When ordering, please use the complete part number

#### **KEY PARAMETERS**

$V_{CES}$		1700V
V <sub>CE(sat)</sub>	* (typ)	2.7V
l <sub>c</sub> ` ´	(max)	600A
I <sub>C(PK)</sub>	(max)	1200A

<sup>\*</sup> Measured at the power busbars, not the auxiliary terminals

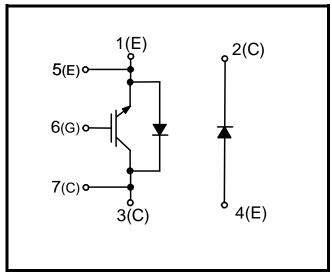


Fig. 1 Circuit configuration

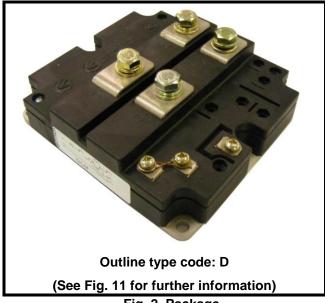


Fig. 2 Package



## **ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V	1700	V
V <sub>GES</sub>	Gate-emitter voltage		±20	V
I <sub>C</sub>	Continuous collector current	T <sub>case</sub> = 75°C	600	Α
I <sub>C(PK)</sub>	Peak collector current	1ms, T <sub>case</sub> = 110°C	1200	Α
P <sub>max</sub>	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	5200	W
l <sup>2</sup> t	Diode I <sup>2</sup> t value (IGBT arm)	$V_R = 0, t_p = 10 \text{ms}, T_j = 125^{\circ}\text{C}$		kA <sup>2</sup> s
''	Diode I <sup>2</sup> t value (Diode arm)			kA <sup>2</sup> s
V <sub>isol</sub>	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	4000	V
$Q_{PD}$	Partial discharge – per module	IEC1287, V <sub>1</sub> = 1800V, V <sub>2</sub> = 1300V, 50Hz RMS	10	рС

## THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

20mm

10mm

350

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R <sub>th(j-c)</sub>	Thermal resistance – transistor (per arm)	Continuous dissipation – junction to case		-	24	°C/kW
D	Thermal resistance – diode (IGBT arm)	Continuous dissipation –	-	-	40	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance – diode (Diode arm)	junction to case			40	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	ı	-	8	°C/kW
Tj	Junction temperature	Transistor	-	-	150	°C
		Diode		-	125	°C
T <sub>stg</sub>	Storage temperature range	-	-40	-	125	°C
		Mounting – M6	-	-	5	Nm
	Screw torque	Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm



## **ELECTRICAL CHARACTERISTICS**

 $T_{case}$  = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
	Collector cut-off current	$V_{GE} = 0V$ , $V_{CE} = V_{CES}$			1	mA
I <sub>CES</sub>		V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub> , T <sub>case</sub> = 125°C			20	mA
I <sub>GES</sub>	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			4	μA
V <sub>GE(TH)</sub>	Gate threshold voltage	$I_C = 40$ mA, $V_{GE} = V_{CE}$	4.5	5.5	6.5	V
\/ †	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 600A		2.7	3.2	V
$V_{CE(sat)}^{\dagger}$		V <sub>GE</sub> = 15V, I <sub>C</sub> = 600A, T <sub>j</sub> = 125°C		3.4	4.0	V
I <sub>F</sub>	Diode forward current	DC			600	Α
I <sub>FM</sub>	Diode maximum forward current	t <sub>p</sub> = 1ms			1200	Α
	Diode forward voltage (IGBT arm)	I <sub>F</sub> = 600A		2.2	2.5	V
	Diode forward voltage (Diode arm)			2.2	2.5	V
$V_F^{\dagger}$	Diode forward voltage	L COOA T 4050C		2.3	2.6	V
	Diode forward voltage (Diode arm)	I <sub>F</sub> = 600A, T <sub>j</sub> = 125°C		2.3	2.6	V
$C_{ies}$	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		45		nF
$Q_g$	Gate charge	±15V		6.8		μC
$C_{res}$	Reverse transfer capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz				nF
L <sub>M</sub>	Module inductance – per arm			20		nΗ
R <sub>INT</sub>	Internal transistor resistance – per arm			270		μΩ
$SC_Data$	Short circuit current, I <sub>SC</sub>	$T_{j} = 125^{\circ}C, V_{CC} = 1000V$ $t_{p} \le 10\mu s, V_{GE} \le 15V$ $V_{CE (max)} = V_{CES} - L^{*} x dl/dt$ IEC 60747-9		2400		A

## Note:

<sup>†</sup> Measured at the power busbars, not the auxiliary terminals L is the circuit inductance + L<sub>M</sub>



## **ELECTRICAL CHARACTERISTICS**

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
$t_{d(off)}$	Turn-off delay time			1200		ns
t <sub>f</sub>	Fall time	$I_{C} = 600A$ $V_{GE} = \pm 15V$		140		ns
E <sub>OFF</sub>	Turn-off energy loss	$V_{CE} = 900V$		190		mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = 3.3\Omega$		250		ns
t <sub>r</sub>	Rise time	$R_{G(OFF)} = 3.3\Omega$ $L_S \sim 100 nH$		250		ns
E <sub>ON</sub>	Turn-on energy loss	25 1001111		220		mJ
$Q_{rr}$	Diode reverse recovery charge	Diode arm		150		μC
I <sub>rr</sub>	Diode reverse recovery current	$I_{F} = 600A$ $V_{CF} = 900V$		350		Α
E <sub>rec</sub>	Diode reverse recovery energy	$dI_{F}/dt = 3000A/\mu s$		100		mJ

## T<sub>case</sub> = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
$t_{d(off)}$	Turn-off delay time			1500		ns
t <sub>f</sub>	Fall time	$I_C = 600A$ $V_{GF} = \pm 15V$		170		ns
E <sub>OFF</sub>	Turn-off energy loss	$V_{CE} = 900V$		270		mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = 3.3\Omega$ $R_{G(OFF)} = 3.3\Omega$ $L_S \sim 100 \text{nH}$		400		ns
t <sub>r</sub>	Rise time			250		ns
E <sub>ON</sub>	Turn-on energy loss			350		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	Diode arm		250		μC
I <sub>rr</sub>	Diode reverse recovery current	$I_F = 600A$ $V_{CE} = 900V$		400		Α
E <sub>rec</sub>	Diode reverse recovery energy	$v_{CE} = 900V$ dI <sub>F</sub> /dt = 3000A/µs		150		mJ



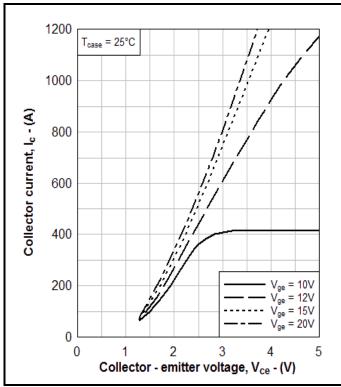


Fig. 3 Typical output characteristics

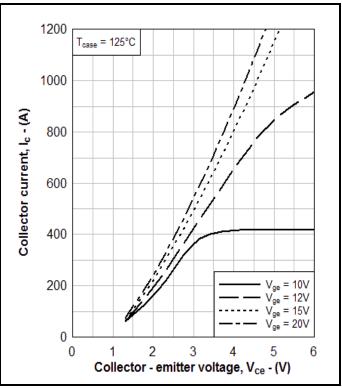


Fig. 4 Typical output characteristics

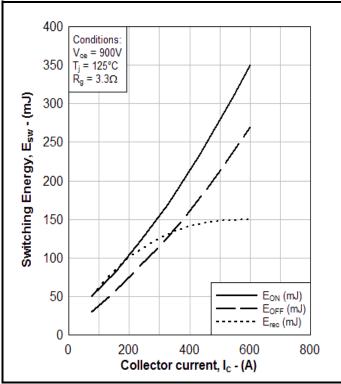


Fig. 5 Typical switching energy vs collector current

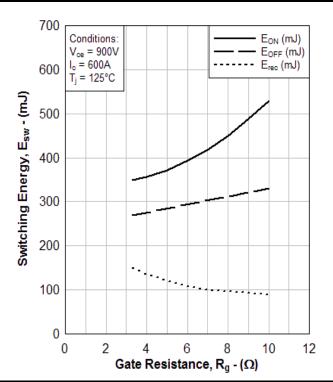


Fig. 6 Typical switching energy vs gate resistance



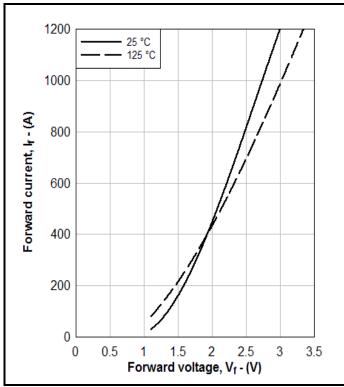


Fig. 7 Diode typical forward characteristics

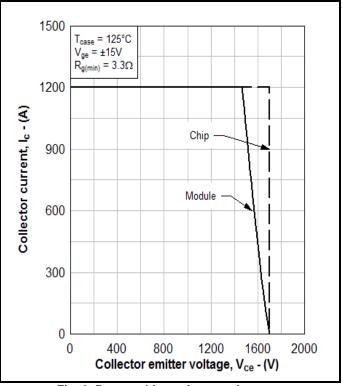


Fig. 8 Reverse bias safe operating area

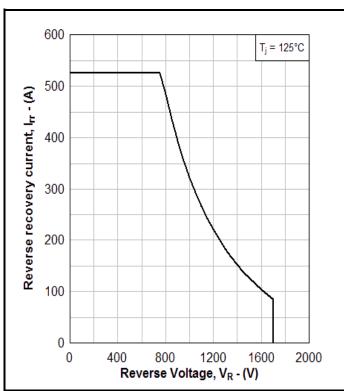


Fig. 9 Diode reverse bias safe operating area

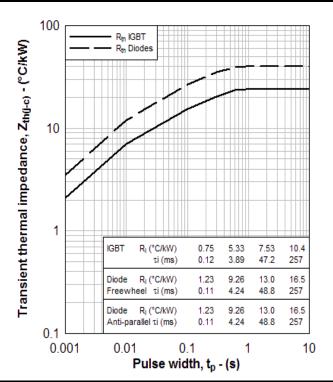


Fig. 10 Transient thermal impedance



## **PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

## DO NOT SCALE.

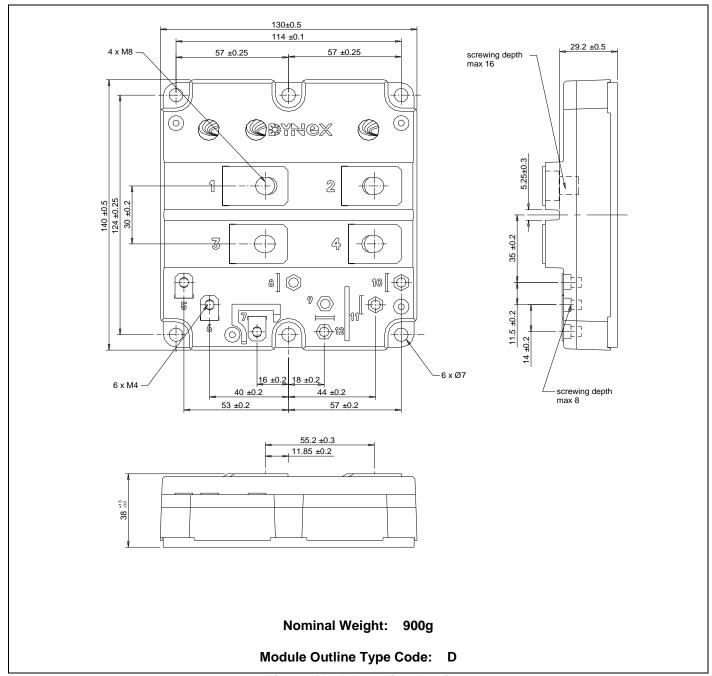


Fig. 11 Module outline drawing



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