The RF Line NPN Silicon High-Frequency Transistor

• Tape and reel packaging available for MRF3866R2: R2 suffix = 2,500 units per reel

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	30	Vdc
Collector-Base Voltage	VCBO	55	Vdc
Emitter-Base Voltage	VEBO	3.5	Vdc
Collector Current — Continuous	IC	0.4	Adc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	1.5 12	Watts mW/°C
Operating and Storage Junction Temperature Range	TJ, Tstg	-55 to +150	°C
Maximum Junction Temperature	T _{Jmax}	150	°C



MRF3866R2

CASE 751–05, STYLE 1 (SO–8)

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case	R _θ JC	83.3	°C/W
Thermal Resistance, Junction to Ambient	R _{θJA}	125	°C/W

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit				
OFF CHARACTERISTICS								
Collector–Emitter Breakdown Voltage ($I_C = 5.0 \text{ mAdc}, R_{BE} = 10 \Omega$)	V _(BR) CER	55	—	Vdc				
Collector–Emitter Sustaining Voltage ($I_C = 5.0 \text{ mAdc}, I_B = 0$)	V _{CEO(sus)}	30	—	Vdc				
Emitter–Base Breakdown Voltage (IE = 100 μ Adc, IC = 0)	V _{(BR)EBO}	3.5	—	Vdc				
Collector Cutoff Current (V _{CE} = 28 Vdc, I _B = 0)	ICEO		0.02	mAdc				
Collector Cutoff Current (V _{CE} = 30 Vdc, V _{BE} = -1.5 Vdc (Rev.), T _C = 150° C) (V _{CE} = 55 Vdc, V _{BE} = -1.5 Vdc (Rev.)	ICEX		5.0 0.1	mAdc				
Emitter Cutoff Current (V _{BE} = 3.5 Vdc, I _C = 0)	IEBO	_	0.1	mAdc				

(continued)





ELECTRICAL CHARACTERISTICS — continued ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Symbol Min		Unit			
ON CHARACTERISTICS							
DC Current Gain ($I_C = 360 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}$) (1) ($I_C = 50 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}$)	hFE	5.0 10		_			
Collector–Emitter Saturation Voltage ($I_C = 100 \text{ mAdc}, I_B = 20 \text{ mAdc}$)	VCE(sat)	—	1.0	Vdc			
SMALL-SIGNAL CHARACTERISTICS							
Current–Gain — Bandwidth Product (I _C = 50 mAdc, V _{CE} = 15 Vdc, f = 200 MHz)	ŕŢ	500	-	MHz			
Output Capacitance (V _{CB} = 28 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	_	3.0	pF			
FUNCTIONAL TEST			-				
Amplifier Power Gain (V _{CC} = 28 Vdc, P _{out} = 1.0 W, f = 400 MHz)	G _{pe}	10		dB			
Collector Efficiency (V _{CC} = 28 Vdc, P _{out} = 1.0 W, f = 400 MHz)	η	45	-	%			

NOTE:

1. Pulse Test: Pulse Width \leq 300 $\mu s,$ Duty Cycle \leq 2.0%.

VCE	lc	IC f (mA) (MHz)	S ₁₁		s ₂₁		\$ ₁₂		\$ ₂₂	
(Volts)	(mA)		s ₁₁	$\angle \phi$	s ₂₁	$\angle \phi$	S ₁₂	$\angle \phi$	S ₂₂	$\angle \phi$
15	50	100	0.67	-166	13.75	92	0.016	44	0.32	-27
		200	0.69	-176	6.93	81	0.024	53	0.30	-24
		300	0.70	177	4.57	73	0.032	57	0.32	-31
		400	0.71	172	3.38	67	0.042	59	0.34	-37
		500	0.72	168	2.66	61	0.049	59	0.37	-45
		600	0.72	164	2.17	54	0.056	61	0.40	-53
		700	0.72	160	1.85	49	0.061	63	0.43	-60
		800	0.72	155	1.61	44	0.068	65	0.47	-66
		900	0.71	151	1.40	39	0.075	64	0.50	-73
		1000	0.70	146	1.25	34	0.084	68	0.53	-79

Table 1. MRF3866R2 Common Emitter S–Parameters



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