

CYStech Electronics Corp.

Spec. No.: C330LA Issued Date: 2003.04.16

Revised Date : Page No. : 1/3

3.0Amp. Axial Leaded Schottky Barrier Diodes

1N582XLA Series

Features

- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications
- Plastic material used carries Underwriters Laboratory Flammability Classification 94V-0
- Low leakage current
- High surge capability
- High temperature soldering: 250°C/10 seconds at terminals
- High reliability

Mechanical Data

• Case: DO-201AD molded plastic.

• Terminals: Axial leads, solderable per MIL-STD-202 method 208

• Polarity: Indicated by cathode band.

• Weight: 1.10 gram

Maximum Ratings and Electrical Characteristics

(Rating at 25°C ambient temperature unless otherwise noted. Single phase, half wave, 60Hz, resistive or inductive load.)

		Symbol	Type			
Parameter	Conditions		1N5820	1N5821 1N5822		Units
			min	typ	max	
Repetitive peak reverse		Vrrm	20	30	40	V
voltage		V IXIXIVI	20			V
Maximum RMS voltage		Vrms	14	21	28	V
Maximum DC blocking		V_R	20	30	40	V
voltage		V K	20	30	70	•
Maximum instantaneous	IF=3A (Note 1)	$V_{\rm F}$	0.475	0.500	0.525	V
forward voltage	11 3/1 (Note 1)	V I	0.473	0.500	0.323	•
Maximum average forward		Io	3			Α
rectified current		10	3			7.1
Peak forward surge current	8.3ms single half sine wave superimposed on rated load(JEDEC method)	Ifsm	80			A
Maximum DC reverse	$V_R=V_{RRM}, T_A=25^{\circ}C \text{ (Note 1)}$	т_	2			mA
current	V _R =V _{RRM} ,T _A =125°C (Note 1)	IR	I _R 20			mA
Maximum thermal	Junction to ambient(Note 2)	Rth,JA	40			°C/w
resistance	C 13 411 1 1 1 1 411			250		Б
Diode junction capacitance	f=1MHz and applied 4V reverse voltage	CJ	250			pF
Storage temperature		Tstg	-65~+125			$^{\circ}\!\mathbb{C}$
Operating temperature		ТЈ	-65~+125			$^{\circ}\! C$

Notes: 1.Pulse test, pulse width=300 μ sec, 2% duty cycle

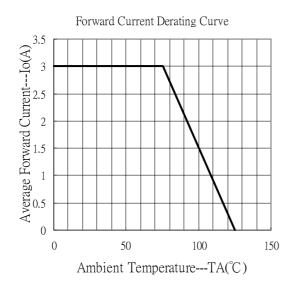


CYStech Electronics Corp.

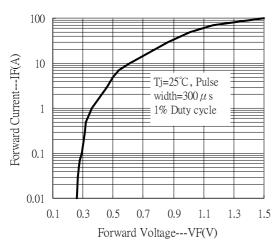
Spec. No.: C330LA Issued Date: 2003.04.16

Revised Date : Page No. : 2/3

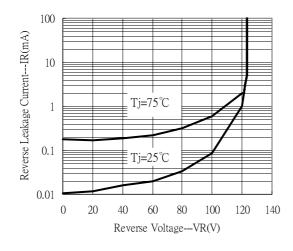
Characteristic Curves



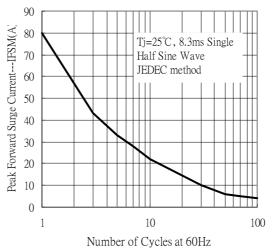
Forward Current vs Forward Voltage



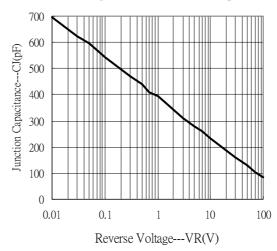
Reverse Leakage Current vs Reverse Voltage



Maximum Non-Repetitive Forward Surge Current



Junction Capacitance vs Reverse Voltage



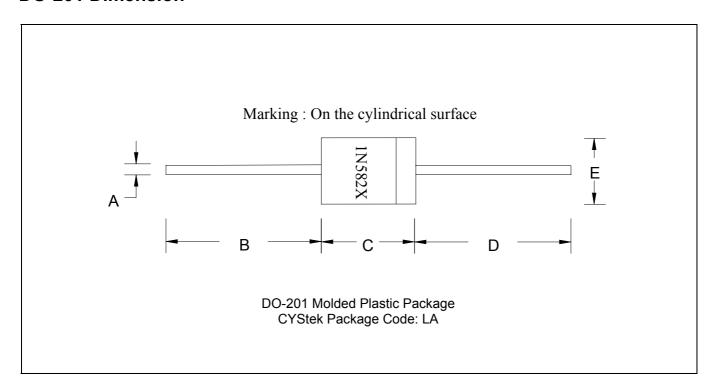


CYStech Electronics Corp.

Spec. No.: C330LA Issued Date: 2003.04.16

Revised Date : Page No. : 3/3

DO-201 Dimension



*:Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.	DIIVI	Min.	Max.	Min.	Max.
Α	0.0472	0.0512	1.20	1.30	D	1.0000	-	25.40	-
В	1.0000	-	25.40	-	Е	0.1890	0.2087	4.80	5.30
С	0.2835	0.3740	7.20	9.50					

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material. 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead : Axial leads, solderable per MIL-STD-202, Method 208 guaranteed.
- Mold Compound : Epoxy resin family, flammability solid burning class: UL94V-0

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.