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 U.S.A.

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FEATURES

- High Gain At Low Current $h_{FE} > 200 @ 10 \mu A$
- Low Output Capacitance $C_{obo} < 0.8 \text{ pF}$
- h_{FE} Match $h_{FE1} / h_{FE2} < 10\%$
- Tight V_{BE} Tracking
 $\Delta (V_{BE1} - V_{BE2}) < 3 \mu V / ^\circ C -55^\circ C \text{ to } +125^\circ C$
- Dielectrically isolated matched pairs for differential amplifiers.

**DUAL MONOLITHIC
 MATCHED NPN SILICON
 PLANAR TRANSISTORS**

**2N4044 2N4045 2N4100
 2N4878 2N4879 2N4880**

<p>GENERAL DESCRIPTION</p> <p>Dual monolithic matched NPN silicon planar transistors used for differential amplifier applications.</p>	<p>ORDERING INFORMATION</p> <table border="1" style="width:100%; border-collapse: collapse; text-align: center;"> <tr> <th>TO78</th> <th>TO71</th> <th>WAFER</th> <th>CHIP</th> </tr> <tr> <td>2N4044</td> <td></td> <td>2N4044/W</td> <td>2N4044/D</td> </tr> <tr> <td>2N4045</td> <td></td> <td>2N4045/W</td> <td>2N4045/D</td> </tr> <tr> <td>2N4100</td> <td></td> <td>2N4100/W</td> <td>2N4100/D</td> </tr> <tr> <td></td> <td>2N4878</td> <td></td> <td></td> </tr> <tr> <td></td> <td>2N4879</td> <td></td> <td></td> </tr> <tr> <td></td> <td>2N4880</td> <td></td> <td></td> </tr> </table>	TO78	TO71	WAFER	CHIP	2N4044		2N4044/W	2N4044/D	2N4045		2N4045/W	2N4045/D	2N4100		2N4100/W	2N4100/D		2N4878				2N4879				2N4880																																																																												
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