

TERMINAL TAIL
SEE NOTE 4

FITTING NAIL
SEE NOTE 4

8 CIRCUIT ASSEMBLY

6 CIRCUIT ASSEMBLY

PC BOARD LAYOUT FOR
8 POSITION HOUSING
(COMPONENT SIDE OF BOARD)

PC BOARD LAYOUT FOR
6 POSITION HOUSING
(COMPONENT SIDE OF BOARD)

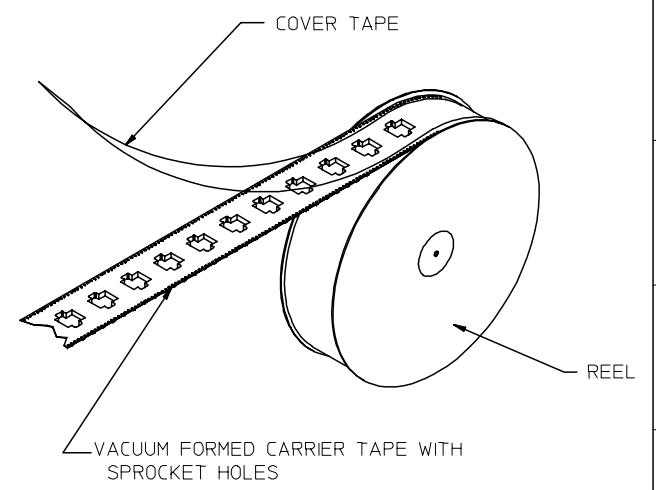
- NOTES:
- MATERIAL:
HOUSING & INSULATOR: NYLON(PA), GLASS FILLED, UL94V-0, COLOR: BLACK
TERMINALS: PHOSPHOR BRONZE, .012/(0.30) THICK
FITTING NAILS: BRASS, .010/(0.25) THICK
 - FINISH:
TERMINALS:
A = SELECT GOLD IN CONTACT AREA: 50 MICROINCHES MIN.
*SELECT TIN IN PC TAIL AREA: 100 MICROINCHES MIN.
WITH OVERALL NICKEL UNDERPLATE: 50 MICROINCHES MIN.
B = SELECT GOLD IN CONTACT AREA: 30 MICROINCHES MIN.
*SELECT TIN IN PC TAIL AREA: 100 MICROINCHES MIN.
WITH OVERALL NICKEL UNDERPLATE: 50 MICROINCHES MIN.
C = SELECT GOLD IN CONTACT AREA: 15 MICROINCHES MIN.
*SELECT TIN IN PC TAIL AREA: 100 MICROINCHES MIN.
WITH OVERALL NICKEL UNDERPLATE: 50 MICROINCHES MIN.
FITTING NAILS:
*100 MICROINCHES TIN OVER 50 MICROINCHES NICKEL UNDERPLATE
*THE PRIMARY SHIPPING CARTON WILL BE LABELED "COMPLIANT TO RoHS
DIRECTIVE 2002/95/EC AND ELV ANNEX II OF DIRECTIVE 2000/53/EC".
CARTONS WITHOUT THIS LABEL MAY CONTAIN PRODUCT WITH TIN-LEAD
IN THE PC TAILS AND/OR FITTING NAILS.
 - PRODUCT SPECIFICATION AND PROCESSING PARAMETERS: PS-43743-0001.
 - PACKAGING SPECIFICATION:
CONNECTOR ASSEMBLIES TO BE PACKAGED USING TAPE & REEL SYSTEM.
 - COPLANARITY SPECIFICATION:
WHEN AN ASSEMBLY IS PLACED UPON A LEVEL SURFACE, THE MAXIMUM
GAP BETWEEN THE LEVEL SURFACE AND ALL TERMINAL TAILS AND
BOTH FITTING NAILS IS .006/(0.15).
 - FOR ASSEMBLIES HAVING VOIDED CIRCUITS, THE TAIL PORTION IS CUT
AND REMOVED. THE MATING PORTION OF THE TERMINAL REMAINS INTACT.
VOIDED CIRCUITS MUST BE THE SAME FOR BOTH PARTS.
 - CONFORMS TO FCC REGULATION PART 68.5 FOR MODULAR JACKS.

FITTING NAIL PLATING	EC NO: UCP2004-1781	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)
	DR:WALLSCHMIDT 2004/03/19		mm INCH
G1	CHKD:LSCHMIDT 2004/03/19	DESCRIPTION	4 PLACES ± --- ± ---
	APPR:FSMITH 2004/03/23		3 PLACES ± --- ± .010
			2 PLACES ± 0.25 ± .015
			1 PLACE ± .38 ± ---
			ANGULAR ±1/2°
			DRAFT WHERE APPLICABLE
			MUST REMAIN WITHIN DIMENSIONS

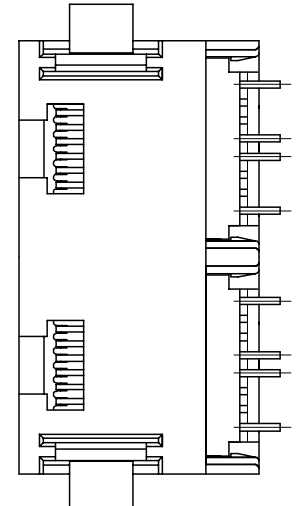
SCALE 4:1	DESIGN UNITS INCH	THIRD ANGLE PROJECTION	REVISE ON CAD ONLY
DIMENSION STYLE IN/MM		TITLE	
DRAWN BY PEARSON	DATE 01/07/97	4, 6 & 8 CIRCUIT DUAL PORT SMT MODJACK	
CHECKED BY MUELLER	DATE 01/07/97		
APPROVED BY EDGLEY	DATE 01/07/97	MATERIAL NO. SEE SHT 2	DOCUMENT NO. SD-43841-001
THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION		SHEET NO. 1 OF 2	

2	F
1	G1
SHT	REV

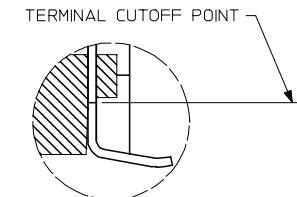
	13	12	11	10	9	8	7	6	5	4	3	2
	ASSEMBLY ITEM NO.	CONNECTOR SIZE	CIRCUIT SIZE	TERMINAL PLATING	TAPE & REEL PACKAGING NO.	VOIDED CIRCUITS						
J	43841-0001	8	4	A	PK-70873-7004	NA						
	43841-0002	8	6	A	PK-70873-7004	↑ ↓						
	43841-0003	8	8	A	PK-70873-7004							
I	43841-0004	6	4	A	PK-70873-7003							
	43841-0005	6	6	A	PK-70873-7003							
	43841-0006	8	4	B	PK-70873-7004							
H	43841-0007	8	6	B	PK-70873-7004							
	43841-0008	8	8	B	PK-70873-7004							
	43841-0009	6	4	B	PK-70873-7003							
	43841-0010	6	6	B	PK-70873-7003							
G	43841-0011	8	4	C	PK-70873-7004							
	43841-0012	8	6	C	PK-70873-7004							
	43841-0013	8	8	C	PK-70873-7004							
	43841-0014	6	4	C	PK-70873-7003							
F	43841-0015	6	6	C	PK-70873-7003							
	43841-0016	6	2	A	PK-70873-7003							
	43841-0017	6	2	B	PK-70873-7003							
	43841-0018	6	2	C	PK-70873-7003		NA					



TAPE AND REEL PACKAGING



TYPICAL VIEW SHOWING VOIDED CIRCUIT (CIRCUITS 2,3,6,7 SHOWN VOIDED)



VIEW SHOWING TYPICAL TERMINAL CUTOFF OF TAIL (SEE NOTE 5) FOR VOIDED CIRCUITS.

HOUSING SIZE	CIRCUIT SIZE	DIM. A
6	6	.250 (6.35)
6	4	.150 (3.81)
6	2	.050 (1.27)
8	8	.350 (8.89)
8	6	.250 (6.35)
8	4	.150 (3.81)

SEE SHEET ONE EC NO: UCP2004-0798 TDRW:LSCHMIDT 2004/01/05 CHKD:LSCHMIDT 2004/01/05 APPR:FSMITH 2004/01/08	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)	SCALE 4:1	DESIGN UNITS INCH	THIRD ANGLE PROJECTION	REVISE ON CAD ONLY
	▽ = 0 ▽ = 0	4 PLACES ± .010 3 PLACES ± .015 2 PLACES ± 0.25 ± .015 1 PLACE ± 0.38 ± .015 ANGULAR ± 1/2°	DIMENSION STYLE IN/MM		TITLE	
			DRAWN BY PEARSON DATE 01/07/98		4, 6 & 8 CIRCUIT DUAL PORT SMT MODJACK	
			CHECKED BY PEARSON DATE 01/07/98		MOLEX INCORPORATED	
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		APPROVED BY EDGLEY DATE 01/07/98	MATERIAL NO. SEE CHART	DOCUMENT NO. SD-43841-001	SHEET NO. 2 OF 2	
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