



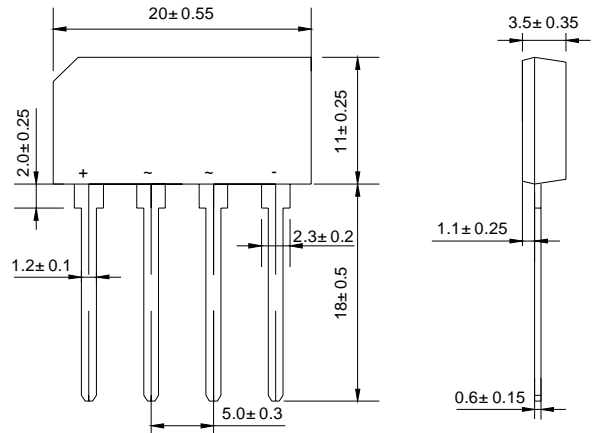
VOLTAGE RANGE: 50 --- 1000 V

CURRENT: 4.0 A

Features

- ◇ Ideal for printed circuit board
- ◇ Reliable low cost construction utilizing molded plastic technique
- ◇ Plastic material has U/L flammability classification 94V-0
- ◇ Mounting position: Any
- ◇ Glass passivated chip junctions

KBJ2



Dimensions in millimeters

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate by 20%.

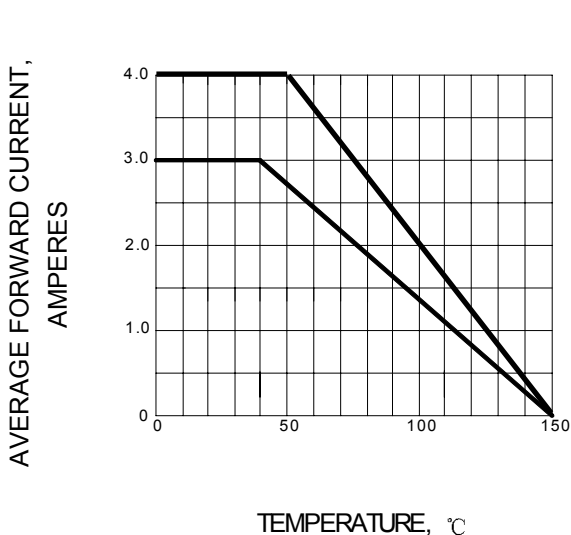
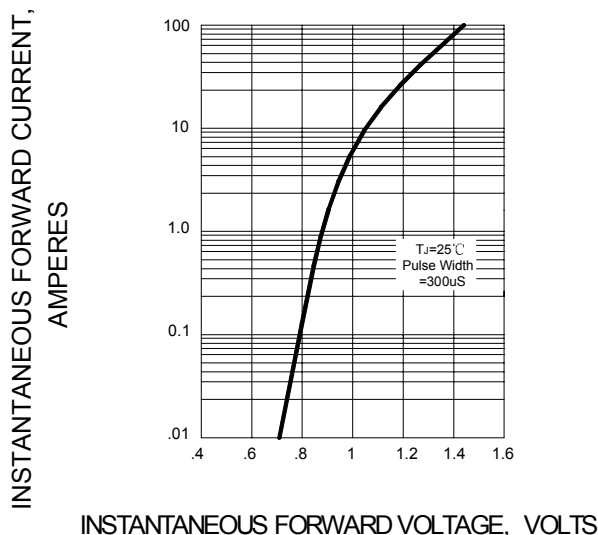
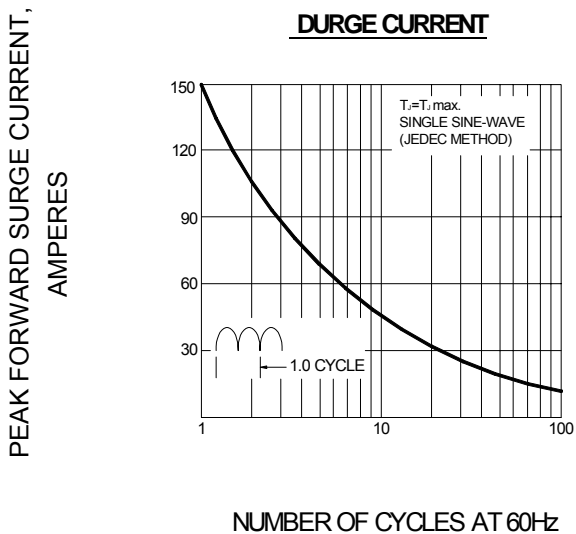
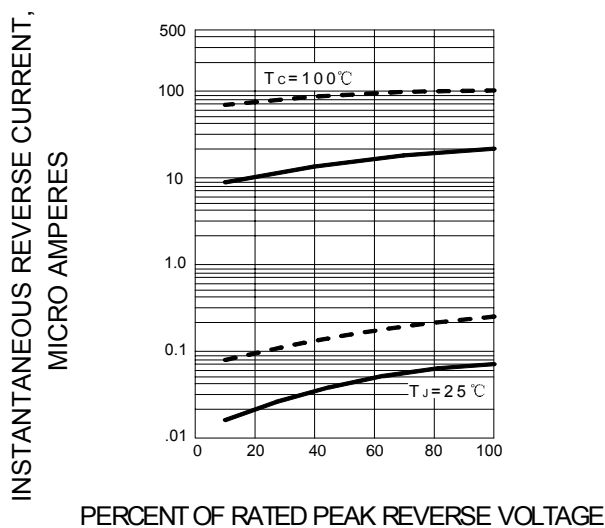
		GBL 4005	GBL 401	GBL 402	GBL 404	GBL 406	GBL 408	GBL 410	UNITS
Maximum recurrent peak reverse voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum average forward output current @ $T_c=50^\circ C$ @ $T_J=40^\circ C$	$I_{F(AV)}$	4.0 3.0							A
Peak forward surge current 8.3ms single half-sine-wave superimposed on rated load	I_{FSM}	150.0							A
Maximum instantaneous forward voltage at 2.0 A	V_F	1.0							V
Maximum reverse current @ $T_A=25^\circ C$ at rated DC blocking voltage @ $T_C=100^\circ C$	I_R	5.0 500.0							μA
Typical junction capacitance per leg (note 1)	C_J	65				25			pF
Typical thermal resistance per leg (note 2) (note 3)	$R_{\theta JL}$ $R_{\theta JL}$	34 15							$^\circ C/W$
Operating junction temperature range	T_J	- 55 ---- + 150							$^\circ C$
Storage temperature range	T_{STG}	- 55 ---- + 150							$^\circ C$

NOTE: 1. Measured at 1.0 MHz and applied reverse voltage of 4.0 volts.

2. Units mounted on P.C.B. with 0.5x0.5" (12x12mm) copper pads and 0.375" (9.5mm) lead length.

3. Unit case mounted on 3.0x3.0x0.11" thick (7.5x7.5x0.3cm) Al. Plate.

Ratings AND Characteristic Curves

FIG.1 – DERATING CURVE FOR OUTPUT RECTIFIED CURRENT

FIG.2 – TYPICAL FORWARD CHARACTERISTIC

FIG.3 – MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

FIG.4 – TYPICAL REVERSE CHARACTERISTIC

FIG.5 – TYPICAL JUNCTION CAPACITANCE PER LEG
