Vishay High Power Products

Insulated Ultrafast Rectifier Module, 200 A

FEATURES

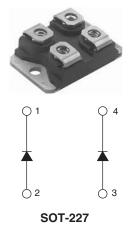
- Two fully independent diodes
- Ceramic fully insulated package (V_{ISOL} = 2500 V_{AC})
- Ultrafast reverse recovery
- · Ultrasoft reverse recovery current shape
- Low forward voltage
- Optimized for power conversion: welding and industrial SMPS applications
- Industry standard outline
- · Plug-in compatible with other SOT-227 packages
- · Easy to assemble
- Direct mounting to heatsink
- Totally lead (Pb)-free
- Designed and qualified for industrial level

DESCRIPTION

The UFB200FA60P insulated modules integrate two state of the art Vishay's ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The planar structure of the diodes, and the platinum doping life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness, and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be a predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, and dc-to-dc converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R		600	V	
Continuous forward current per diode	١ _F	T _C = 85 °C	126	•	
Single pulse forward current per diode	I _{FSM}	T _C = 25 °C	1000	A	
Maximum power dissipation per module	PD	T _C = 85 °C	360	W	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V	
Operating junction and storage temperatures	T _J , T _{Stg}		- 55 to 175	°C	



PRODUCT SUMMARY				
V _R	600 V			
I _{F(AV)} per module at T _C = 92 °C	200 A			
t _{rr}	83 ns			

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ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA		600	-	-	
Fernandushasa	N	I _F = 100 A		-	1.46	1.78	
		I _F = 200 A		-	1.7	2.05	V
Forward voltage	V _{FM}	I _F = 100 A	T _J = 125 °C	-	1.23	1.52	-
		I _F = 200 A		-	1.5	1.78	
December 1 and a second s	$V_R = V_R$ rated		-	0.1	100	μA	
Reverse leakage current	I _{RM}	$T_J = 175 \text{ °C}, V_R = V_R \text{ rated}$		-	0.3	1.0	mA
Junction capacitance	CT	V _R = 600 V - 80 -		pF			

DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse receiver time	ecovery time t _{rr}	T _J = 25 °C	I _F = 50 A dI _F /dt = 200 A/μs V _R = 200 V	-	83	108	ns
neverse recovery time		T _J = 125 °C		-	182	235	
Deck receivery current	recovery current	T _J = 25 °C		-	7	10	A
Peak recovery current		T _J = 125 °C		-	18	22	
Reverse recovery charge Q _r	0	T _J = 25 °C		-	290	540	nC
	Q _{rr}	T _J = 125 °C		-	1595	2585	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting			-	-	0.5	
Junction to case, both leg conducting	– R _{thJC}		-	-	0.25	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque			-	1.3	-	N · m



1000

100

10

1

Tj = 175°C

Tj = 125°C

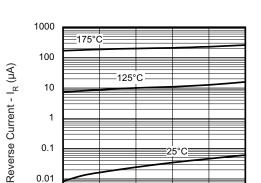
Tj = 25°C

(Per Diode)

Instantaneous Forward Current - I_F (A)

UFB200FA60P

Insulated Ultrafast

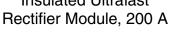


0.01

0.001

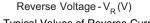
100

200



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0.2 0.4 0.6 0.8 1.0 1.2 1.4 1.6 1.8 2.0 2.2 Forward Voltage Drop - V_F(V) Fig. 1 - Typical Forward Voltage Drop Characteristics



400

500

600

300

Fig. 2 - Typical Values of Reverse Current vs. **Reverse Voltage**

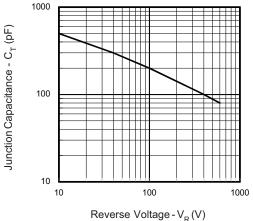


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

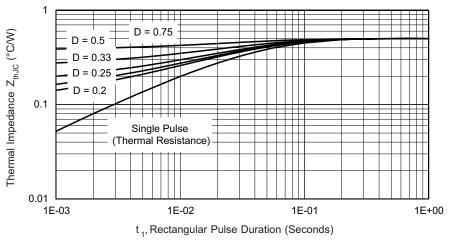


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Diode)

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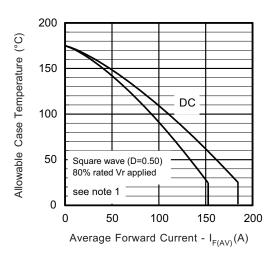


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

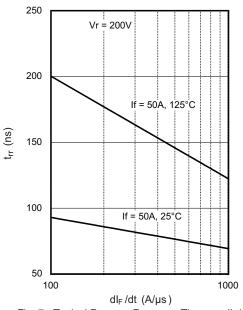


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

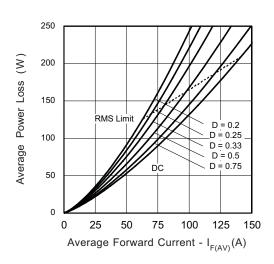


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; $I_R at V_{R1} = 80 \%$ rated V_R

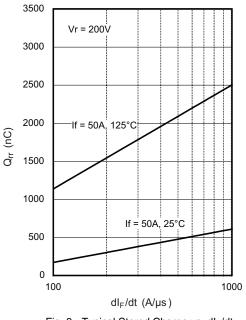
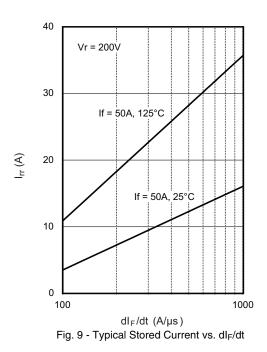


Fig. 8 - Typical Stored Charge vs. dl_F/dt



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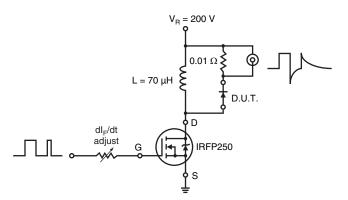
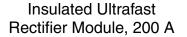
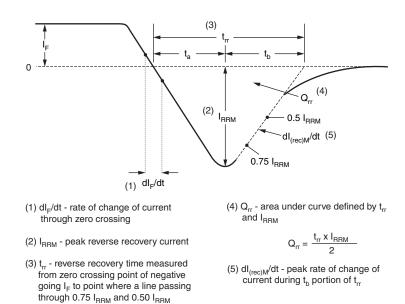


Fig. 10 - Reverse Recovery Parameter Test Circuit

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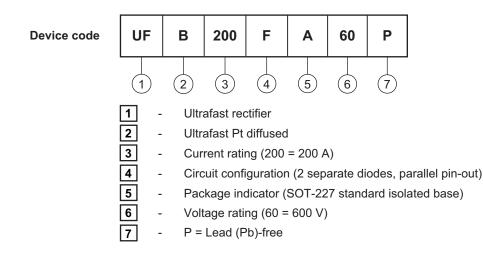






extrapolated to zero current. Fig. 11 - Reverse Recovery Waveform and Definitions





LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95036				
Packaging information	http://www.vishay.com/doc?95037			



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