

FEATURES

- Frequency Range: 1200 - 1200 MHz
- Step Size: 1000 KHz
- PLL-24 - Style Package

APPLICATIONS

- Basestations
- Satellite Communications

PERFORMANCE SPECIFICATIONS

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	1200 - 1200	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-103	dBc/Hz
Harmonic Suppression (2nd, typ.)	-15	dBc
Sideband Spurs (typ.)	-65	dBc
Power Output	0±3	dBm
Load Impedance	50	Ω
Step Size	1000	kHz
Charge Pump Output Current	5000	μA
Switching Speed (typ., adjacent channel)	2	mSec
Startup Lock Time (typ.)	2	mSec
Operating Temperature Range	0 to 60	°C
Package Style	PLL-24	

POWER SUPPLY REQUIREMENTS

Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	33	mA

All specifications are typical unless otherwise noted and subject to change without notice.

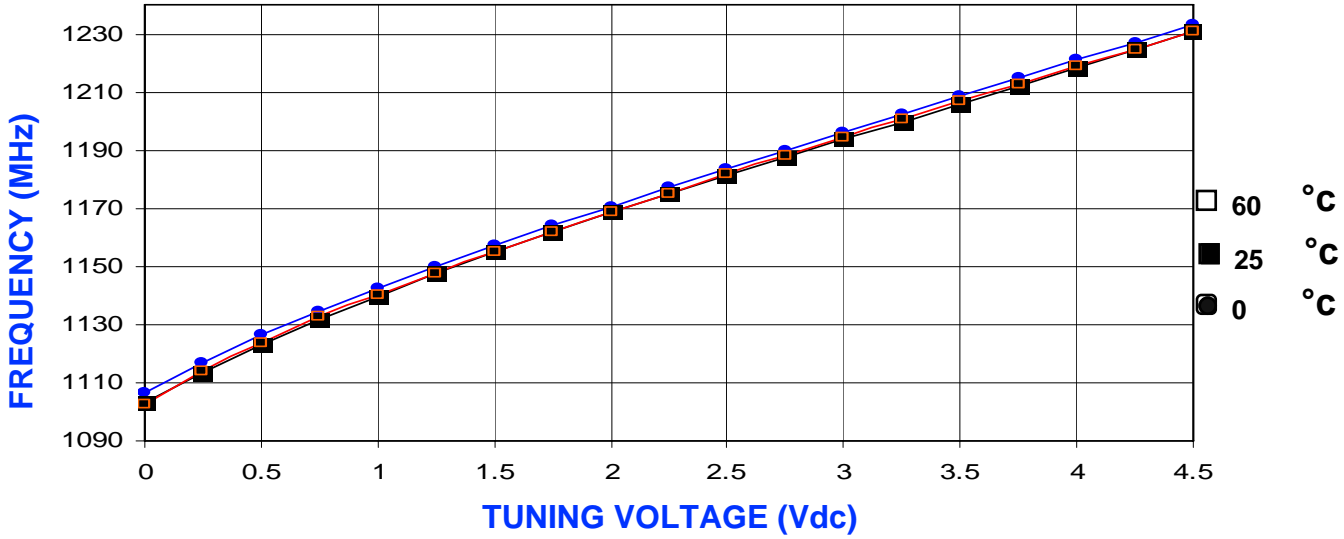
APPLICATION NOTES

- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-202 : PLL Functional Description

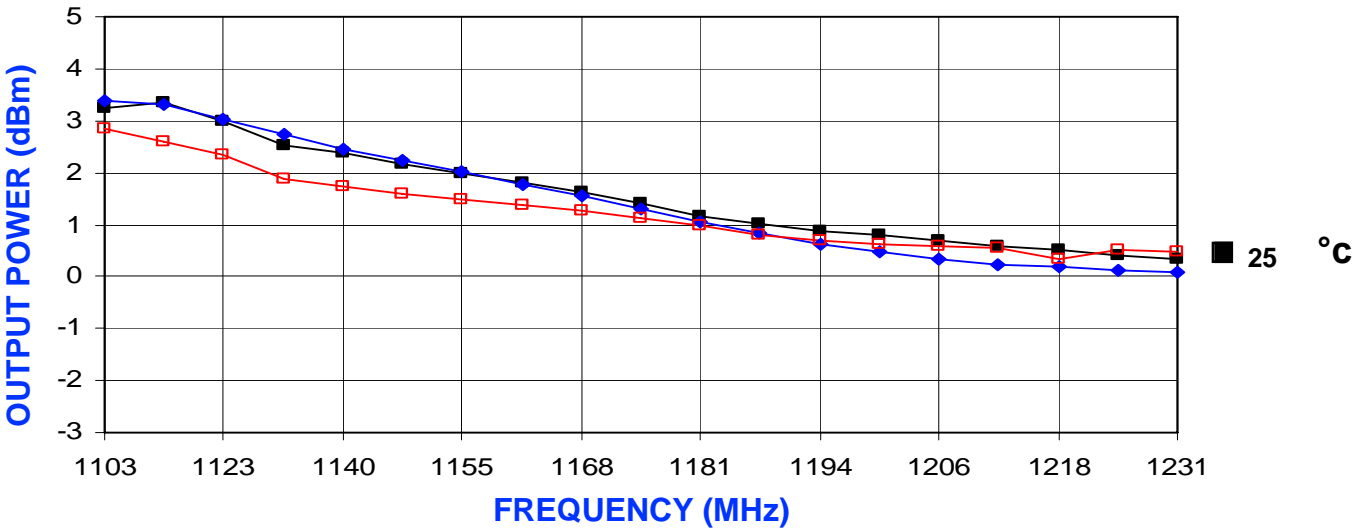
NOTES:

Reference Oscillator Signal: $5 \text{ MHz} < f_{\text{osc}} < 100 \text{ MHz}$
 Frequency Synthesizer: Analog Devices - ADF4106

VCO TUNING CURVE, typ.



VCO POWER CURVE, typ.



PHYSICAL DIMENSIONS

