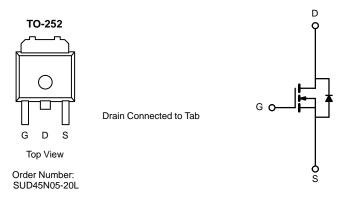


SUD45N05-20L

N-Channel 50 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	
50	0.018 @ V _{GS} = 10 V	±30	
	0.020 @ V _{GS} = 4.5 V	±30	



N-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	50	V	
Gate-Source Voltage		V _{GS}	±20		
Continuous Drain Current ^a	T _C = 25°C	1-	±30		
Continuous Diain Curient-	T _C = 100°C	I _D	±30		
Pulsed Drain Current		I _{DM}	±100	А	
Continuous Source Current (Diode Conduction) ^a		I _S	43		
Avalanche Current		I _{AR}	37		
Repetitive Avalanche Energy (Duty Cycle ≤ 1%)	L = 0.1 mH	E _{AR}	93	mJ	
Maximum Power Dissipation	T _C = 25°C	P _D	75	w	
waximum rower dissipation	T _A = 25°C	rb —	2.5 ^a	• • • • • • • • • • • • • • • • • • • •	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Maximum Junction-to-Ambient	Free Air, FR4 Board Mount	P	60		
	Free Air, Vertical Mount	- R _{thJA}	110	°C/W	
Maximum Junction-to-Case		R _{thJC}	2.0		

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<sup>a. Package limited.
b. Surface Mounted on FR4 Board, t ≤ 10 sec.</sup>



SUD45N05-20L N-Channel

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Parameter	Symbol	Test Condition	Min	Typ ^a	Max	Unit	
Static	•		•		•		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	50			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.0	2.0			
Gate-Body Leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V			± 100	nA	
Zero Gate Voltage Drain Current		V _{DS} = 50 V, V _{GS} = 0 V			1		
	I _{DSS}	V _{DS} = 50 V, V _{GS} = 0 V, T _J = 125°C			50	μΑ	
		V _{DS} = 50 V, V _{GS} = 0 V, T _J = 175°C			150		
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	43			Α	
Drain-Source On-State Resistance ^b		V _{GS} = 10 V, I _D = 20 A			0.018		
		$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 125^{\circ}\text{C}$			0.036		
	r _{DS(on)}	$V_{GS} = 10 \text{ V}, I_D = 43 \text{ A}, T_J = 125^{\circ}\text{C}$			0.040	Ω	
		$V_{GS} = 4.5 \text{ V}, I_D = 43 \text{ A}$			0.020		
Forward Transconductanceb	9 _{fs}	V _{DS} = 15 V, I _D = 43 A	20			S	
Dynamic ^a			•				
Input Capacitance	C _{iss}			1800	3600	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		370			
Reverse Transfer Capacitance	C _{rss}			130			
Total Gate Charge ^c	Qg			43	60	nC	
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 25 \text{ V}, \ V_{GS} = 10 \text{ V}, \ I_D = 43 \text{ A}$		7			
Gate-Drain Charge ^c	Q _{gd}			10			
Turn-On Delay Time ^c	t _{d(on)}			10	20	ns	
Rise Time ^c	t _r	V_{DD} = 25 V, R_L = 0.6 Ω I_D \cong 43 A, V_{GEN} = 10 V, R_G = 2.5 Ω		10	20		
Turn-Off Delay Time ^c	t _{d(off)}			32	60		
Fall Time ^c	t _f			7	15		
Source-Drain Diode Ratings a	nd Characteristi	ic (T _C = 25°C)					
Pulsed Current	I _{SM}				43	А	
Diode Forward Voltageb	V _{SD}	I _F = 43 A, V _{GS} = 0 V			1.5	V	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 43 A, di/dt = 100 A/μs		49	100	ns	

Notes

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width $\leq 300 \,\mu\text{s}$, duty cycle $\leq 2\%$.
- c. Independent of operating temperature.

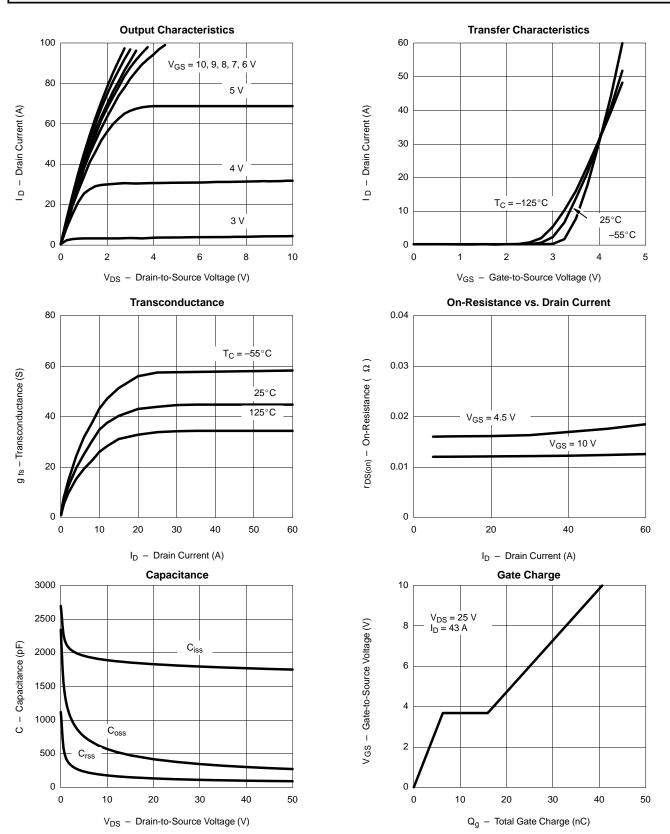
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N-Channel 50 V (D-S) 175 °C MOSFET

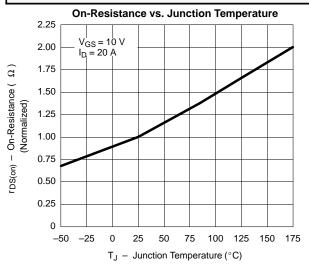
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

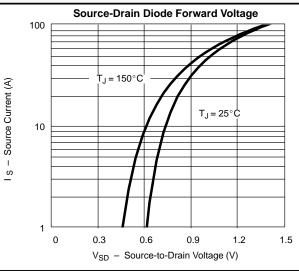


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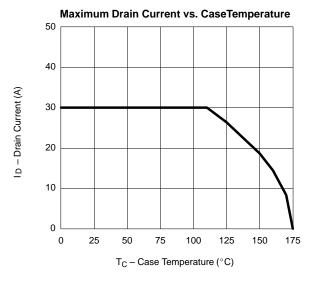
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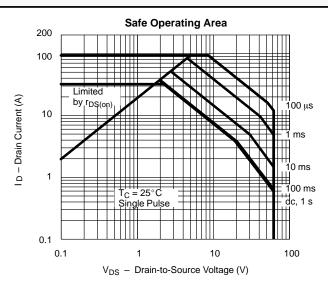
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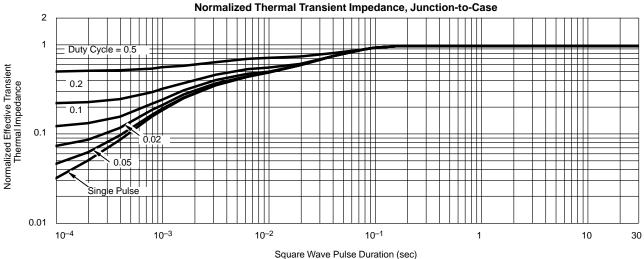




THERMAL RATINGS







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SUD45N05-20L N-Channel 50 V (D-S) 175 °C MOSFET

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