

## Memory Module Specification

## KVR100X64SC2/128 100MHz SDRAM 144-Pin SODIMM Memory Module

## **DESCRIPTION:**

This document describes ValueRAM's CL2 16M x 64-bit (128MB) SDRAM (Synchronous DRAM) PC100 Small Outline DIMM memory module. The components on this module include eight 8M x 16-bit (2M x 16-bit x 4 Bank / PC100 components) 4K Refresh SDRAM in TSOP packages. This 144-pin SODIMM uses gold contact fingers and requires +3.3V. The electrical and mechanical specifications are as follows:

## **FEATURES:**

	CLK Cycle Time (tCC)	min. = $10$ ns (CAS Latency = $2 / \text{max.} = 1000$ ns
$ \underline{\checkmark} $	Row Cycle Time (tRC)	70ns
$ \underline{\checkmark} $	CLK to Output (tSAC)	max. = 6ns (CAS Latency = 2)
$ \underline{\checkmark} $	Single Power Supply of	+3.3V (+/3V)
	Power	2.244 W (operating)
$   \sqrt{} $	UL Rating	94 V - 0

**Note:** The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.250" (31.75mm).

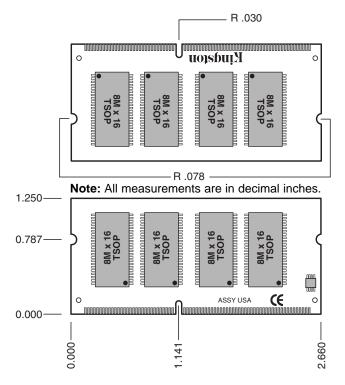


Figure 1: KVR100X64SC2/128 128MB 144-Pin SDRAM Module