

Single N-channel MOSFET

ELM34414AA-N

■General description

ELM34414AA-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■Features

- $V_{ds}=30V$
- $I_d=15A$
- $R_{ds(on)} < 8m\Omega$ ($V_{gs}=10V$)
- $R_{ds(on)} < 12m\Omega$ ($V_{gs}=4.5V$)

■Maximum absolute ratings

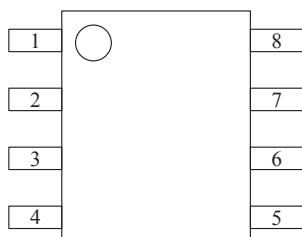
Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	V_{ds}	30	V	
Gate-source voltage	V_{gs}	± 20	V	
Continuous drain current Ta=25°C	I_d	15	A	3
Ta=90°C		12		
Pulsed drain current	I_{dm}	50	A	3
Power dissipation Ta=25°C	P_d	2.5	W	
Ta=90°C		2.0		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C	

■Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	Steady-state	$R_{\theta ja}$		50	°C/W	

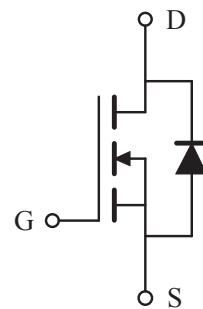
■Pin configuration

SOP-8(TOP VIEW)



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

■Circuit



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■Electrical characteristics

T_a=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BV _{dss}	Id=250μA, V _{gs} =0V	30			V	
Zero gate voltage drain current	Id _{ss}	V _{ds} =24V, V _{gs} =0V			1	μA	
		V _{ds} =20V, V _{gs} =0V, T _j =55°C			10		
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±20V			±100	nA	
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , Id=250μA	1.0	1.5	3.0	V	
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =10V, Id=15A		6.8	8.0	mΩ	1
		V _{gs} =4.5V, Id=12A		8.8	12.0	mΩ	
Forward transconductance	G _{fs}	V _{ds} =15V, Id=15A		60		S	1
Diode forward voltage	V _{sd}	I _f =3A, V _{gs} =0V			1.1	V	1
Max. body-diode continuous current	I _s				3	A	
Pulsed body-diode current	I _{sm}				6	A	3
DYNAMIC PARAMETERS							
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =15V, f=1MHz		1900		pF	
Output capacitance	C _{oss}			530		pF	
Reverse transfer capacitance	C _{rss}			120		pF	
SWITCHING PARAMETERS							
Total gate charge	Q _g	V _{gs} =10V, V _{ds} =15V, Id=15A		18.0	28.0	nC	2
Gate-source charge	Q _{gs}			4.2		nC	2
Gate-drain charge	Q _{gd}			5.4		nC	2
Turn-on delay time	t _{d(on)}	V _{gs} =10V, V _{ds} =15V, Id≈1A R _l =15Ω, R _{gen} =6Ω		10		ns	2
Turn-on rise time	t _r			24		ns	2
Turn-off delay time	t _{d(off)}			48		ns	2
Turn-off fall time	t _f			12		ns	2
Body diode reverse recovery time	t _{rr}	I _f =3A, dI/dt=100A/μs		50	80	ns	

NOTE :

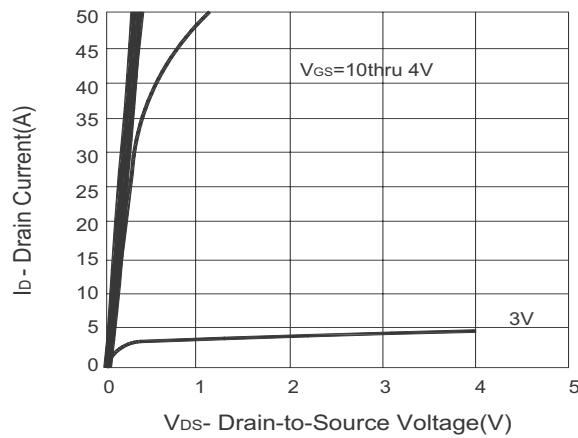
1. Pulsed width≤300μsec and Duty cycle≤2%;
2. Independent of operating temperature;
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle ≤ 1%.

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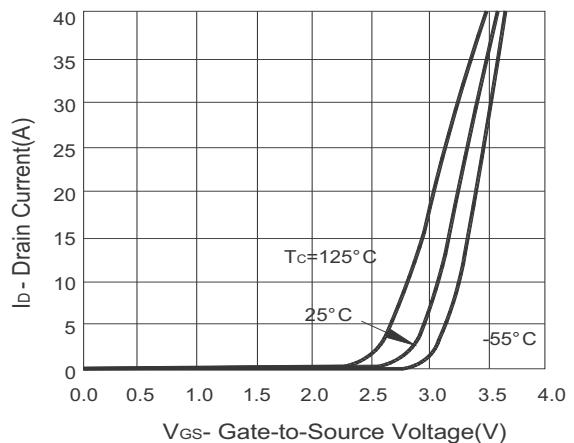
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■ Typical electrical and thermal characteristics

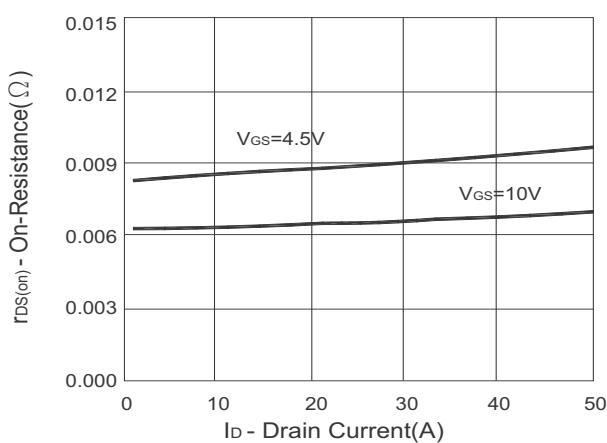
Output Characteristics



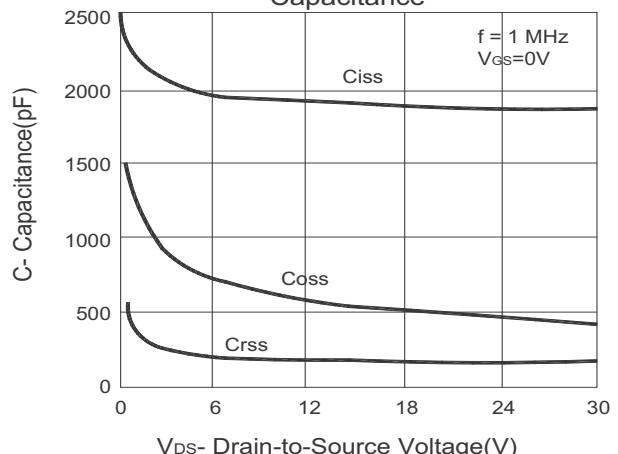
Transfer Characteristics



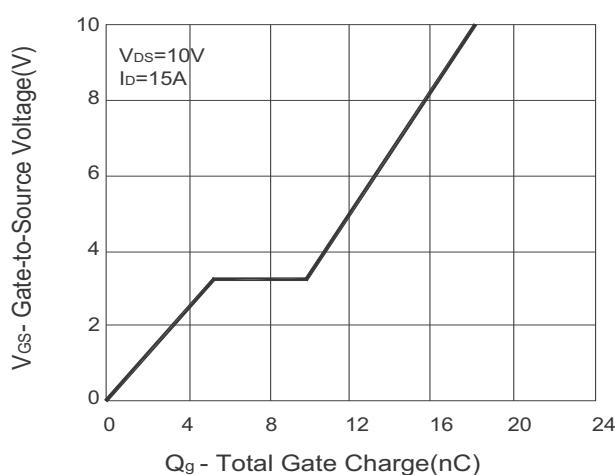
On-Resistance vs. Drain Current



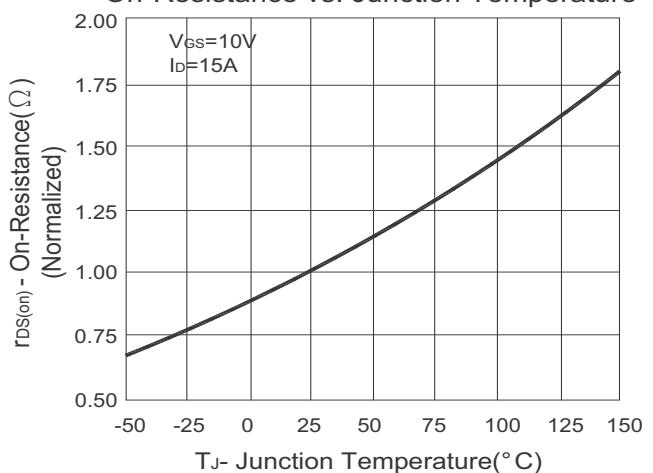
Capacitance



Gate Charge

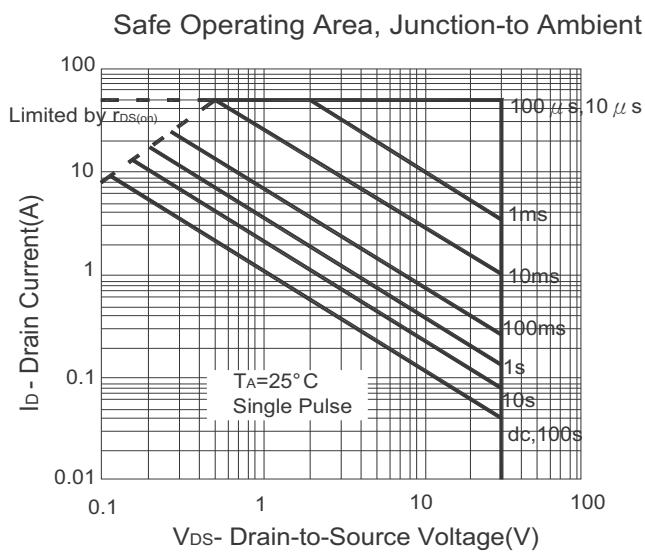
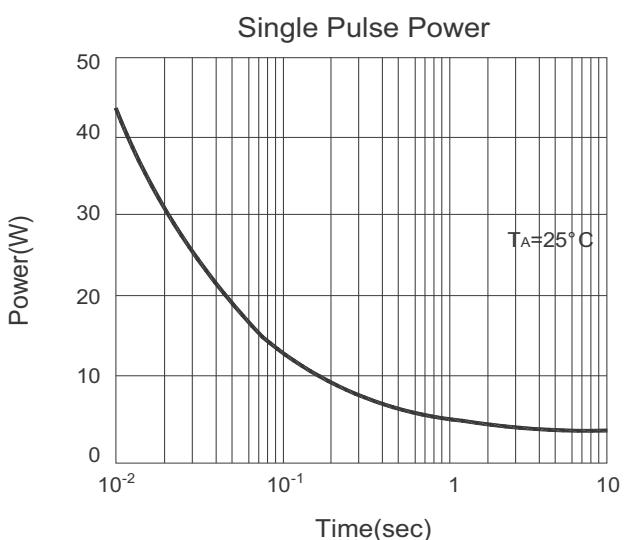
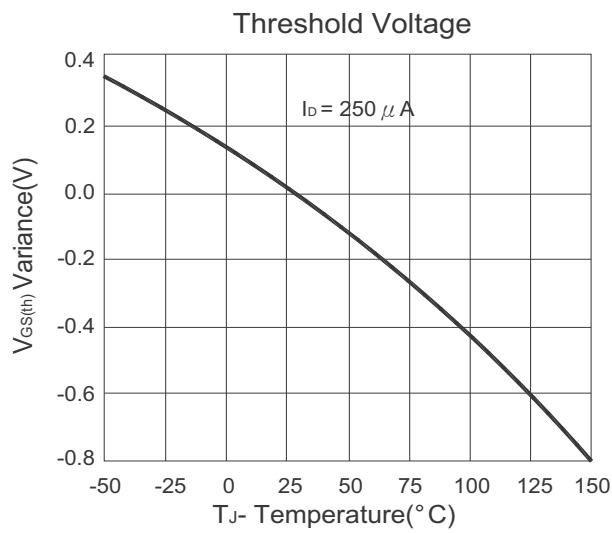
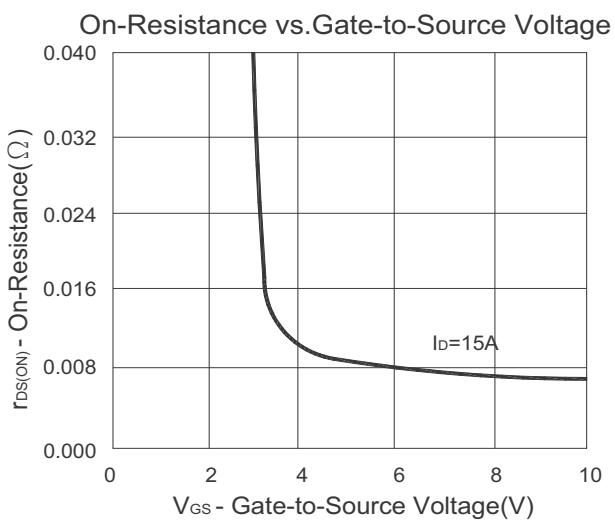
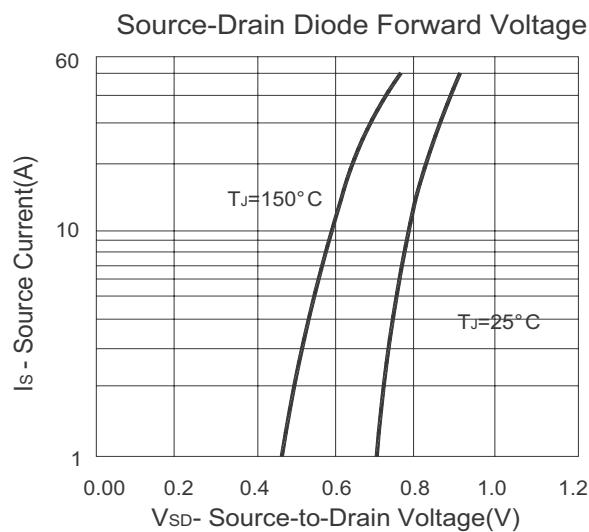


On-Resistance vs. Junction Temperature



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