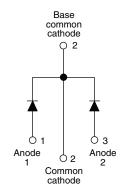


Vishay Semiconductors

# Schottky Rectifier, 2 x 25 A

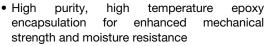




PRODUCT SUMMARY					
Package	TO-247AC				
I <sub>F(AV)</sub>	2 x 25 A				
$V_R$	30 V				
V <sub>F</sub> at I <sub>F</sub>	0.38 V				
I <sub>RM</sub> max.	450 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	27 mJ				

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

### **DESCRIPTION**

The VS-52CPQ030... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I <sub>F(AV)</sub>	Rectangular waveform	50	Α				
V <sub>RRM</sub>		30	V				
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	2180	Α				
V <sub>F</sub>	25 Apk, T <sub>J</sub> = 125 °C (per leg)	0.38	V				
T <sub>J</sub>	Range	- 55 to 150	°C				

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-52CPQ030PbF	VS-52CPQ030-N3	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	30	30	V		
Maximum working peak reverse voltage	$V_{RWM}$	30	30	V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDI	TEST CONDITIONS		UNITS
Maximum average forward current	per leg	1 .	<sub>V)</sub> 50 % duty cycle at T <sub>C</sub> = 132 °C, rectangular waveform		25	
See fig. 5	per device	I <sub>F(AV)</sub>			50	Α
Maximum peak one cycle	nt nor log	I	5 μs sine or 3 μs rect. pulse	μs sine or 3 μs rect. pulse Following any rated load condition and with rated		A
non-repetitive surge current per leg See fig. 7		IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	600	
Non-repetitive avalanche	energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 6 A, L = 1.5 mH		27	mJ
Repetitive avalanche curre	ent per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		6	Α

Revision: 31-Aug-11 Document Number: 94237



# VS-52CPQ030PbF, VS-52CPQ030-N3

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
		25 A	T <sub>.1</sub> = 25 °C	0.48	V	
Maximum forward voltage drop per leg	V (1)	50 A	1j=25 C	0.55		
See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	25 A	T 407.00	0.38		
		50 A	T <sub>J</sub> = 125 °C	0.49		
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C		1.9	4	
See fig. 2	IRM (1)	T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	450	mA	
Threshold voltage	V <sub>F(TO)</sub>	T T mayimum		0.24	V	
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		5.05	mΩ	
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		4600	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 m	7.5	nH		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	Rated V <sub>R</sub>		V/μs	

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	)	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		В	DC operation See fig. 4	0.8		
Maximum thermal resistance, junction to case per package		$R_{thJC}$	DC operation	0.4	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.25		
Approximate weight				6	g	
Approximate weight				0.21	OZ.	
Manustina taunus	minimum			6 (5)	kgf ⋅ cm	
Mounting torque —	maximum			12 (10)	(lbf ⋅ in)	
Marking device			Case style TO-247AC (JEDEC)	52CP	Q030	

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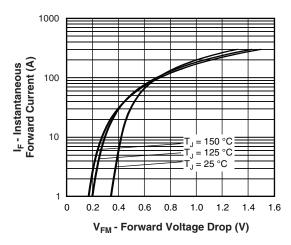


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

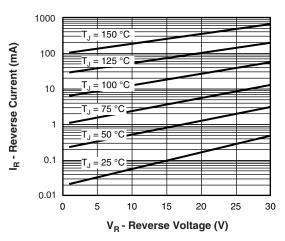


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

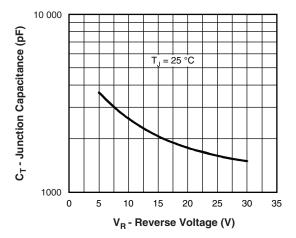


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

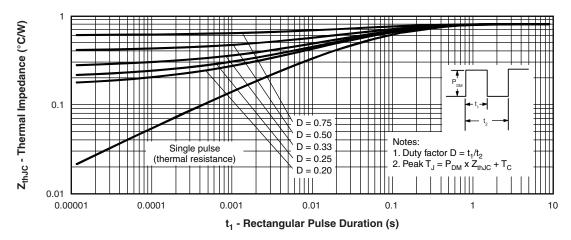


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)



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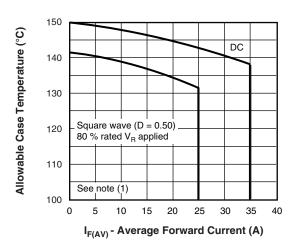


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

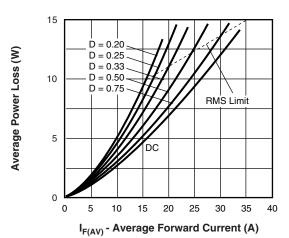


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

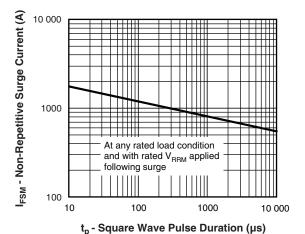


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

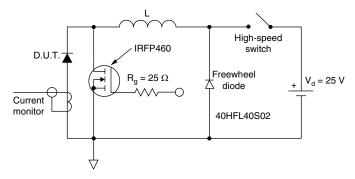


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

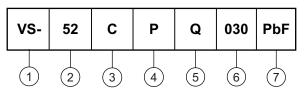
 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{th}JC}; \\ \text{Pd} = \text{Forward power loss} = I_{\text{F}(\text{AV})} \times \text{V}_{\text{FM}} \text{ at } (I_{\text{F}(\text{AV})}/D) \text{ (see fig. 6)}; \\ \text{Pd}_{\text{REV}} = \text{Inverse power loss} = \text{V}_{\text{R1}} \times \text{I}_{\text{R}} \text{ (1 - D)}; I_{\text{R}} \text{ at } \text{V}_{\text{R1}} = 80 \text{ \% rated V}_{\text{R}} \\ \end{array}$ 

# VS-52CPQ030PbF, VS-52CPQ030-N3

Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

Current rating (50 A)

Circuit configuration:

C = Common cathode

4 - Package:

P = TO-247

5 - Schottky "Q" series

6 - Voltage code (030 = 30 V)

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

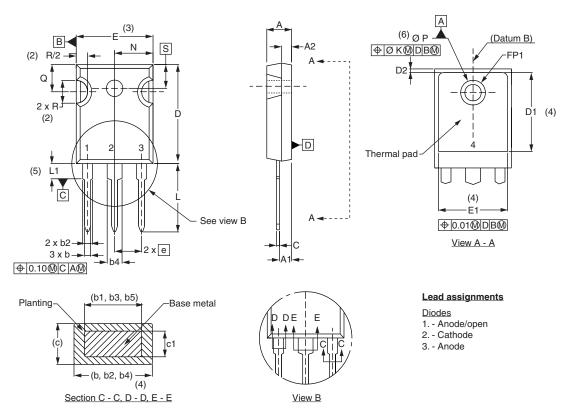
ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-52CPQ030PbF	25	500	Antistatic plastic tube				
VS-52CPQ030-N3	25	500	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95223</u>					
Dort marking information	TO-247AC PbF	www.vishay.com/doc?95226			
Part marking information	TO-247AC -N3	www.vishay.com/doc?95007			



## Vishay Semiconductors

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215	BSC	
FK	2.	54	0.0	010	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62	BSC	0	.3	
ΦР	3.56	3.66	0.14	0.144	
ФР1	1	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51	BSC	0.217	'BSC	

#### **Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c



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