**New Product** 



Vishay General Semiconductor

# **Surface Mount Ultrafast Plastic Rectifier**



DO-214AA (SMB)

2.0 A

100 V, 150 V, 200 V

50 A

20 ns

0.76 V

150 °C

**PRIMARY CHARACTERISTICS** 

I<sub>F(AV)</sub> V<sub>RRM</sub>

I<sub>FSM</sub>

t<sub>rr</sub>

 $V_F$  at  $I_F = 2.0 \text{ A}$ 

T<sub>.1</sub> max.

## FEATURES

- Oxide planar chip junction
- Ultrafast recovery time
- Low forward voltage, low power losses
- High forward surge capability



- Meets MSL level 1, per J-STD-020, LF maximum compliant peak of 260 °C
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

## **TYPICAL APPLICATIONS**

For use in low voltage, high frequency rectifier of switching power supplies, freewheeling diodes, dc-to-dc converters or polarity protection application.

### **MECHANICAL DATA**

**Case:** DO-214AA (SMB) Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant, commercial grade

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 E3 suffix meets JESD 201 class 1A whisker test

Polarity: Color band denotes cathode end

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL U2B U2C U2D		U2D	UNIT		
Device marking code		U2B U2C U2D		U2D		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	100	150	200	V	
Maximum average forward rectified current (fig. 1)	I <sub>F(AV)</sub>	2.0			А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	50			А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 150			°C	

# U2B, U2C, U2D



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage	I <sub>F</sub> = 2 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.86	0.90	V	
		T <sub>A</sub> = 100 °C		0.76	0.83		
Reverse current	Rated V <sub>R</sub>	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	10	μA	
		T <sub>A</sub> = 100 °C		180	350		
Reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$	T <sub>A</sub> = 25 °C	t <sub>rr</sub>	-	20	ns	
	$I_{F} = 2.0 \text{ A}, \text{ dI/dt} = 50 \text{ A/}\mu\text{s}, \\ V_{R} = 30 \text{ V}, I_{rr} = 0.1 \text{ I}_{RM}$	T <sub>A</sub> = 25 °C		27	-		
		T <sub>A</sub> = 100 °C		35	-		
Storage charge	$I_{F} = 2.0 \text{ A}, \text{ dI/dt} = 50 \text{ A/}\mu\text{s}, \\ V_{R} = 30 \text{ V}, I_{rr} = 0.1 I_{RM}$	T <sub>A</sub> = 25 °C	Q <sub>rr</sub>	9	-	nC	
		T <sub>A</sub> = 100 °C		19	-		
Typical junction capacitance	4.0 V, 1 MHz		CJ	16	-	pF	

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	DL U2B U2C U2D		U2D	UNIT	
Typical thermal resistance	R <sub>0JA</sub> <sup>(1)</sup>	105			°C/W	
	R <sub>0JM</sub> <sup>(1)</sup>	18				

Note

<sup>(1)</sup> Free air, mounted on recommended copper pad area. Thermal resistance  $R_{\theta JA}$  - junction to ambient,  $R_{\theta JM}$  - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
U2D-E3/52T	0.099	52T	750	7" diameter plastic tape and reel	
U2D-E3/5BT	0.099	5BT	3200	13" diameter plastic tape and reel	

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

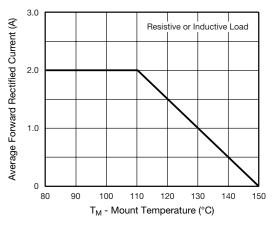


Fig. 1 - Maximum Forward Current Derating Curve

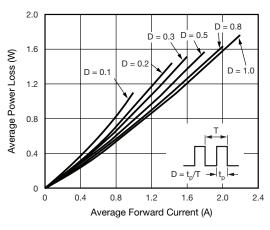


Fig. 2 - Forward Power Loss Characteristics

For technical questions within your region, please contact one of the following: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u>



# U2B, U2C, U2D

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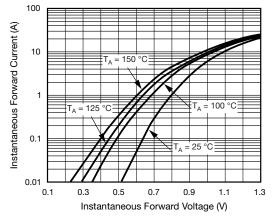


Fig. 3 - Typical Instantaneous Forward Characteristics

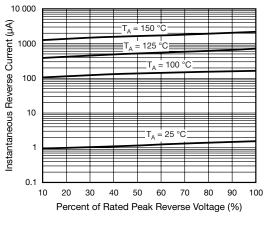


Fig. 4 - Typical Reverse Characteristics

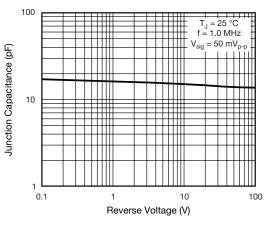


Fig. 5 - Typical Junction Capacitance

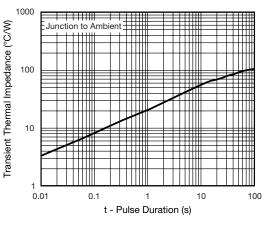
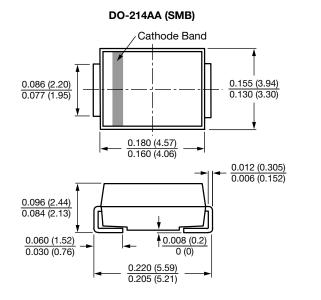
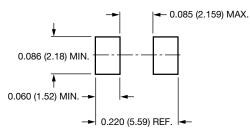


Fig. 6 - Typical Transient Thermal Impedance

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



Mounting Pad Layout



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