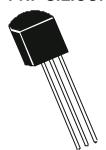




An IS/ISO 9002 and IECQ Certified Manufacturer

#### PNP SILICON PLANAR EPITAXIAL TRANSISTORS



BC556, A, B, C BC557, A, B, C BC558, A, B, C TO-92 Plastic Package

## **General Purpose Transistors**

#### ABSOLUTE MAXIMUM RATINGS(Ta=25 deg C unless otherwise specified)

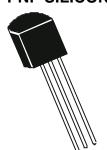
DESCRIPTION	SYMBOL		BC556	BC557	BC558	UNITS
Collector Emitter Voltage	V <sub>CEO</sub>		65	45	30	V
Collector Emitter Voltage	VCEO		80	50	30	V
Collector Base Voltage	V <sub>CBO</sub>		80	50	30	V
Emitter Base Voltage	V <sub>EBO</sub>	DataSheet4LLcom	5	5	5	V
Collector Current Continuous	I <sub>C</sub>			100		mA
Peak	I <sub>CM</sub>			200		mΑ
Base Current - Peak	I <sub>BM</sub>			200		mΑ
Emitter Current - Peak	I <sub>EM</sub>			200		mΑ
Collector Power Dissipation	P <sub>TA</sub>			500		mW
Ta =25 deg C						
Operating And Storage Junction	T <sub>j</sub> , T <sub>stg</sub>			-55 to +150		٥C
Temperature Range						
THERMAL RESISTANCE						
Junction to ambient	$R_{th(j-a)}$			250		°C/W

#### **ELECTRICAL CHARACTERISTICS (Ta=25 deg C Unless Otherwise Specified)**

DESCRIPTION		SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Emitter Voltage							
	BC556	$V_{CEO}$	$I_C=2mA,I_B=0$	65			V
	BC557			45			V
	BC558			30			V

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## PNP SILICON PLANAR EPITAXIAL TRANSISTORS



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DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Base Voltage			_			
BC556	V <sub>CBO</sub>	I <sub>C</sub> =100uA,I <sub>E</sub> =0	80			V
BC557	'		50			V
BC558			30			V
Emitter Base Voltage	V <sub>EBO</sub>	I <sub>E</sub> =100uA, I <sub>C</sub> =0	5			V
Collector Cut off Current	I <sub>CBO</sub>	$V_{CB} = 30V, I_{E} = 0$			15	nA
		$V_{CB} = 30V, I_E = 0$			4	uA
		Tj= 150 deg C				
Collector Cut off Current						
BC556	0=0	V <sub>CE</sub> =80V		0.2	15	nA
BC557		V <sub>CE</sub> =50V		0.2	15	nA
BC558	l.	V <sub>CE</sub> =30V <sub>-141</sub> COM V <sub>CE</sub> =80V,Tj=125 <sup>O</sup> C		0.2	15	nA
BC556		V <sub>CE</sub> =80V,Tj=125 <sup>O</sup> C			4	uA
BC557		V <sub>CE</sub> =50V,Tj=125 <sup>O</sup> C			4	uA
BC558		V <sub>CE</sub> =30V,Tj=125 <sup>O</sup> C			4	uA
DC Current Gain						
Α	. h <sub>FE</sub>	$V_{CE}=5V,I_{C}=10uA$		90		
В				150		
С				270		
BC556	h <sub>FE</sub>	$V_{CE}=5V,I_{C}=2mA$	75		475	
BC557, BC558	<b>†</b>	02 , 0	75		800	
			440	400	000	
A B			110	180	220	
B			200 420	290 500	450 800	
			720	- 500	300	
Α	. h <sub>FE</sub>	V <sub>CE</sub> =5V,I <sub>C</sub> =100mA		120		
В				200		
С				400		

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## PNP SILICON PLANAR EPITAXIAL TRANSISTORS

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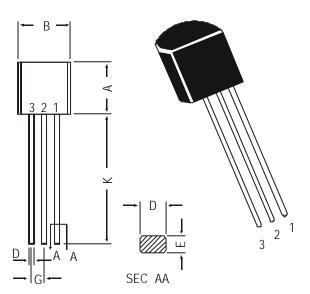
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS	1
Collector Emitter Saturation Voltage							Ĺ
	$V_{CE(sat)}$	$I_C=10$ mA, $I_B=0.5$ mA		0.09	0.3	V	
		I <sub>C</sub> =100mA,I <sub>B</sub> =5mA		0.25	0.65	V	Ĺ
				<u> </u>	<u> </u>		1
Base Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=10$ mA, $I_B=0.5$ mA		0.70		V	1
		I <sub>C</sub> =100mA,I <sub>B</sub> =5mA		0.90		V	1
Base Emitter On Voltage	V <sub>BE(on)</sub>	$I_{C}=2mA, V_{CE}=5V$	0.55	0.66	0.70	V	1
Dase Limiter On Voltage	v B⊏(on)	$I_{C}=10\text{mA}, V_{CE}=5\text{V}$	0.00	0.00	0.70	V	1
	<u> </u>				0.02		
DYNAMICS CHARACTERISTICS	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS	
							4
Transition Frequency	f⊤	$I_{C}=10$ mA, $V_{CE}=5$ V			<del> </del>	<u> </u>	Da
	<b></b>	f=100MHz	<u> </u>	150	<del> </del>	MHz	4
	<u> </u>		<u> </u>		<del></del>	<del> </del>	4
Collector output Capacitance	C <sub>cbo</sub>	$V_{CB} = 10V, f = 1MH_Z$	<del>  </del>	<u> </u>	6	pF	-
Noise Figure	NF	$V_{CE} = 5V, I_{C} = 0.2 \text{mA}$		2	10	dB	
		$R_S=2K\Omega, f=1KH_Z$			†		
		B =200H <sub>Z</sub>					
					<u> </u>		
Small Signal Current Gain							
Α	h <sub>fe</sub>	$V_{CE} = 5V, I_{C} = 2mA$		220	<u> </u>		
В	<u> </u>	f=1KHz		330			
С	<b></b>			600	<del> </del>	<u> </u>	1
Input Impedance	<del> </del>	<u>-</u>	-	<del> </del>	+	<u> </u>	1
A	h <sub>ie</sub>	$V_{CE} = 5V, I_{C} = 2mA$	1.6	2.7	4.5	kΩ	
В		f=1KHz	3.2	4.5	8.5		
С			6.0	8.7	15		
Voltage Feedback							
Α		$V_{CE} = 5V, I_{C} = 2mA$		1.5	<u></u>	x10	1
В		f=1KHz		2.0	<u> </u>		1
С				3.0			4
Output Admittance	<u> </u>	V - 5\/ 1 - 2m Λ	<del>  </del>	10	1 20	МИ	1
A		$V_{CE} = 5V, I_{C} = 2mA$	ļI	18	30	u MHO	1
В	1	f=1KHz	Į.	30	60		1

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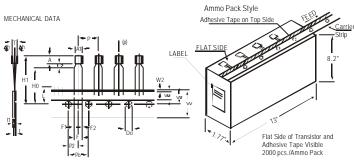
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## **TO-92 Plastic Package**



# **TO-92 Transistors on Tape and Ammo Pack**



All dimensions in mm unless specified otherwise

	ITEM		,	SPECIF	ICATIO	N	
	ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	REMARKS
	BODY WIDTH BODY HEIGHT BODY THICKNESS	A1 A T	4.0 4.8 3.9	10.7	4.8 5.2 4.2	4	
ta	PITCH OF COMPONENT FEED HOLE PITCH	P Po		12.7 12.7		±1 ±0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH
	FEED HOLE CENTRE TO COMPONENT CENTRE	P2		6.35		±0.4	TO BE MEASURED AT BOTTOM OF CLINCH
	DISTANCE BETWEEN OUTER LEADS COMPONENT ALIGNMENT TAPE WIDTH HOLD-DOWN TAPE WIDTH HOLE POSITION	F Ah W Wo W1		5.08 0 18 6 9	1	+0.6 -0.2 ±0.5 ±0.2 +0.7 -0.5	AT TOP OF BODY
	HOLD-DOWN TAPE POSITION LEAD WIRE CLINCH HEIGHT COMPONENT HEIGHT LENGTH OF SNIPPED LEADS FEED HOLE DIAMETER TOTAL TAPE THICKNESS LEAD - TO - LEAD DISTANCEF1,	W2 H0 H1 L D0 t F2		0.5 16 4 2.54	23.25 11.0 1.2	±0.2 ±0.5 ±0.2 +0.4 -0.1	t1 0.3 - 0.6
	CLINCH HEIGHT PULL - OUT FORCE	H2 (P)	6N		3		

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PIN CONFIGURATION

- 1. COLLECTOR
- 2. BASE
- 3. EMITTER

^ -
3
:0
9
5
0
0
3
-

MAX.

- 1. MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm
- 2. MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20 PITCHES.

  3. HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO.
- 3. INCLUDIOWN THE NOT TO EACLED BETOIN THE EDGE(S) OF CARRIER THE AND THERE SHALL BE NO EXPOSURE OF ADHESIVE.
   4. NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED.
   5. A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT.
   6. SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

## **Packing Detail**

PACKAGE	STANDARD PACK		STANDARD PACK INNER CARTON BOX		OUTER CARTON BOX		
				1	1		I

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**Notes** 

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#### **Disclaimer**

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