

GI60L02**N-CHANNEL ENHANCEMENT MODE POWER MOSFET**

BVDSS	25V
RDS(ON)	13mΩ
ID	50A

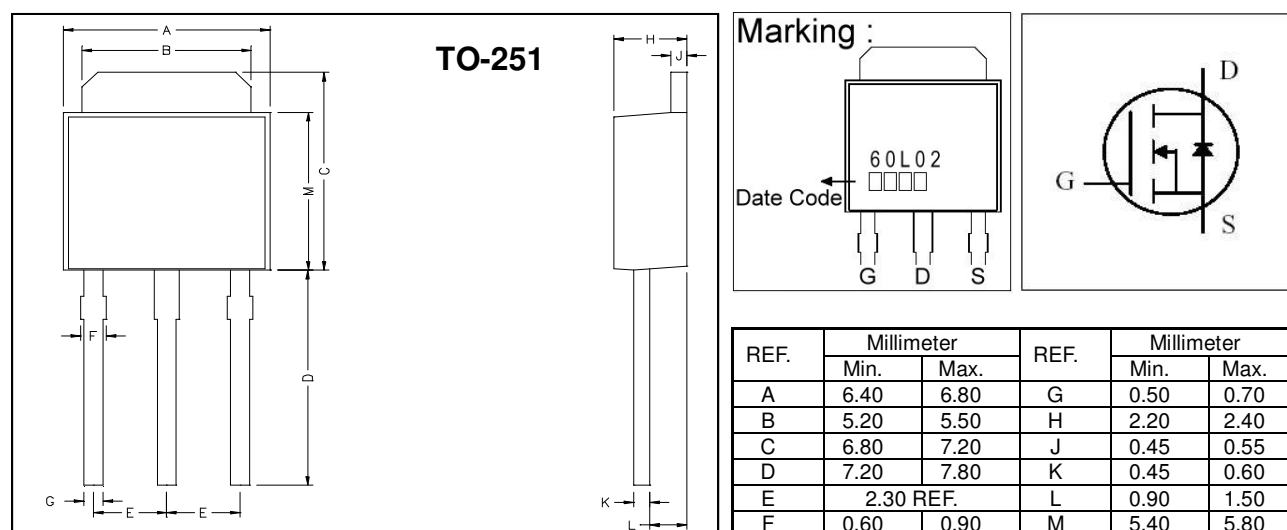
Description

The GI60L02 provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The through-hole version (TO-251) is available for low-profile applications and suited for low voltage applications such as DC/DC converters.

Features

- *Simple Drive Requirement
- *Low Gate Charge
- *Fast Switching

Package Dimensions**Absolute Maximum Ratings**

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	25	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current, $V_{GS}@10V$	$I_D @T_C=25^\circ C$	50	A
Continuous Drain Current, $V_{GS}@10V$	$I_D @T_C=100^\circ C$	32	A
Pulsed Drain Current ¹	I_{DM}	180	A
Total Power Dissipation	$P_D @T_C=25^\circ C$	62.5	W
Linear Derating Factor		0.5	W/ $^\circ C$
Operating Junction and Storage Temperature Range	T_j, T_{stg}	-55 ~ +150	$^\circ C$

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-case Max.	R_{thj-c}	2.0	$^\circ C/W$
Thermal Resistance Junction-ambient Max.	R_{thj-a}	110	$^\circ C/W$

Electrical Characteristics (T_j = 25°C unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV _{DSS}	25	-	-	V	V _{GS} =0, I _D =250uA
Breakdown Voltage Temperature Coefficient	ΔBV _{DSS} / ΔT _j	-	0.037	-	V/°C	Reference to 25°C, I _D =1mA
Gate Threshold Voltage	V _{GS(th)}	1.0	-	3.0	V	V _{DS} =V _{GS} , I _D =250uA
Forward Transconductance	g _{fs}	-	30	-	S	V _{DS} =10V, I _D =25A
Gate-Source Leakage Current	I _{GSS}	-	-	±100	nA	V _{GS} = ±20V
Drain-Source Leakage Current(T _j =25°C)	I _{DSS}	-	-	1	uA	V _{DS} =25V, V _{GS} =0
Drain-Source Leakage Current(T _j =150°C)		-	-	25	uA	V _{DS} =20V, V _{GS} =0
Static Drain-Source On-Resistance	R _{DS(ON)}	-	-	13	mΩ	V _{GS} =10V, I _D =25A
		-	-	26		V _{GS} =4.5V, I _D =20A
Total Gate Charge ²	Q _g	-	21	-	nC	I _D =25A V _{DS} =20V V _{GS} =5V
Gate-Source Charge	Q _{gs}	-	2.8	-		
Gate-Drain ("Miller") Change	Q _{gd}	-	16	-		
Turn-on Delay Time ²	T _{d(on)}	-	8	-	ns	V _{DS} =15V I _D =20A V _{GS} =10V R _G =3.3Ω R _D =0.75Ω
Rise Time	T _r	-	75	-		
Turn-off Delay Time	T _{d(off)}	-	22	-		
Fall Time	T _f	-	20	-		
Input Capacitance	C _{iss}	-	605	-	pF	V _{GS} =0V V _{DS} =25V f=1.0MHz
Output Capacitance	C _{oss}	-	415	-		
Reverse Transfer Capacitance	C _{rss}	-	195	-		

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage ²	V _{SD}	-	-	1.26	V	I _S =50A, V _{GS} =0V, T _j =25°C
Continuous Source Current (Body Diode)	I _S	-	-	50	A	V _D =V _G =0V, V _S =1.26V
Pulsed Source Current (Body Diode) ¹	I _{SM}	-	-	180	A	

Drain-Source Avalanche Ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Single Pulse Avalanche Energy ²	E _{AS}	-	-	61	mJ	V _{DD} =25V, I _D =35A, L=100uH
Avalanche Current	I _{AR}	-	-	35	A	V _{GS} =10V,

Notes: 1. Pulse width limited by safe operating area.

2. Pulse width ≤ 300us, duty cycle ≤ 2%.

Characteristics Curve

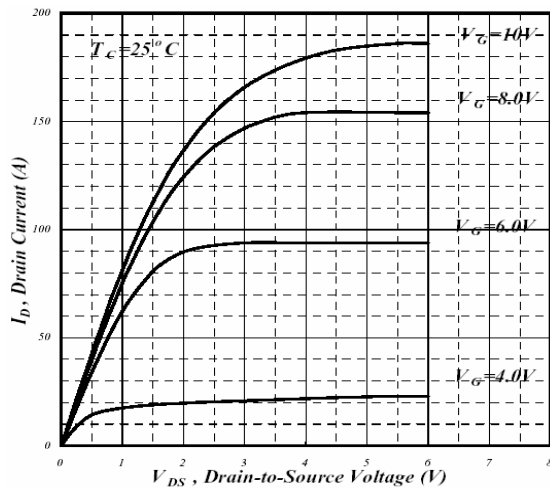


Fig 1. Typical Output Characteristics

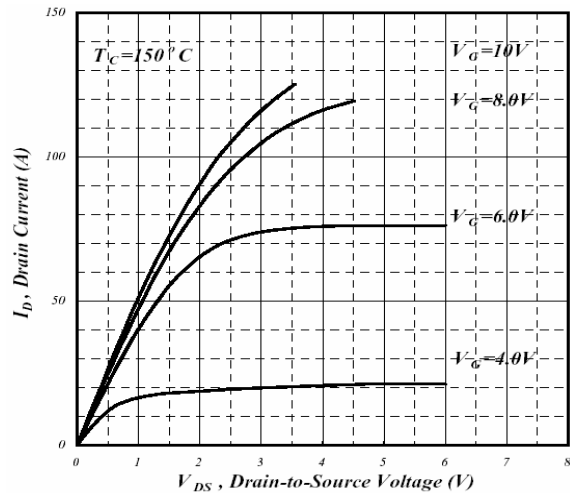


Fig 2. Typical Output Characteristics

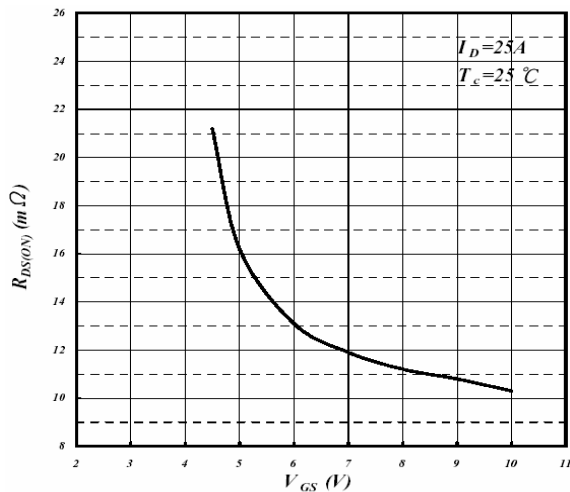


Fig 3. On-Resistance v.s. Gate Voltage

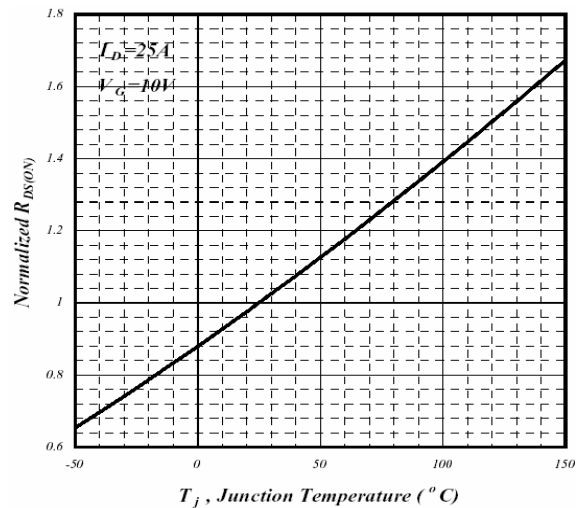


Fig 4. Normalized On-Resistance v.s. Junction Temperature

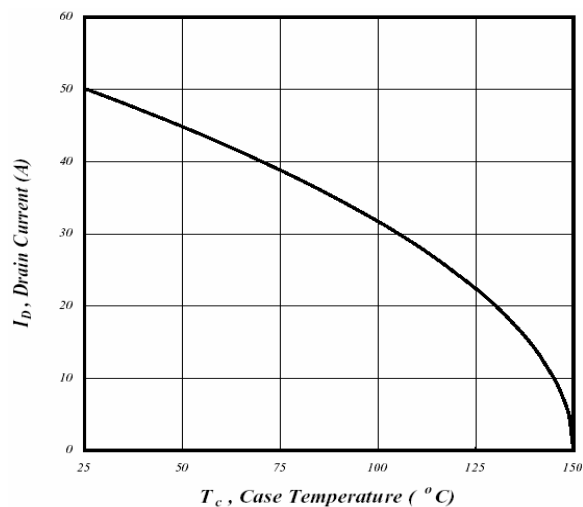


Fig 5. Maximum Drain Current v.s. Case Temperature

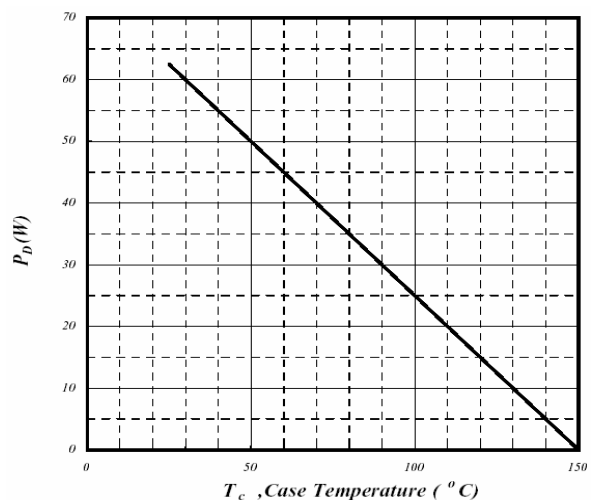
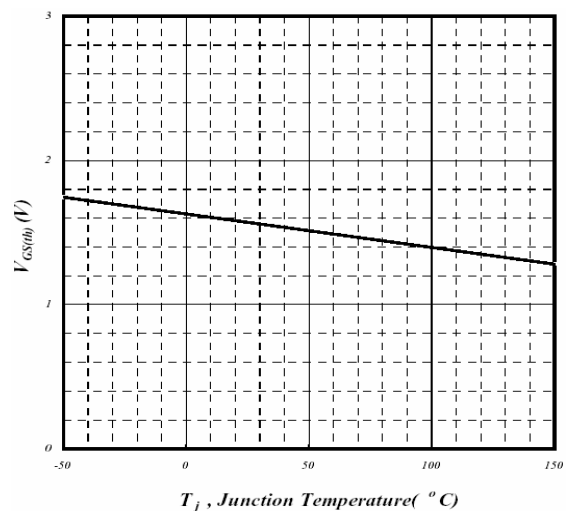
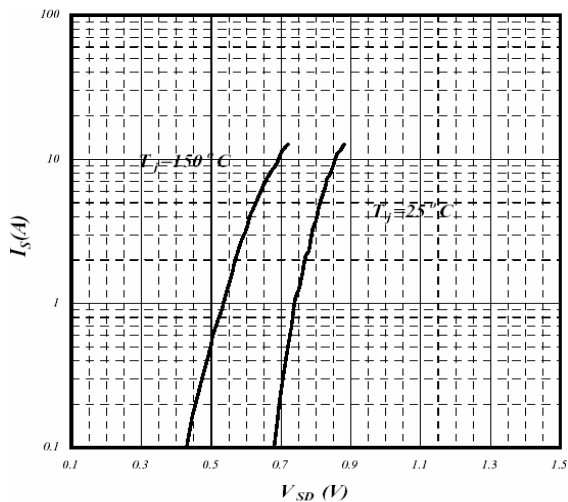
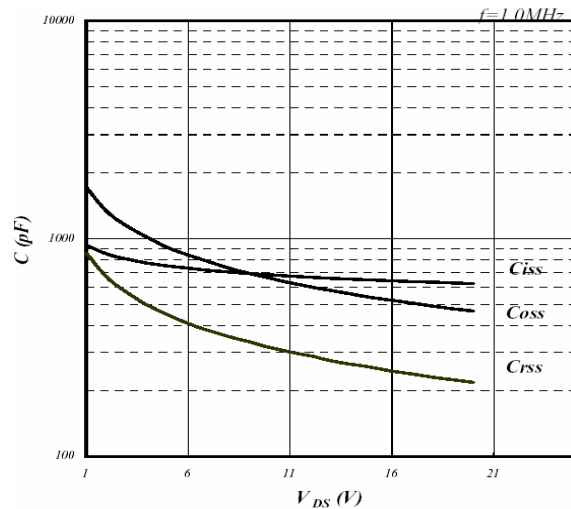
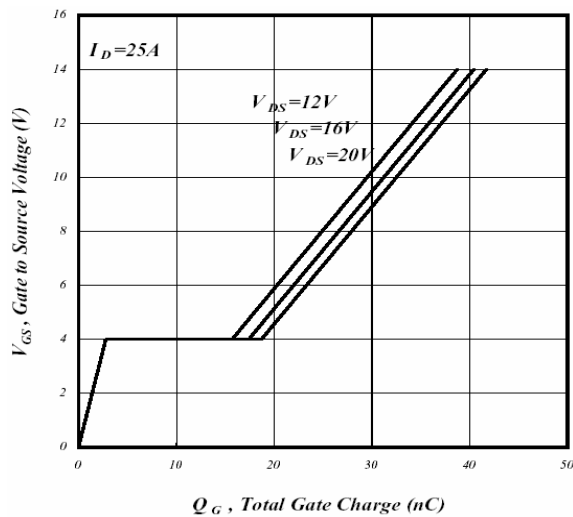
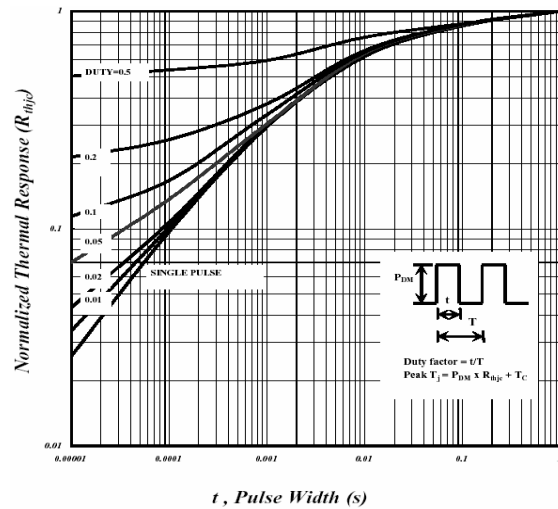
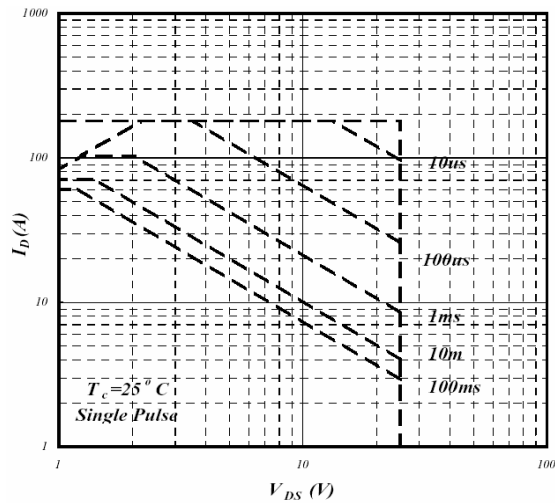


Fig 6. Type Power Dissipation



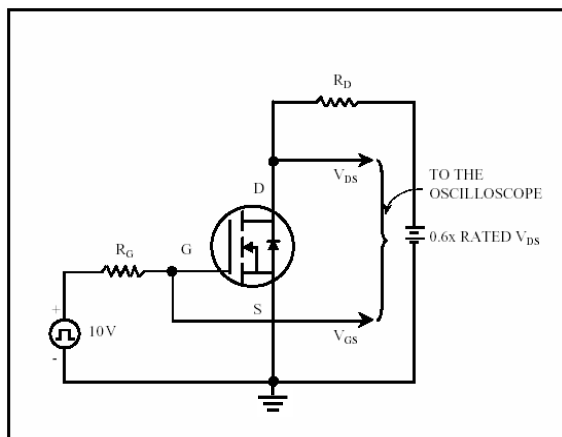


Fig 13. Switching Time Circuit



Fig 14. Switching Time Waveform

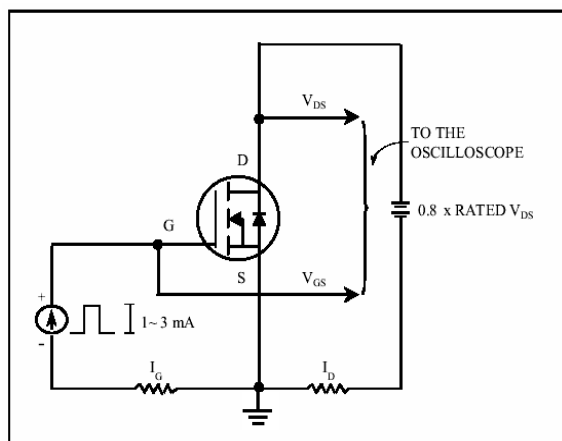


Fig 15. Gate Charge Circuit

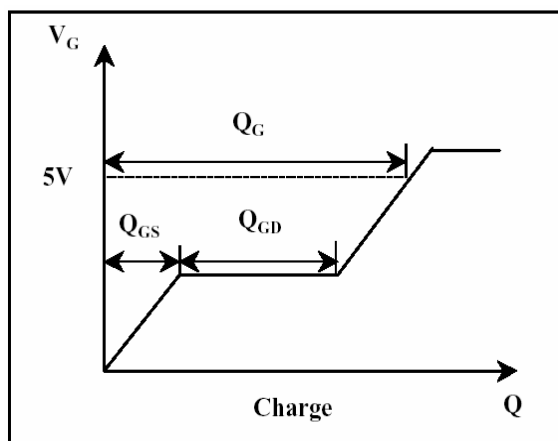


Fig 16. Gate Charge Waveform

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