- Designed for Complementary Use with the TIP36 Series
- 125 W at 25°C Case Temperature
- 25 A Continuous Collector Current
- 40 A Peak Collector Current
- Customer-Specified Selections Available

SOT-93 PACKAGE (TOP VIEW) B 1 C 3

Pin 2 is in electrical contact with the mounting base.

MDTRAA

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING			VALUE	UNIT
	TIP35		80	
Collector base valtage (I = 0)	TIP35A	\ _\ \	100	v
Collector-base voltage (I _E = 0)	TIP35B	V _{CBO}	120	V
	TIP35C		140	
	TIP35		40	
Callaghan amittan vallaga (I	TIP35A	\ \ \ _V	60	V
Collector-emitter voltage (I _B = 0)	TIP35B	V _{CEO}	80	
	TIP35C		100	
Emitter-base voltage			5	٧
Continuous collector current			25	Α
Peak collector current (see Note 1)			40	Α
Continuous base current			5	Α
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)			125	W
Continuous device dissipation at (or below) 25°C free air temperature (see Note 3)			3.5	W
Unclamped inductive load energy (see Note 4)			90	mJ
Operating junction temperature range			-65 to +150	°C
Storage temperature range			-65 to +150	°C
Lead temperature 3.2 mm from case for 10 seconds	TL	250	°C	

NOTES: 1. This value applies for $t_p \le 0.3$ ms, duty cycle $\le 10\%$

- 2. Derate linearly to 150°C case temperature at the rate of 1 W/°C.
- 3. Derate linearly to 150°C free air temperature at the rate of 28 mW/°C.
- 4. This rating is based on the capability of the transistor to operate safely in a circuit of: L = 20 mH, $I_{B(on)}$ = 0.4 A, R_{BE} = 100 Ω , $V_{BE(off)}$ = 0, R_S = 0.1 Ω , V_{CC} = 20 V.

TIP35, TIP35A, TIP35B, TIP35C NPN SILICON POWER TRANSISTORS

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electrical characteristics at 25°C case temperature

PARAMETER		TEST CONDITIONS			MIN	TYP	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C = 30 mA (see Note 5)	I _B = 0	TIP35 TIP35A TIP35B TIP35C	40 60 80 100			V
I _{CES}	Collector-emitter cut-off current	$V_{CE} = 80 \text{ V}$ $V_{CE} = 100 \text{ V}$ $V_{CE} = 120 \text{ V}$ $V_{CE} = 140 \text{ V}$	$V_{BE} = 0$ $V_{BE} = 0$ $V_{BE} = 0$ $V_{BE} = 0$	TIP35 TIP35A TIP35B TIP35C			0.7 0.7 0.7 0.7	mA
I _{CEO}	Collector cut-off current	V _{CE} = 30 V V _{CE} = 60 V	I _B = 0 I _B = 0	TIP35/35A TIP35B/35C			1	mA
I _{EBO}	Emitter cut-off current	V _{EB} = 5 V	I _C = 0				1	mA
h _{FE}	Forward current transfer ratio	$V_{CE} = 4 V$ $V_{CE} = 4 V$	I _C = 1.5 A I _C = 15 A	(see Notes 5 and 6)	25 10		50	
V _{CE(sat)}	Collector-emitter saturation voltage	I _B = 1.5 A I _B = 5 A	I _C = 15 A I _C = 25 A	(see Notes 5 and 6)			1.8 4	٧
V _{BE}	Base-emitter voltage	$V_{CE} = 4 V$ $V_{CE} = 4 V$	$I_{\rm C} = 15 {\rm A}$ $I_{\rm C} = 25 {\rm A}$	(see Notes 5 and 6)			2 4	٧
h _{fe}	Small signal forward current transfer ratio	V _{CE} = 10 V	I _C = 1 A	f = 1 kHz	25			
h _{fe}	Small signal forward current transfer ratio	V _{CE} = 10 V	I _C = 1 A	f = 1 MHz	3			

NOTES: 5. These parameters must be measured using pulse techniques, $t_p = 300 \,\mu s$, duty cycle $\leq 2\%$.

thermal characteristics

	PARAMETER	MIN	TYP	MAX	UNIT
R _{0JC} Junction to ca	se thermal resistance			1	°C/W
R _{θJA} Junction to from	e air thermal resistance			35.7	°C/W

resistive-load-switching characteristics at 25°C case temperature

	PARAMETER	TEST CONDITIONS †			MIN	TYP	MAX	UNIT
t _{on}	Turn-on time	I _C = 15 A	$I_{B(on)} = 1.5 A$	I _{B(off)} = -1.5 A		1.2		μs
t _{off}	Turn-off time	$V_{BE(off)} = -4.15 \text{ V}$	$R_L = 2 \Omega$	$t_p = 20 \ \mu s, \ dc \le 2\%$		0.9		μs

[†] Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.

^{6.} These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

TYPICAL CHARACTERISTICS

TYPICAL DC CURRENT GAIN VS COLLECTOR CURRENT 1000 $V_{CE} = 4 V$ $T_{C} = 25^{\circ}C$ $t_{p} = 300 \,\mu s, duty cycle < 2\%$ 100 100 100 100 100 100

Figure 1.

COLLECTOR-EMITTER SATURATION VOLTAGE

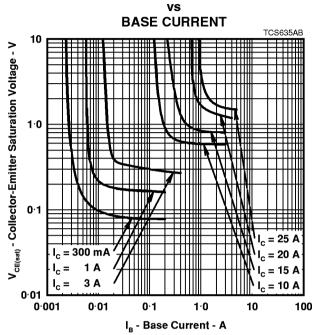


Figure 2.

BASE-EMITTER VOLTAGE

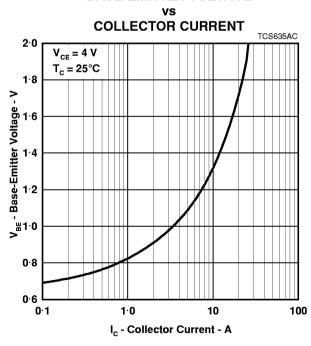
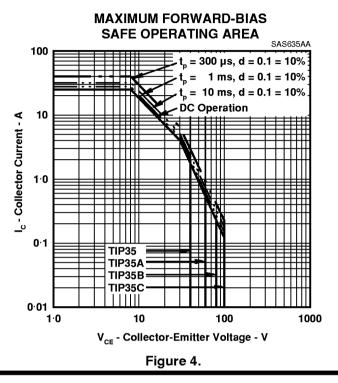


Figure 3.

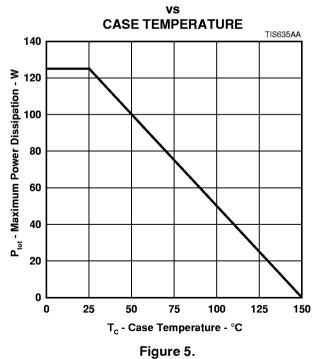


MAXIMUM SAFE OPERATING REGIONS



THERMAL INFORMATION

MAXIMUM POWER DISSIPATION



PRODUCT INFORMATION

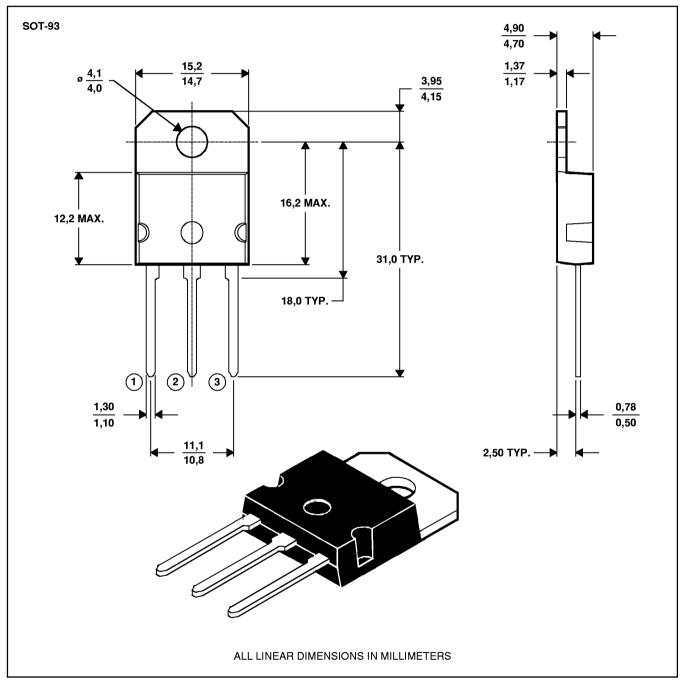
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MECHANICAL DATA

SOT-93

3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTE A: The centre pin is in electrical contact with the mounting tab.

MDXXAW



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