## Silicon PNP Epitaxial

# **HITACHI**

## Application

Low frequency power amplifier complementary pair with 2SD1177

#### Outline

TO-220AB

1. Base
2. Collector
(Flange)
3. Emitter

## Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol		Unit
Collector to base voltage	$V_{\scriptscriptstyle \sf CBO}$	-100	V
Collector to emitter voltage	$V_{\text{ceo}}$	-60	V
Emitter to base voltage	$V_{\scriptscriptstyle{EBO}}$	<b>-</b> 5	V
Collector current	I <sub>c</sub>	-2	A
Collector peak current	I <sub>C(peak)</sub>	-3	A
Collector power dissipation	P <sub>c</sub> *1	20	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

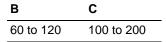
Note: 1. Value at  $T_c = 25^{\circ}C$ 

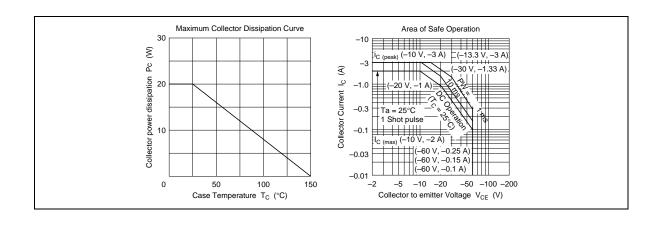
### **Electrical Characteristics** (Ta = 25°C)

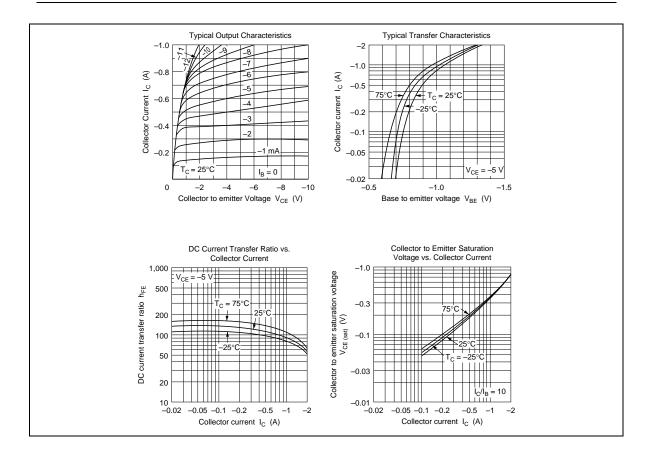
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(BR)CBO}$	-100	_	_	V	$I_c = -1 \text{ mA}, I_E = 0$
Collector to emitter breakdown voltage	$V_{(BR)CEO}$	-60	_	_	V	$I_{\rm c}$ = -10 mA, $R_{\rm BE}$ = $\infty$
Emitter to base breakdown voltage	$V_{\text{(BR)EBO}}$	<b>-</b> 5	_	_	V	$I_{\rm E} = -1$ mA, $I_{\rm C} = 0$
Collector cutoff current	I <sub>CBO</sub>		_	-1.0	μΑ	$V_{CB} = -80 \text{ V}, I_{E} = 0$
Emitter cutoff current	I <sub>EBO</sub>		_	-1.0	μΑ	$V_{EB} = -5 \text{ V}, I_{C} = 0$
DC current transfer ratio	h <sub>FE1</sub> *1	60	_	200		$V_{CE} = -5 \text{ V}, I_{C} = -0.5 \text{ A}^{*2}$
	h <sub>FE2</sub>	40	_	_		$V_{CE} = -5 \text{ V}, I_{C} = -2 \text{ A}^{*2}$
Base to emitter voltage	$V_{\text{BE}}$	_	_	-1.4	V	$V_{CE} = -5 \text{ V}, I_{C} = -2 \text{ A}^{*2}$
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	_	-0.6	-1.0	V	$I_{c} = -1.5 \text{ A } I_{B} = -0.15 \text{ A}^{*2}$
Gain bandwidth product	f <sub>T</sub>		250	_	MHz	$V_{CE} = -5 \text{ V}, I_{C} = -0.5 \text{ A}^{*2}$
Collector output capacitance	Cob	_	50	_	pF	$V_{CB} = -10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$

Notes: 1. The 2SB874 is grouped by  $h_{FE}$  as follows.

2. Pulse test







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