Frequency Synthesizer

SSN-2025A+

50Ω 2010 to 2025 MHz

The Big Deal

CASE STYLE: KJ1367

- Fractional N synthesizer
- Low phase noise and spurious
- Very small size 0.60" x 0.60" x 0.138"

Product Overview

The SSN-2025A+ is a Frequency Synthesizer, designed to operate from 2010 to 2025 MHz for TD-SCDMA application. The SSN-2025A+ is packaged in a metal case (size of 0.60" x 0.60" x 0.138") to shield against unwanted signals and noise.

Key Features

Feature	Advantages
Low phase noise and spurious: • Phase Noise: -101 dBc/Hz typ. @ 10 kHz offset • Step Size Spurious: -85 dBc typ. • Comparison Spurious: -95 dBc typ. • Reference Spurious: -85 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of SSN-2025A+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Small size, 0.60" x 0.60" x 0.138"	The small size enables the SSN-2025A+ to be used in compact designs.

Surface Mount

Frequency Synthesizer

SSN-2025A+

50Ω 2010 to 2025 MHz

Features

- Fractional N synthesizer
- Integrated VCO + PLL
- Low phase noise and spurious
- Robust design and construction
- Low operating voltage (VCC VCO=+4.85V, VCC PLL=+3.2V)
- Small size 0.60" x 0.60" x 0.138"

CASE STYLE: KJ1367
PRICE: $29.95 ea. QTY (1-9)

+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

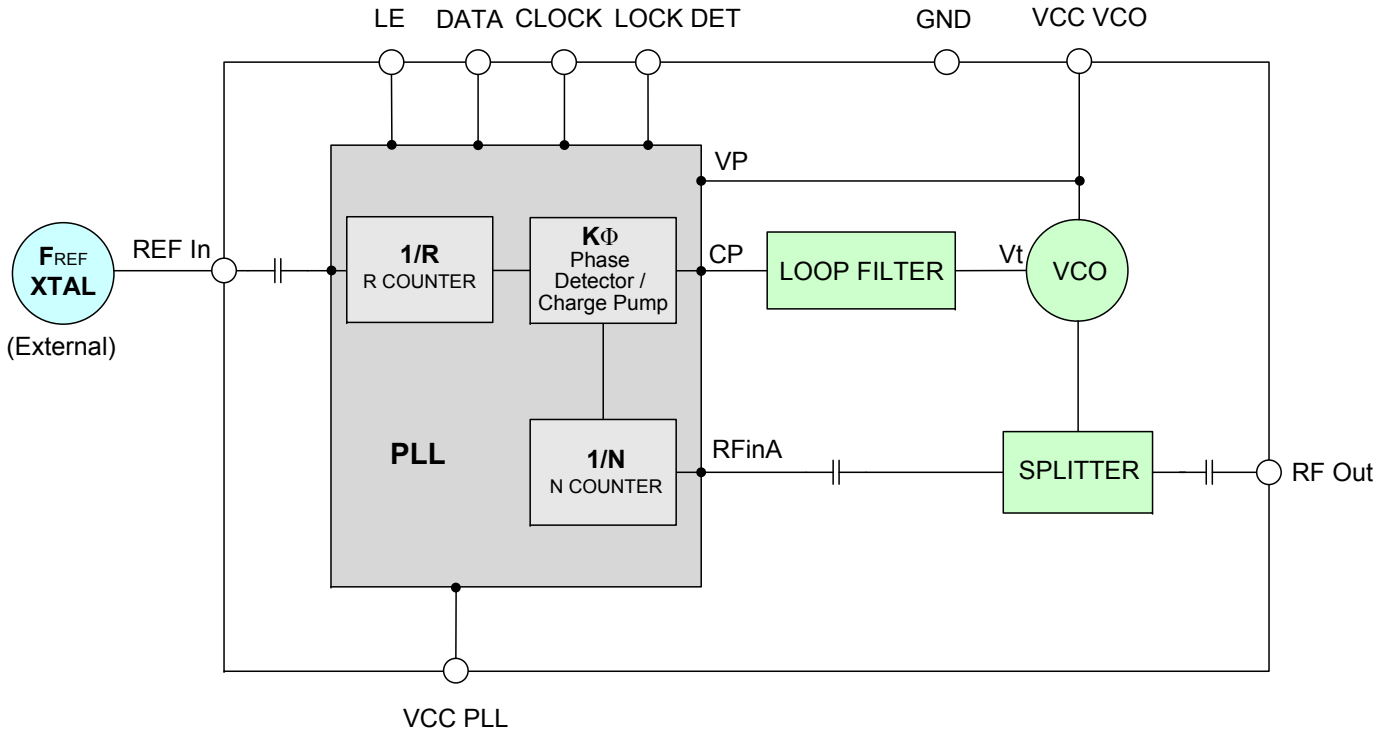
Applications

- TD-SCDMA

General Description

The SSN-2025A+ is a Frequency Synthesizer, designed to operate from 2010 to 2025 MHz for TD-SCDMA application. The SSN-2025A+ is packaged in a metal case (size of 0.60" x 0.60" x 0.138") to shield against unwanted signals and noise. To enhance the robustness of SSN-2025A+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

Simplified Schematic



Electrical Specifications *(over operating temperature -40°C to +85°C)*

Parameters		Test Conditions	Min.	Typ.	Max.	Units
Frequency Range		-	2010	-	2025	MHz
Step Size		-	-	200	-	kHz
Comparison Frequency		-	-	13	-	MHz
Settling Time		Within ± 1 kHz	-	4	-	mSec
Output Power		-	+1	+4	+7	dBm
SSB Phase Noise		@ 100 Hz offset	-	-80	-	dBc/Hz
		@ 1 kHz offset	-	-87	-82	
		@ 10 kHz offset	-	-101	-96	
		@ 100 kHz offset	-	-126	-120	
		@ 1 MHz offset	-	-146	-140	
Integrated SSB Phase Noise		@ 1kHz to 10MHz	-	-50	-	dBc
Step Size Spurious Suppression		Step Size 200 kHz	-	-85	-75	dBc
0.5 Step Size Spurious Suppression		0.5 Step Size 100 kHz	-	-81	-70	
Reference Spurious Suppression		Ref. Freq. 52 MHz	-	-85	-75	
Comparison Spurious Suppression		Comp. Freq. 13 MHz	-	-95	-80	
Non - Harmonic Spurious Suppression		-	-	-90	-	
Harmonic Suppression		-	-	-25	-18	
VCO Supply Voltage		+4.85	+4.75	+4.85	+5.25	V
PLL Supply Voltage		+3.20	+3.10	+3.20	+3.30	
VCO Supply Current		-	-	45	52	mA
PLL Supply Current		-	-	15	23	
Reference Input (External)	Frequency	52 (square wave)	-	52	-	MHz
	Amplitude	1	-	1	-	V_{P-P}
	Input impedance	-	-	100	-	KΩ
	Phase Noise @ 1 kHz offset	-	-	-135	-	dBc/Hz
RF Output port Impedance		-	-	50	-	Ω
Input Logic Level	Input high voltage	-	2.65	-	-	V
	Input low voltage	-	-	-	0.60	V
Digital Lock Detect	Locked	-	2.70	-	3.60	V
	Unlocked	-	-	-	0.50	V
Frequency Synthesizer PLL		-	ADF4153			
PLL Programming		-	3-wire serial 3V CMOS			
Register Map @ 2025 MHz	R0_Register	-	(MSB) 10011011000000011001000 (LSB)			
	R1_Register	-	(MSB) 10101000000100000101 (LSB)			
	R2_Register	-	(MSB) 111100010 (LSB)			
	R3_Register	-	(MSB) 1111000111 (LSB)			

Absolute Maximum Ratings

Parameters	Ratings
VCO Supply Voltage	5.6V
PLL Supply Voltage	4.0V
VCO Supply Voltage to PLL Supply Voltage	-0.3V to +5.8V
Reference Frequency Voltage	-0.3Vmin, VCC PLL +0.3Vmax
Data, Clock, LE Levels	-0.3Vmin, VCC PLL +0.3Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded

Typical Performance Data

FREQUENCY (MHz)	POWER OUTPUT (dBm)			VCO CURRENT (mA)			PLL CURENT (mA)		
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
2010	3.42	3.91	3.97	42.60	45.33	46.51	13.40	15.29	17.55
2013	3.48	3.84	3.91	43.01	45.74	46.53	13.24	15.13	17.36
2016	3.45	3.78	3.85	42.99	45.18	46.54	13.25	15.12	17.38
2019	3.38	3.74	3.79	42.76	44.95	46.56	13.36	15.23	17.50
2022	3.38	3.73	3.73	42.68	45.37	46.56	13.35	15.23	17.50
2025	3.41	3.72	3.68	42.69	45.39	46.57	13.30	15.18	17.44

FREQUENCY (MHz)	HARMONICS (dBc)					
	F2			F3		
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
2010	-36.92	-39.14	-42.10	-25.08	-25.58	-28.57
2013	-36.28	-39.01	-40.89	-23.83	-24.57	-26.74
2016	-35.59	-39.30	-39.96	-23.15	-24.15	-25.69
2019	-34.88	-39.80	-39.16	-22.77	-24.03	-25.02
2022	-34.30	-40.02	-39.11	-23.84	-24.72	-25.30
2025	-33.78	-40.10	-39.44	-25.64	-25.82	-26.05

FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @OFFSETS +25°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
2010	-81.41	-90.55	-102.37	-126.53	-146.30
2013	-81.98	-90.53	-102.03	-126.50	-146.67
2016	-82.61	-91.27	-101.98	-126.46	-146.55
2019	-83.26	-92.40	-102.07	-126.42	-146.20
2022	-84.19	-92.04	-101.98	-126.40	-146.22
2025	-85.26	-90.94	-101.81	-126.38	-146.43

FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @OFFSETS -45°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
2010	-83.30	-93.07	-101.51	-126.78	-146.68
2013	-82.87	-90.72	-102.19	-127.28	-147.65
2016	-83.63	-90.64	-102.39	-127.52	-147.91
2019	-84.97	-91.70	-102.36	-127.65	-147.82
2022	-84.51	-92.19	-102.44	-127.62	-147.55
2025	-83.14	-92.39	-102.58	-127.51	-147.20

FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @OFFSETS +85°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
2010	-80.94	-88.85	-102.06	-125.85	-145.91
2013	-83.36	-90.31	-101.71	-125.72	-145.95
2016	-84.56	-90.29	-101.51	-125.63	-145.83
2019	-85.17	-89.52	-101.40	-125.56	-145.64
2022	-84.70	-89.42	-101.35	-125.48	-145.63
2025	-83.70	-89.67	-101.34	-125.39	-145.71

COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS @Fcarrier 2010MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @Fcarrier 2017.6MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @Fcarrier 2025MHz+(n*Fcomparison) (dBc) note 1		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-97.09	-97.72	-98.09	-97.05	-98.97	-97.49	-95.89	-98.91	-96.61
-4	-98.08	-100.60	-99.32	-98.14	-102.03	-99.72	-98.45	-99.28	-100.65
-3	-111.45	-105.75	-109.34	-111.79	-104.23	-114.49	-114.17	-106.53	-114.33
-2	-101.93	-101.88	-102.48	-103.56	-100.74	-101.70	-103.26	-105.04	-102.07
-1	-99.85	-98.15	-97.58	-99.64	-99.78	-98.15	-98.33	-102.86	-96.88
0 note 2	-	-	-	-	-	-	-	-	-
+1	-99.86	-102.85	-101.71	-101.81	-105.20	-103.65	-99.31	-103.79	-100.08
+2	-108.03	-110.03	-107.45	-108.20	-113.53	-107.21	-106.13	-118.18	-104.35
+3	-105.86	-105.39	-103.98	-105.35	-105.42	-104.02	-108.23	-110.28	-103.79
+4	-102.23	-100.37	-101.08	-102.03	-102.39	-101.10	-104.04	-102.62	-102.55
+5	-101.22	-101.71	-102.47	-104.66	-103.34	-105.14	-103.46	-103.96	-107.16

Note 1: Comparison frequency 13 MHz
Note 2: All spurs are referenced to carrier signal (n=0).

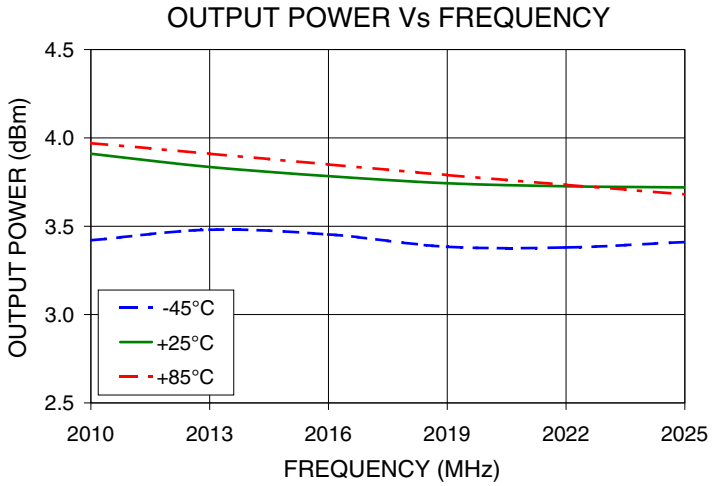
REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS @Fcarrier 2010MHz+(n*Freference) (dBc) note 3			REFERENCE SPURIOUS @Fcarrier 2017.6MHz+(n*Freference) (dBc) note 3			REFERENCE SPURIOUS @Fcarrier 2025MHz+(n*Freference) (dBc) note 3		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-97.27	-100.19	-107.44	-96.54	-99.55	-106.02	-96.71	-100.62	-108.51
-4	-109.33	-109.00	-109.31	-117.80	-109.32	-109.79	-109.43	-110.09	-110.14
-3	-87.93	-92.76	-96.39	-89.01	-93.53	-98.68	-88.62	-93.38	-97.76
-2	-89.97	-92.37	-95.86	-90.62	-92.62	-96.75	-90.46	-92.20	-97.52
-1	-98.86	-100.35	-100.58	-98.41	-101.40	-100.59	-98.48	-99.27	-99.97
0 note 4	-	-	-	-	-	-	-	-	-
+1	-102.19	-100.40	-99.89	-101.58	-103.20	-102.80	-103.33	-103.40	-102.02
+2	-92.63	-93.96	-96.24	-93.26	-94.80	-97.43	-92.34	-94.08	-96.32
+3	-91.75	-93.57	-97.27	-93.17	-94.41	-98.54	-92.88	-95.46	-98.91
+4	-99.78	-103.55	-106.33	-99.84	-103.89	-105.47	-100.12	-104.59	-106.84
+5	-98.88	-101.89	-105.50	-100.17	-102.87	-108.79	-99.36	-102.78	-105.99

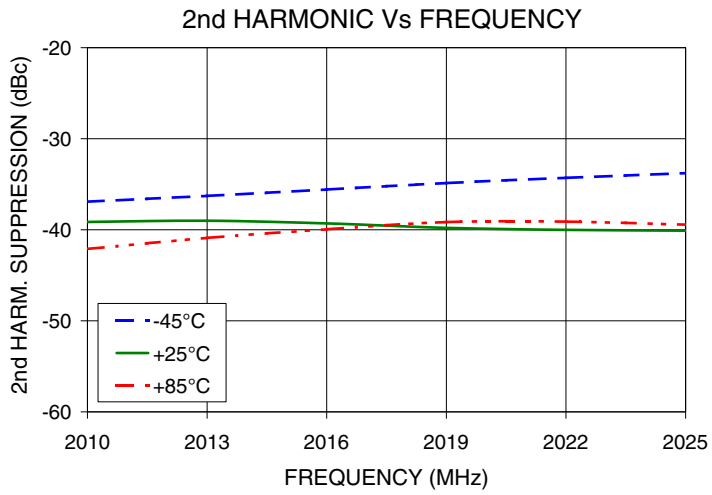
Note 3: Reference frequency 52 MHz
Note 4: All spurs are referenced to carrier signal (n=0).

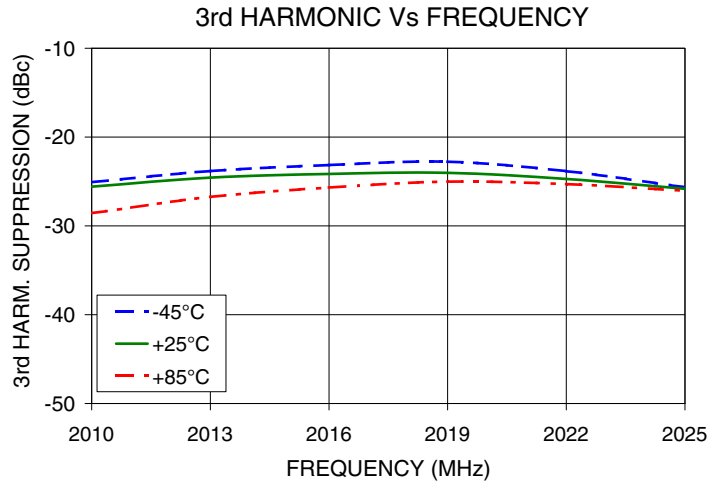
STEP SIZE SPURIOUS ORDER	0.5 STEP SIZE & STEP SIZE SPURIOUS @Fcarrier 2010MHz+(n*Fstep size) (dBc) note 5			0.5 STEP SIZE & STEP SIZE SPURIOUS @Fcarrier 2017.6MHz+(n*Fstep size) (dBc) note 5			0.5 STEP SIZE & STEP SIZE SPURIOUS @Fcarrier 2025MHz+(n*Fstep size) (dBc) note 5		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5.0	-109.20	-109.24	-117.60	-108.73	-106.73	-114.11	-109.14	-109.88	-115.24
-4.5	-119.38	-113.04	-117.62	-117.87	-115.94	-115.71	-118.45	-111.93	-116.07
-4.0	-99.32	-102.84	-104.47	-111.47	-115.20	-109.64	-104.87	-101.02	-108.80
-3.5	-116.53	-112.41	-117.30	-114.12	-115.50	-113.03	-114.11	-116.60	-111.97
-3.0	-97.78	-92.73	-96.28	-110.68	-102.28	-110.10	-107.27	-115.66	-106.38
-2.5	-112.32	-110.32	-110.02	-107.51	-110.87	-104.92	-113.21	-109.71	-109.39
-2.0	-91.21	-99.00	-106.05	-97.80	-107.96	-105.98	-98.47	-94.26	-106.72
-1.5	-104.00	-105.03	-103.20	-99.46	-102.14	-103.20	-104.80	-104.28	-105.14
-1.0	-95.77	-97.25	-87.82	-87.18	-94.14	-93.12	-96.36	-88.25	-91.17
-0.5	-84.56	-85.90	-86.31	-86.68	-82.89	-86.25	-85.85	-87.50	-85.46
0 note 6	-	-	-	-	-	-	-	-	-
+0.5	-87.48	-88.32	-84.81	-87.61	-85.51	-84.91	-86.05	-87.78	-82.09
+1.0	-96.20	-92.70	-87.07	-87.28	-91.28	-94.03	-94.05	-87.69	-92.08
+1.5	-104.73	-104.56	-103.31	-104.86	-98.24	-102.56	-101.89	-104.35	-103.64
+2.0	-90.04	-99.00	-107.58	-98.42	-106.21	-107.09	-100.03	-97.07	-98.96
+2.5	-107.57	-111.26	-112.47	-111.91	-111.85	-110.65	-112.09	-111.23	-110.17
+3.0	-96.84	-91.71	-95.94	-115.88	-101.68	-109.09	-108.34	-112.92	-104.75
+3.5	-112.29	-115.42	-117.02	-112.71	-115.72	-112.34	-111.94	-116.42	-115.36
+4.0	-100.09	-101.49	-104.06	-111.70	-116.63	-107.12	-106.23	-99.94	-108.21
+4.5	-113.65	-117.63	-117.59	-119.20	-119.20	-112.96	-117.63	-116.53	-115.27
+5.0	-107.43	-108.57	-119.58	-107.27	-106.38	-113.62	-110.00	-109.93	-116.31

Note 5: Step size 200 kHz
Note 6: All spurs are referenced to carrier signal (n=0).

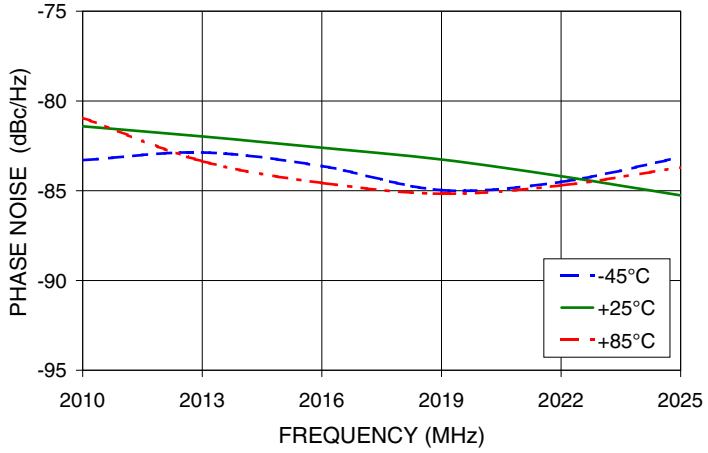
Typical Performance Curves



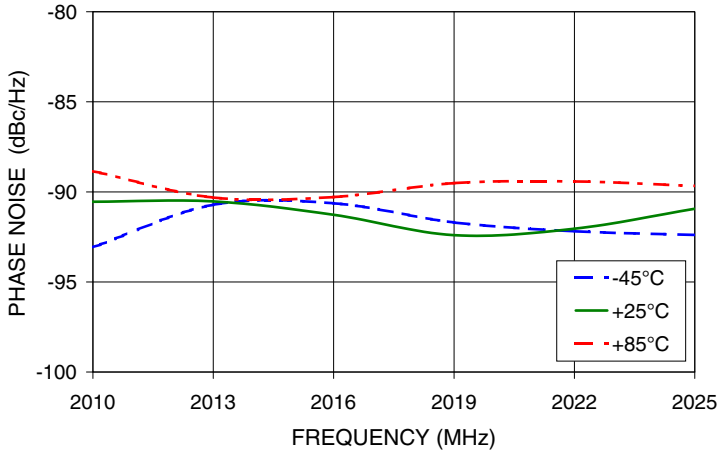




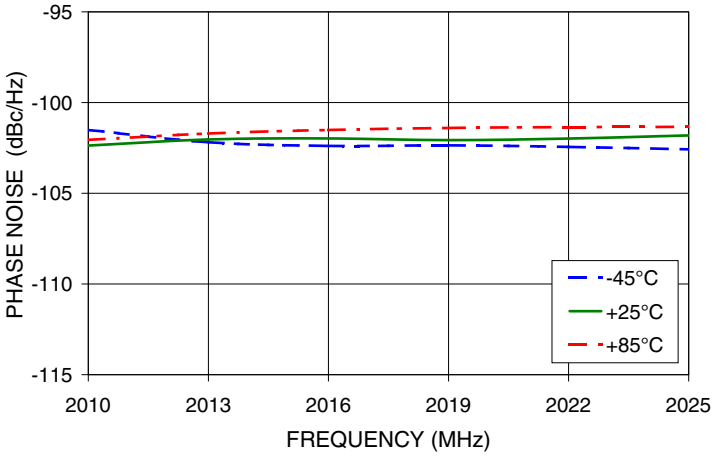
PHASE NOISE @100Hz offset



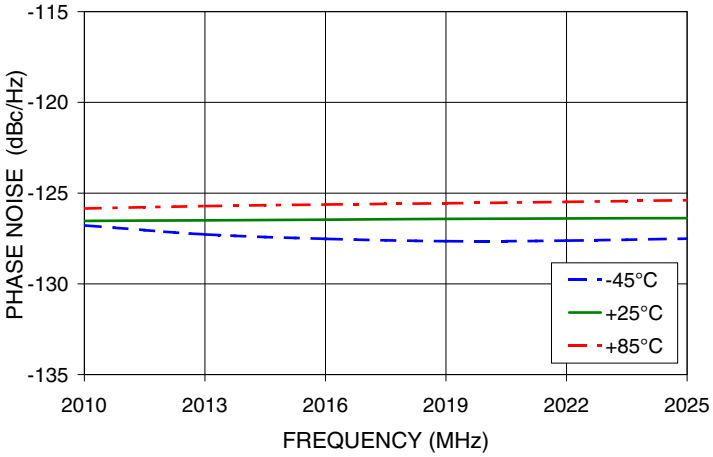
PHASE NOISE @1kHz offset



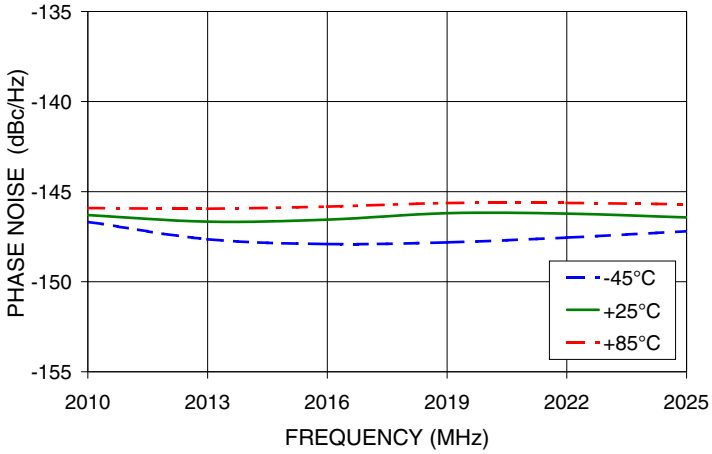
PHASE NOISE @10kHz offset



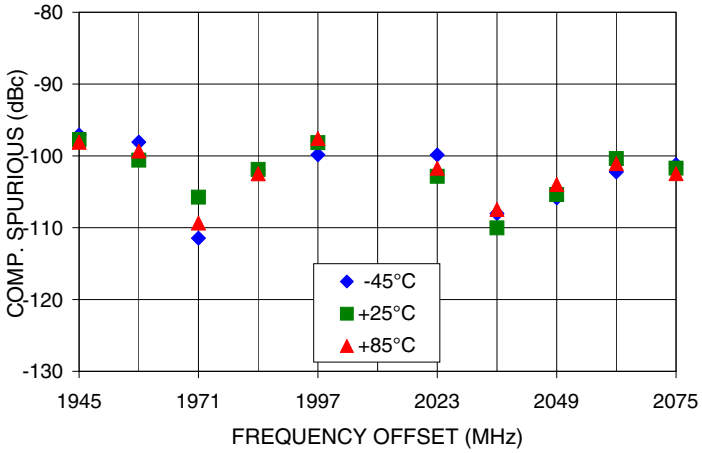
PHASE NOISE @100kHz offset



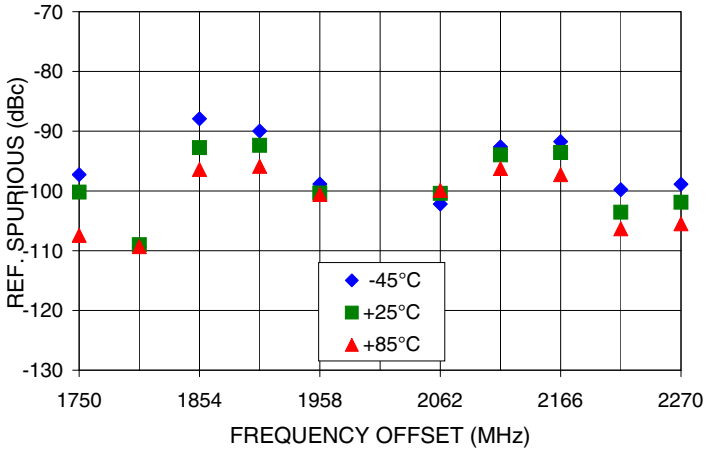
PHASE NOISE @1MHz offset



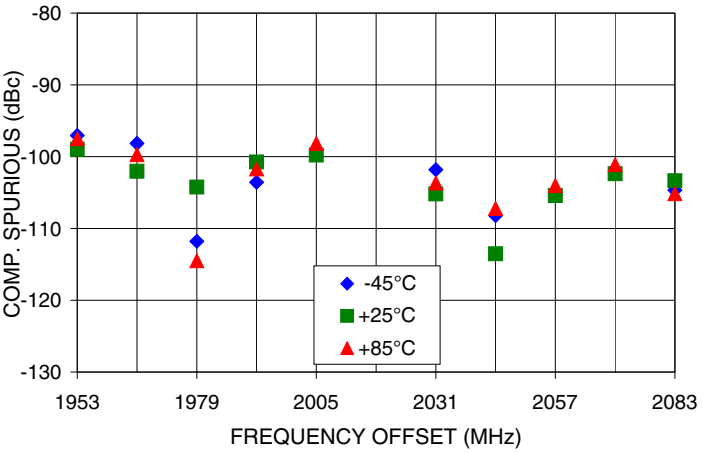
COMPARISON SPURIOUS
Vs FREQ. OFFSET @ Fcar = 2010MHz



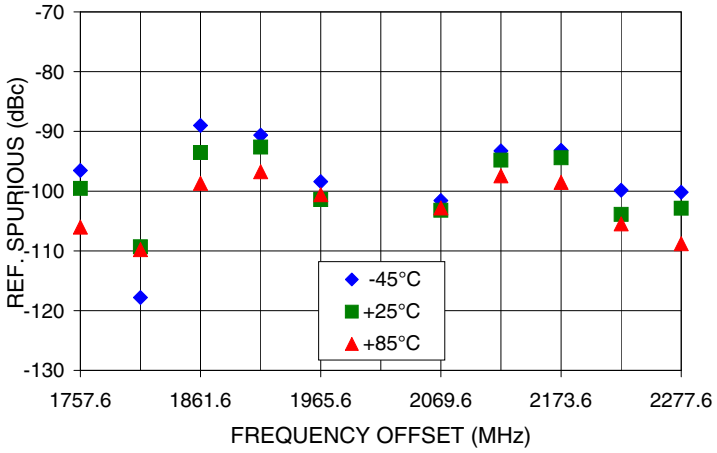
REFERENCE SPURIOUS
Vs FREQ. OFFSET @ Fcar = 2010MHz



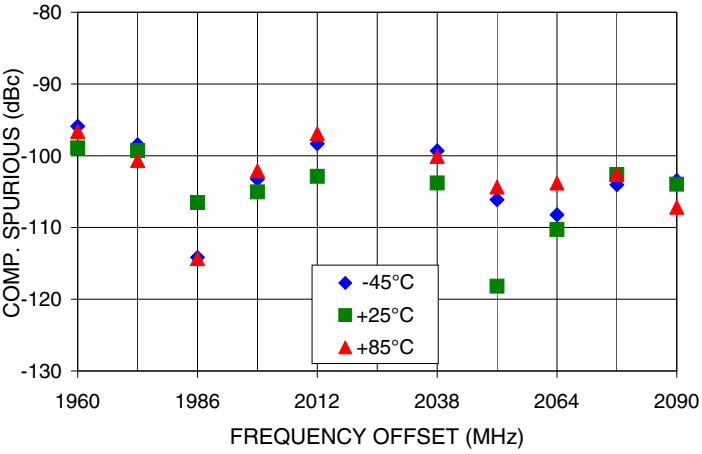
COMPARISON SPURIOUS
Vs FREQ. OFFSET @ Fcar = 2017.6MHz



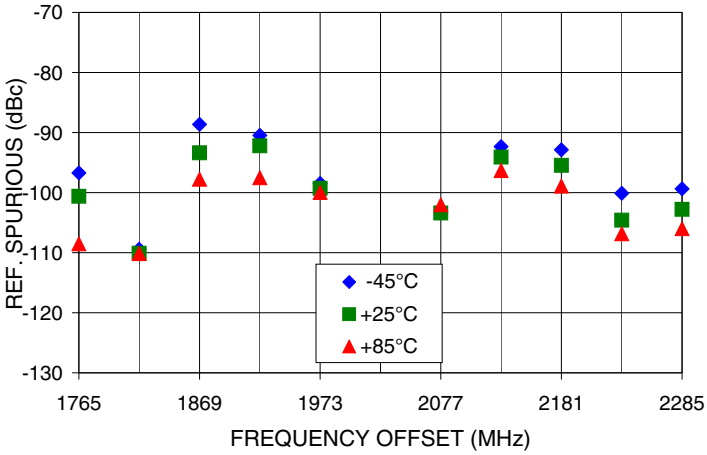
REFERENCE SPURIOUS
Vs FREQ. OFFSET @ Fcar = 2017.6MHz



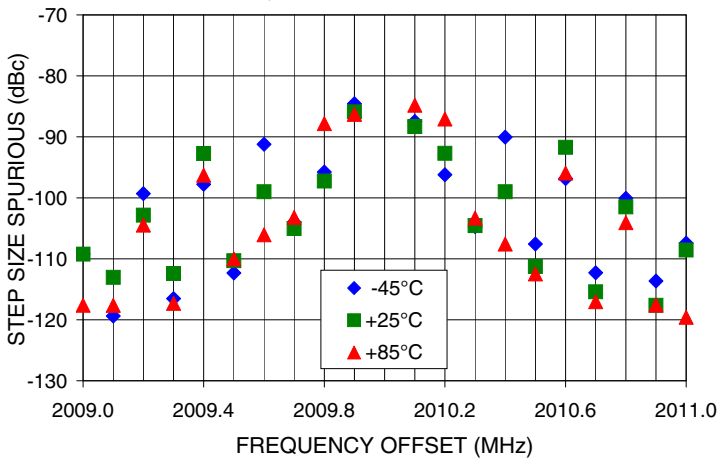
COMPARISON SPURIOUS
Vs FREQ. OFFSET @ Fcar = 2025MHz



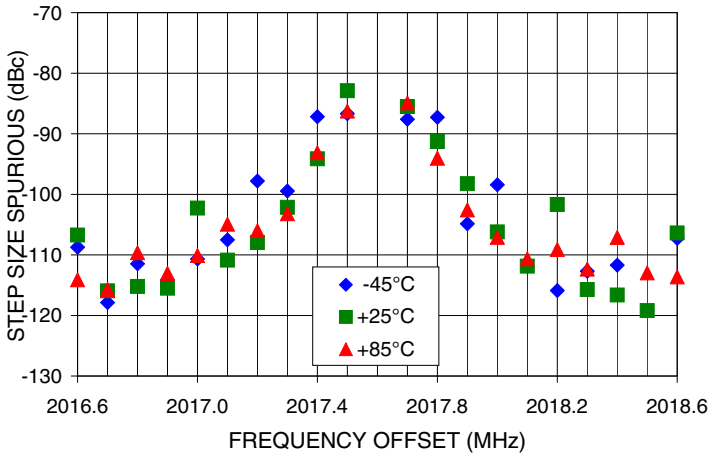
REFERENCE SPURIOUS
Vs FREQ. OFFSET @ Fcar = 2025MHz



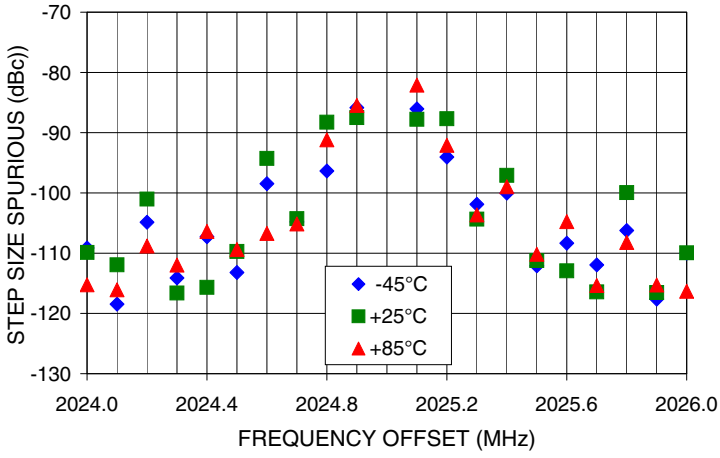
0.5 STEP SIZE & STEP SIZE SPURIOUS
Vs FREQ. OFFSET @ Fcar = 2010MHz



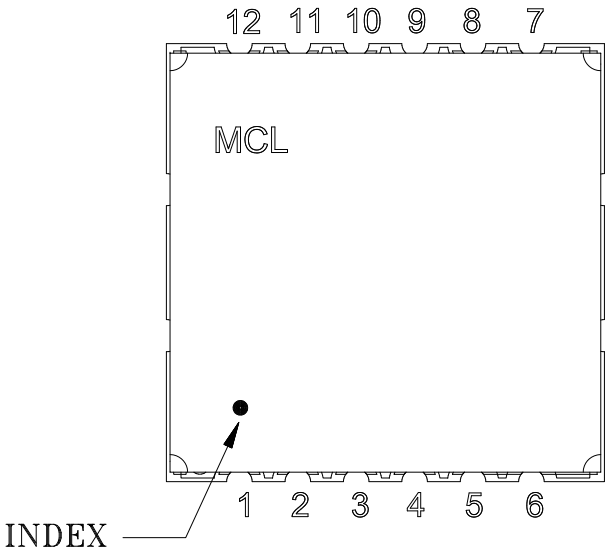
0.5 STEP SIZE & STEP SIZE SPURIOUS
Vs FREQ. OFFSET @ Fcar = 2017.6MHz



0.5 STEP SIZE & STEP SIZE SPURIOUS
Vs FREQ. OFFSET @ Fcar = 2025MHz



Pin Configuration

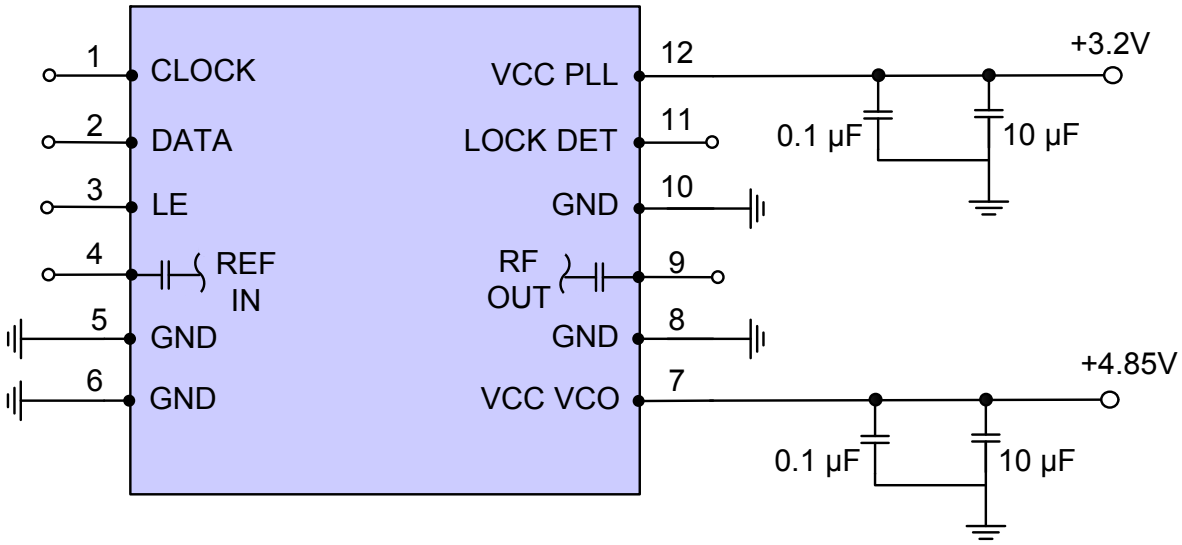


Pin Connection

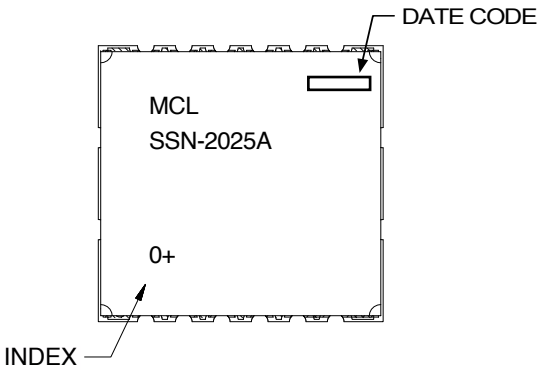
Pin Number	Function
1	CLOCK
2	DATA
3	LE
4	REF IN
5	GND
6	GND
7	VCC VCO
8	GND
9	RF OUT
10	GND
11	LOCK DET
12	VCC PLL

Recommended Application Circuit

Note: REF IN and RF OUT ports are internally AC coupled.



Device Marking



Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: KJ1367

Tape & Reel: TR-F95

Suggested Layout for PCB Design: PL-317

Evaluation Board: TB-552+

Environment Ratings: ENV03T2