

PDTC144V series

NPN resistor-equipped transistors; R1 = 47 k Ω , R2 = 10 k Ω

Rev. 03 — 15 February 2005

Product data sheet

1. Product profile

1.1 General description

NPN resistor-equipped transistors.

Table 1: Product overview

Type number	Package		PNP complement
	Philips	JEITA	
PDTC144VE	SOT416	SC-75	PDTA144VE
PDTC144VK	SOT346	SC-59A	PDTA144VK
PDTC144VM	SOT883	SC-101	PDTA144VM
PDTC144VS [1]	SOT54 (TO-92)	SC-43A	PDTA144VS
PDTC144VT	SOT23	-	PDTA144VT
PDTC144VU	SOT323	SC-70	PDTA144VU

[1] Also available in SOT54A and SOT54 variant packages (see [Section 2](#)).

1.2 Features

- Built-in bias resistors
- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs

1.3 Applications

- General-purpose switching and amplification
- Inverter and interface circuits
- Circuit drivers

1.4 Quick reference data

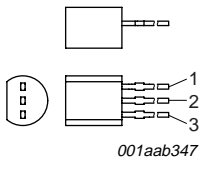
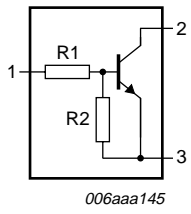
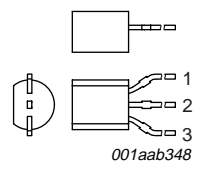
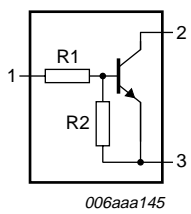
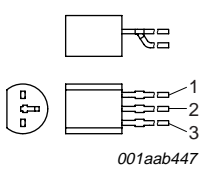
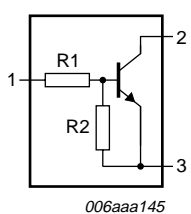
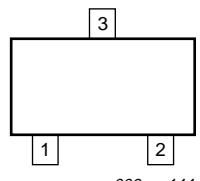
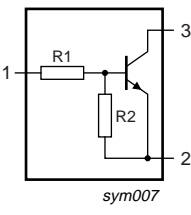
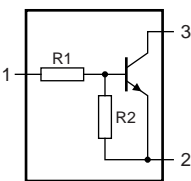
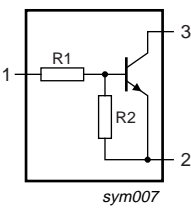
Table 2: Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	50	V
I _O	output current (DC)		-	-	100	mA
R1	bias resistor 1 (input)		33	47	61	k Ω
R2/R1	bias resistor ratio		0.17	0.21	0.26	

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2. Pinning information

Table 3: Pinning

Pin	Description	Simplified outline	Symbol
SOT54			
1	input (base)	 <p>001aab347</p>	 <p>006aaa145</p>
2	output (collector)		
3	GND (emitter)		
SOT54A			
1	input (base)	 <p>001aab348</p>	 <p>006aaa145</p>
2	output (collector)		
3	GND (emitter)		
SOT54 variant			
1	input (base)	 <p>001aab447</p>	 <p>006aaa145</p>
2	output (collector)		
3	GND (emitter)		
SOT23, SOT323, SOT346, SOT416			
1	input (base)	 <p>006aaa144</p>	 <p>sym007</p>
2	GND (emitter)		
3	output (collector)		
SOT883			
1	input (base)		 <p>sym007</p>
2	GND (emitter)		
3	output (collector)		

3. Ordering information

Table 4: Ordering information

Type number	Package		Version
	Name	Description	
PDTC144VE	SC-75	plastic surface mounted package; 3 leads	SOT416
PDTC144VK	SC-59A	plastic surface mounted package; 3 leads	SOT346
PDTC144VM	SC-101	leadless ultra small plastic package; 3 solder lands; body 1.0 × 0.6 × 0.5 mm	SOT883
PDTC144VS ^[1]	SC-43A	plastic single-ended leaded (through hole) package; 3 leads	SOT54
PDTC144VT	-	plastic surface mounted package; 3 leads	SOT23
PDTC144VU	SC-70	plastic surface mounted package; 3 leads	SOT323

[1] Also available in SOT54A and SOT54 variant packages (see [Section 2](#) and [Section 9](#)).

4. Marking

Table 5: Marking codes

Type number	Marking code ^[1]
PDTC144VE	18
PDTC144VK	29
PDTC144VM	G6
PDTC144VS	TC144V
PDTC144VT	*AA
PDTC144VU	*18

[1] * = -: made in Hong Kong
 * = p: made in Hong Kong
 * = t: made in Malaysia
 * = W: made in China

5. Limiting values

Table 6: Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	50	V
V_{CEO}	collector-emitter voltage	open base	-	50	V
V_{EBO}	emitter-base voltage	open collector	-	15	V
V_I	input voltage				
	positive		-	+40	V
	negative		-	-15	V
I_O	output current (DC)		-	100	mA
I_{CM}	peak collector current		-	100	mA
P_{tot}	total power dissipation				
	SOT416	$T_{amb} \leq 25\text{ °C}$	[1] -	150	mW
	SOT346	$T_{amb} \leq 25\text{ °C}$	[1] -	250	mW
	SOT883	$T_{amb} \leq 25\text{ °C}$	[2] [3] -	250	mW
	SOT54	$T_{amb} \leq 25\text{ °C}$	[1] -	500	mW
	SOT23	$T_{amb} \leq 25\text{ °C}$	[1] -	250	mW
	SOT323	$T_{amb} \leq 25\text{ °C}$	[1] -	200	mW
	T_{stg}	storage temperature		-65	+150
T_j	junction temperature		-	150	°C
T_{amb}	ambient temperature		-65	+150	°C

[1] Refer to standard mounting conditions.

[2] Reflow soldering is the only recommended soldering method.

[3] Refer to SOT883 standard mounting conditions; FR4 printed-circuit board with 60 μm copper strip line.

6. Thermal characteristics

Table 7: Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air				
	SOT416		[1] -	-	833	K/W
	SOT346		[1] -	-	500	K/W
	SOT883		[2] [3] -	-	500	K/W
	SOT54		[1] -	-	250	K/W
	SOT23		[1] -	-	500	K/W
	SOT323		[1] -	-	625	K/W

[1] Refer to standard mounting conditions.

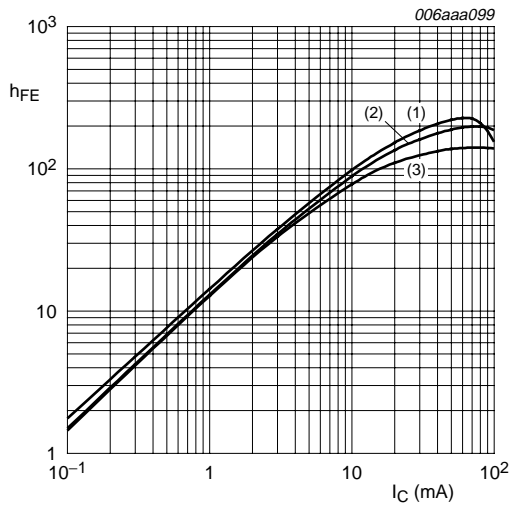
[2] Reflow soldering is the only recommended soldering method.

[3] Refer to SOT883 standard mounting conditions; FR4 printed-circuit board with 60 μm copper strip line.

7. Characteristics

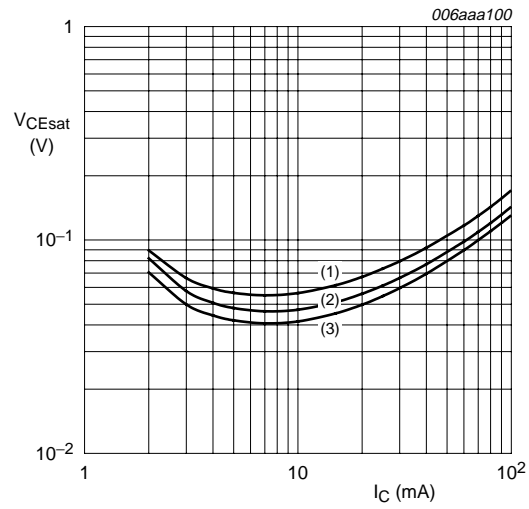
Table 8: Characteristics
T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 50 V; I _E = 0 A	-	-	100	nA
I _{CEO}	collector-emitter cut-off current	V _{CE} = 30 V; I _B = 0 A	-	-	1	μ A
		V _{CE} = 30 V; I _B = 0 A; T _j = 150 °C	-	-	50	μ A
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A	-	-	150	μ A
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 5 mA	40	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA	-	-	150	mV
V _{I(off)}	off-state input voltage	V _{CE} = 5 V; I _C = 100 μ A	-	3.1	1	V
V _{I(on)}	on-state input voltage	V _{CE} = 300 mV; I _C = 2 mA	6	3.8	-	V
R1	bias resistor 1 (input)		33	47	61	k Ω
R2/R1	bias resistor ratio		0.17	0.21	0.26	
C _c	collector capacitance	V _{CB} = 10 V; I _E = i _e = 0 A; f = 1 MHz	-	-	2	pF



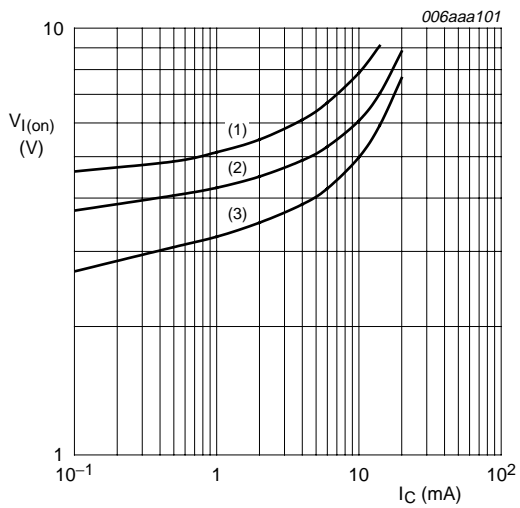
$V_{CE} = 5\text{ V}$
 (1) $T_{amb} = 100\text{ °C}$
 (2) $T_{amb} = 25\text{ °C}$
 (3) $T_{amb} = -40\text{ °C}$

Fig 1. DC current gain as a function of collector current; typical values



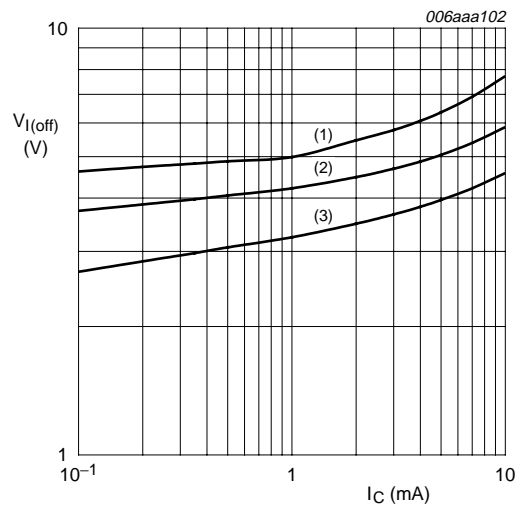
$I_C/I_B = 20$
 (1) $T_{amb} = 100\text{ °C}$
 (2) $T_{amb} = 25\text{ °C}$
 (3) $T_{amb} = -40\text{ °C}$

Fig 2. Collector-emitter saturation voltage as a function of collector current; typical values



$V_{CE} = 0.3\text{ V}$
 (1) $T_{amb} = -40\text{ °C}$
 (2) $T_{amb} = 25\text{ °C}$
 (3) $T_{amb} = 100\text{ °C}$

Fig 3. On-state input voltage as a function of collector current; typical values



$V_{CE} = 5\text{ V}$
 (1) $T_{amb} = -40\text{ °C}$
 (2) $T_{amb} = 25\text{ °C}$
 (3) $T_{amb} = 100\text{ °C}$

Fig 4. Off-state input voltage as a function of collector current; typical values

8. Package outline

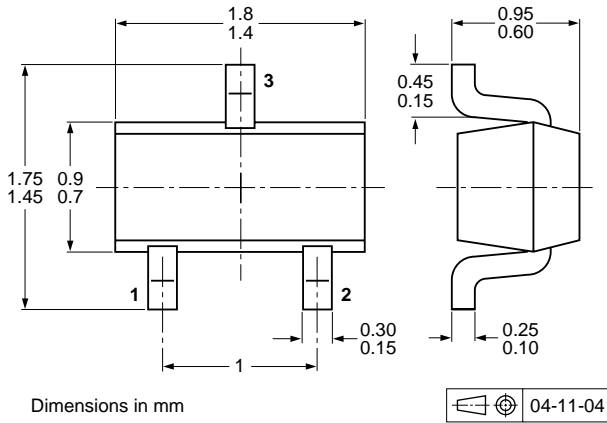


Fig 5. Package outline SOT416 (SC-75)

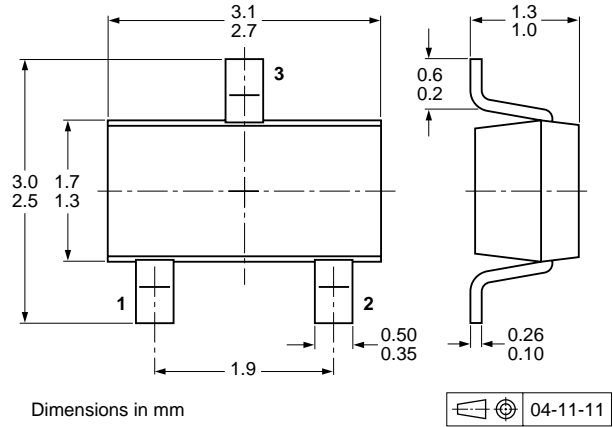


Fig 6. Package outline SOT346 (SC-59A/TO-236)

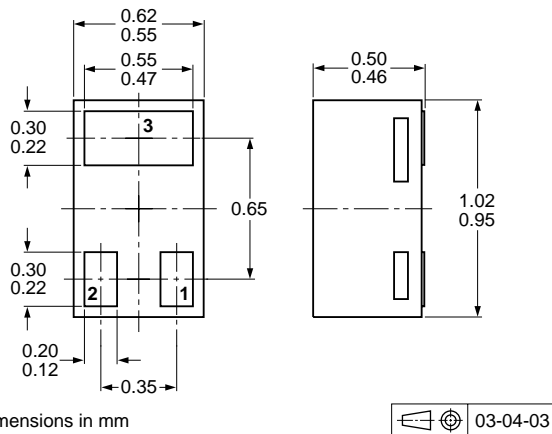


Fig 7. Package outline SOT883 (SC-101)

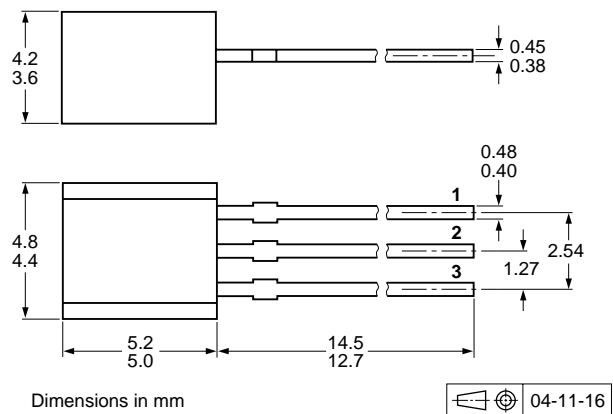


Fig 8. Package outline SOT54 (SC-43A/TO-92)

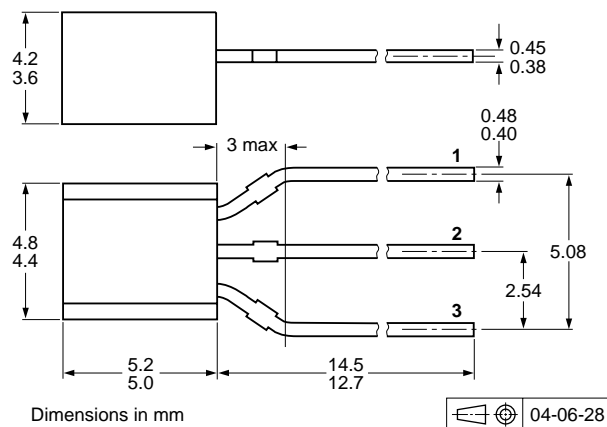


Fig 9. Package outline SOT54A

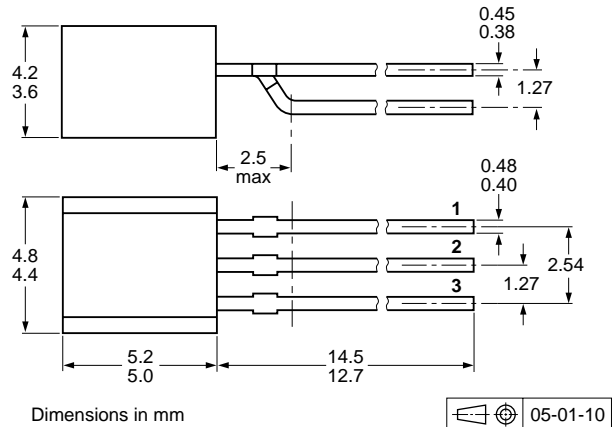
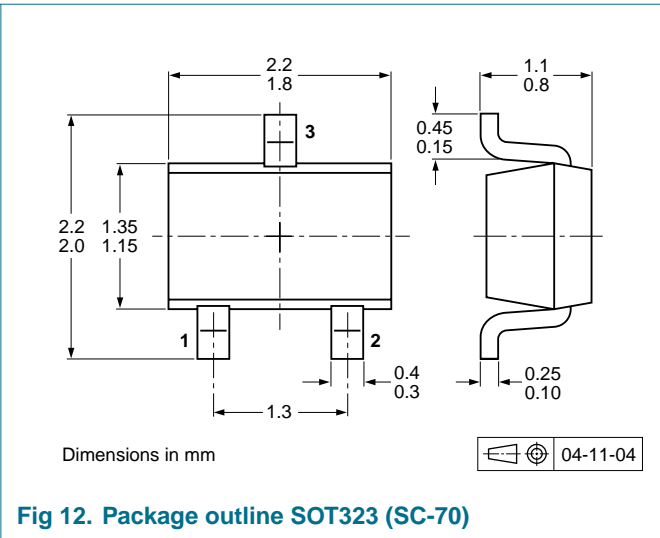
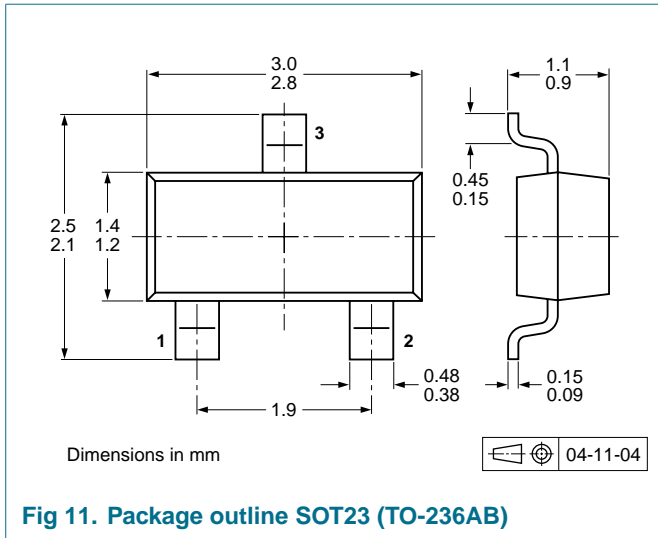


Fig 10. Package outline SOT54 variant



9. Packing information

Table 9: Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code. [1]

Type number	Package	Description	Packing quantity		
			3000	5000	10000
PDTC144VE	SOT416	4 mm pitch, 8 mm tape and reel	-115	-	-135
PDTC144VK	SOT346	4 mm pitch, 8 mm tape and reel	-115	-	-135
PDTC144VM	SOT883	2 mm pitch, 8 mm tape and reel	-	-	-315
PDTC144VS	SOT54	bulk, straight leads	-	-412	-
	SOT54A	tape and reel, wide pitch	-	-	-116
		tape ammpack, wide pitch	-	-	-126
	SOT54 variant	bulk, delta pinning	-	-112	-
PDTC144VT	SOT23	4 mm pitch, 8 mm tape and reel	-215	-	-235
PDTC144VU	SOT323	4 mm pitch, 8 mm tape and reel	-115	-	-135

[1] For further information and the availability of packing methods, see [Section 14](#).

10. Revision history

Table 10: Revision history

Document ID	Release date	Data sheet status	Change notice	Doc. number	Supersedes
PDTC144V_SER_3	20050215	Product data sheet	-	9397 750 14016	PDTC144VT_2
Modifications:	<ul style="list-style-type: none"> The types PDTC144VE, PDTC144VK, PDTC144VM, PDTC144VS and PDTC144VU were added. Table 1 "Product overview" added Table 8 $V_{i(on)}$ redefined to $V_{i(on)}$ on-state input voltage Table 8 $V_{i(off)}$ redefined to $V_{i(off)}$ off-state input voltage Figure 1, 2, 3 and 4 added Section 9 "Packing information" added 				
PDTC144VT_2	20040511	Objective data sheet	-	9397 750 13207	PDTC144VT_1
PDTC144VT_1	20040305	Objective data sheet	-	9397 750 12556	-

11. Data sheet status

Level	Data sheet status ^[1]	Product status ^[2] ^[3]	Definition
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
III	Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Relevant changes will be communicated via a Customer Product/Process Change Notification (CPCN).

[1] Please consult the most recently issued data sheet before initiating or completing a design.

[2] The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL <http://www.semiconductors.philips.com>.

[3] For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

12. Definitions

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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