2 cells Li-ion/Li-polymer battery protection IC Monolithic IC MM3220 Series

Outline

The MM3220 series are protection IC using high voltage CMOS process for overcharge, overdischarge and overcurrent protection of the rechargeable Lithium-ion or Lithium-polymer battery. The overcharge, overdischarge and discharging overcurrent protection of the rechargeable two-cell Lithium-ion or Lithium-polymer battery can be detected.

In addition, the IC has a built-in timer circuit (for each detection delay time), so that the protection circuitry can be comprised with fewer external components.

Features

(Unless otherwise specified, Topr=+25°C)

1. Range and accuracy of detection/release voltage

●Overcharge detection voltage 4.125V to 4.3V, 5mV steps Accuracy±20mV

Accuracy±25mV (Topr=-5 to +60°C)

Overcharge release voltage
 Overdischarge detection voltage
 Overdischarge release voltage

● Discharging overcurrent detection voltage +100mV to +220mV, 5mV steps Accuracy±10mV

● Charging overcurrent detection voltage
-100mV to -300mV, 10mV steps
Accuracy±20mV
● Short detection voltage
0.9V or 1.1V
Accuracy±100mV

2. Range of detection delay time

● Overcharge detection delay time Selection from 0.25s, 1.0s, 1.15s

Overdischarge detection delay time

Selection from 12ms, 20ms, 128ms, 144ms

Plicabarging averagement detection delay time

Selection from 12ms, 20ms, 12ms, 48ms, 256ms

● Discharging overcurrent detection delay time Selection from 6ms, 9ms, 12ms, 48ms, 256ms

● Charging overcurrent detection delay time Selection from 4ms, 6ms, 8ms, 16ms

●Short detection delay time 400µs fixed

3.0V battery charge function Selection from "Prohibition" or "Permission"

4. Low current consumption

●Normal mode Typ. 4.0µA, Max. 8.0µA

●Stand-by mode Max. 0.1µA

5. Absolute maximum ratings

 ●VDD pin
 VSS-0.3V to +12V

 ●COUT pin and V- pin
 VDD-28V to VDD+0.3V

 ●DOUT pin
 VSS-0.3V to VDD+0.3V

● Storage temperature -55 to +125°C ● Operation temperature -40 to +85°C

Pin Assignment

Top view SOT-26A	Pin No.	Function					
VSS VDD VBL 6 5 4 1 2 3 DOUT COUT V-	1	Output of overdischarge detection. Output type is CMOS.					
	2	Output of overcharge detection. Output type is CMOS.					
	3	Input terminal connected to charger negative voltage.					
	4	Input terminal of the low side cell.					
	5	Input terminal of the high side cell. Supply terminal.					
	6	VSS terminal. Connected to ground					

Selection Guide

(3000pcs/Reel)

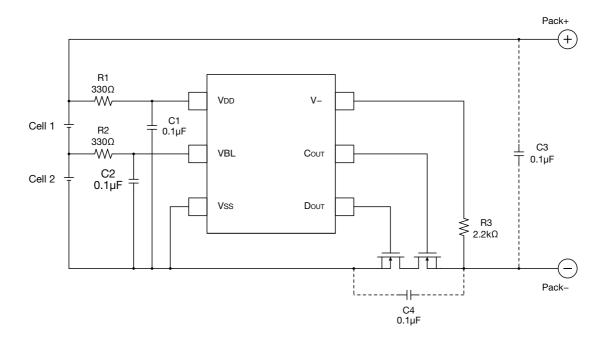
Product name	Package	Overcharge detection voltage [V]	Overcharge release voltage [V]	Overdischarge detection voltage [V]	Overcurrent detection voltage on discharging 1	Overcurrent detection voltage on discharging 2	Overcurrent detection voltage on harging [V]	Short detection voltage [V]	0V battery charge function	Delay time combination ★
		Vdet1H Vdet1L	Vrel1H Vrel1L	Vdet2H Vdet2L	Vdet3-1	Vdet3-2	Vdet4	Vshort	'n	*
MM3220B01NRH	SOT-26A	4.300	4.100	2.000	0.220	-	ı	0.90	Permission	3
MM3220C01NRH	SOT-26A	4.300	4.100	2.000	0.085	0.450	ı	0.90	Permission	4
MM3220C02NRH	SOT-26A	4.300	4.100	2.000	0.100	0.200	ı	0.90	Permission	5
MM3220C03NRH	SOT-26A	4.300	4.100	2.000	0.100	0.200	ı	0.90	Permission	6
MM3220D01NRH	SOT-26A	4.250	4.100	3.000	0.200	-	-	0.90	Permission	7
MM3220F01NRH	SOT-26A	4.225	4.075	2.550	0.200	-	ı	0.90	Permission	7
MM3220G01NRH	SOT-26A	4.290	4.050	3.000	0.200	-	-0.200	1.10	Permission	2
MM3220H01NRH	SOT-26A	4.225	4.075	3.000	0.200	_	-0.200	1.10	Permission	7
MM3220H02NRH	SOT-26A	4.300	4.150	2.400	0.200	-	-0.200	1.10	Prohibition	7
MM3220H03NRH	SOT-26A	4.250	4.100	3.200	0.200	-	-0.200	1.10	Permission	7
MM3220J01NRH	SOT-26A	4.275	4.275	2.500	0.085	-	-0.060	0.30	Prohibition	8
MM3220Z01NRH	SOT-26A	4.300	4.100	2.000	0.100	0.180	ı	0.90	Permission	9

* Delay time combination

		1	2	3	4	5	6	7	8	9
Overcharge detection delay time	tVdet1	1.0s	1.0s	1.0s	1.15s	1.0s	1.0s	1.15s	0.25s	1.0s
Overdischarge detection delay time	tVdet2	128ms	128ms	12ms	10.8ms	12ms	12ms	144ms	20ms	12ms
Overcurrent detection delay time on discharging 1	tVdet3-1	12ms	12ms	12ms	10.8ms	48ms	256ms	9ms	6ms	256ms
Overcurrent detection delay time on discharging 2	tVdet3-2	4ms	-	-	0.5ms	4ms	2.5ms	-	-	2.5ms
Short detection delay time	tshort	400μs	300µs	400μs	400μs	400μs	400μs	300µs	400μs	400μs

Please inquire to us, if you request a rank other than the above.

Application Circuit



R1 , C1 , R2 , C2 stabilize a supply voltage ripple. However, R1 is enlarged, the detection voltage shifts by voltage when current consumption flows into R1. Please decide it after confirming the characteristic. Moreover, adjust the value of C1 , C2 to $0.01\mu F$ or more to do the stability operation, please.

R1 and R3 resistors are current limit resistance if a charger is connected reversibly or a high-voltage charger that exceeds the absolute maximum rating is connected. R1 and R3 may cause a power consumption will be over rating of power dissipation, therefore the `R1+R3` should be more than $1k\Omega$. Moreover, if R3 is too enlarged, the charger connection release cannot be occasionally done after the overdischarge is detected, so adjust the value of R3 to $10k\Omega$ or less, please.

In the state of overdischarge, The current flows through overdischarge pull-up resistance built into between VDD terminal and V- terminal when the charger is connected. As a result, current that flows into VDD terminal increases. When current increases, the voltage is generated in R1. And hysteresis might be caused. Please use it after confirming the characteristic.

C3 and C4 capacitors have effect that the system stability about voltage ripple or imported noise. After check characteristics, decide that these capacitors should be inserted or not, where should be inserted, and capacitance value, please.