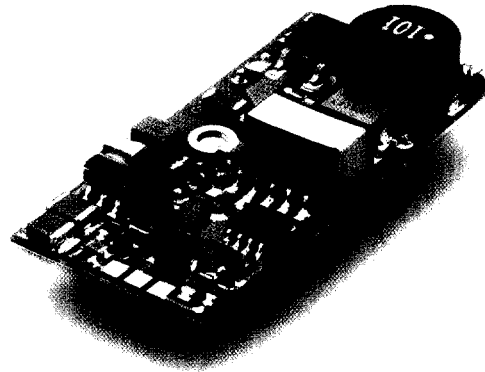


AA10C

10 Watts



AA10C Series

Total Power	10 Watts
Input Voltages	48V
# of Outputs	Single

SPECIAL FEATURES

- 10 W, Single Output
- Unencapsulated package
- 1.0" x 2.0" x 0.38 case
- Unencapsulated package
- Replaces AA10B series
- Trim and enable functions

ENVIRONMENTAL

Operating ambient temperature range:
-40°C to 60°C (full load, no derating)

Baseplate: N/A

Storage temperature: -55°C to +125°C

Humidity: 5% to 95% (non-condensing)

MTBF: 1.0 Mil hours.

SAFETY

- UL** UL1950 Recognition pending
- CSA** CSA22.2-950 Recognition pending
- TUV** EN60950 Recognition pending

ELECTRICAL SPECIFICATIONS

Input

Input range 36 to 75VDC

Input capacitance 1.4µF typ

No load input power 750mW max

Input reflected ripple (P-P) ... 10mA max

Efficiency 83% typical (5V)

Output

Voltage tolerance ±1.0%

Line regulation ±0.1% max

Load regulation ±0.2% max

Noise/ripple 100mV P-P (see note 1 & 2)

Short circuit protection Continuous Power Cycle

Overvoltage protection Active Disable (125% of nom)

Transient response 500 µS max
(25% step load change to within 1%V_o)

Temperature Coefficient ±0.02/°C

Switching frequency 450kHz

Isolation

I/O isolation 1500 VDC

Isolation resistance 1 x 10⁹ (Ohms)

Isolation capacitance 220pF



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ORDERING INFORMATION

Input Voltage	Output Voltage	Efficiency	Model Number
36-75V	1.5V @ 2.0A	71%	AA10C-048L-015S
36-75V	2.0V @ 2.0A	75%	AA10C-048L-020S
36-75V	3.3V @ 2.0A	80%	AA10C-048L-033S
36-75V	5.0V @ 2.0A	84%	AA10C-048L-050S
36-75V	12V @ 0.8A	88%	AA10C-048L-120S

NOTES:

Enable Function

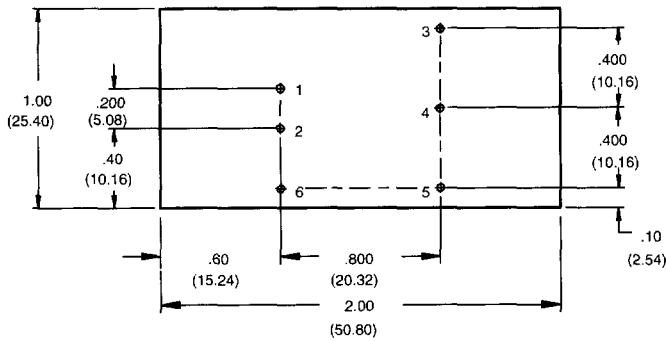
"1" = negative logic enable (std, without "1 or 4" = no enable)

"4" = positive logic enable (std, without "1 or 4" = no enable)

Trim Feature

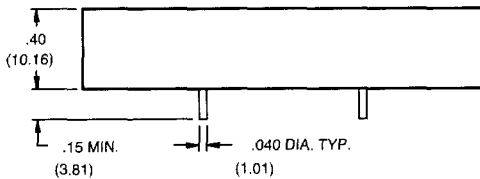
"9" = Trim feature (std, without "9" no Trim)

DIMENSIONS



BOTTOM VIEW

ALL DIMENSIONS ARE IN INCHES (mm)



Case Material: N/A
 Weight: 1.4 oz/40 grams

PIN ASSIGNMENTS

Single Output

1. +Vin
2. -Vin
3. Output 1
4. Trim (-9 option)
5. Comm
6. Enable (-1, -4 option)

AA10C

NOTES

1. 20 MHz bandwidth. An external 0.1 uf ceramic capacitor is recommended to be placed from +V out to comm.
2. Meets EN55022 Class A (-150S/D require 2.2uf input capacitor). Consult factory for filter recommendations to meet FCC class B, VDE or EIC specifications.
3. All specifications are typical at nominal line, full load, and 25°C unless otherwise noted.
4. All specifications subject to change without notice.
5. Mechanical drawings are for reference only.
6. Calculated per MIL HDBK 217F, Ground benign at 25°C, full load. BELLCORE calculated per TR-NWT-000332 method 1 parts count, Case 1.