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Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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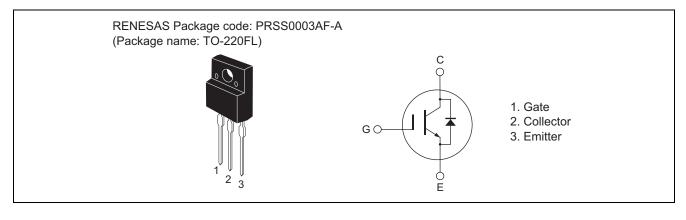
# RJH60D2DPP-M0

Silicon N Channel IGBT Application: Inverter

Features

- High breakdown-voltage
- Low on-voltage
- Built-in diode

# Outline



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

	ltem	Symbol	Ratings	Unit	
Collector to emitter voltage / diode reverse voltage		V <sub>CES</sub> / V <sub>R</sub>	600	V	
Gate to emitter voltage		V <sub>GES</sub>	±30	V	
Collector current	Tc = 25°C	lc	20	А	
	Tc = 100°C	lc	10	А	
Collector peak current		ic(peak) Note1	40	А	
Collector to emitter diode forward current		i <sub>DF</sub>	10	А	
Collector to emitter diode forward peak current		i <sub>DF</sub> (peak) <sup>Note1</sup>	40	А	
Collector dissipation		Pc <sup>Note2</sup>	22.5	W	
Junction to case thermal impedance		θj-c <sup>Note2</sup>	5.5	°C/ W	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

Notes: 1.  $PW \le 10 \ \mu s$ , duty cycle  $\le 1\%$ 

2. Value at Tc = 25°C

# **Electrical Characteristics**

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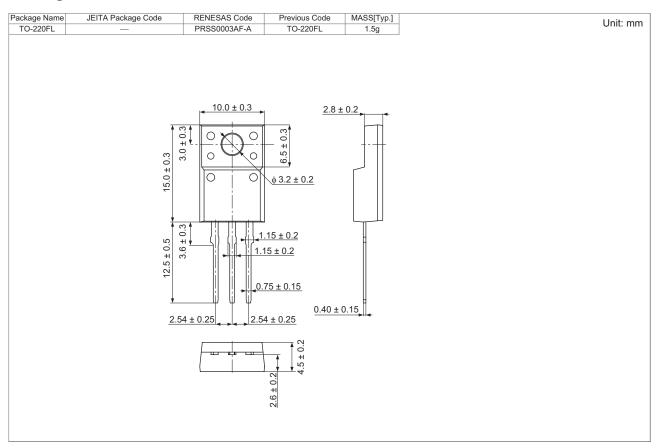
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions
Zero gate voltage collector current / Diode reverse current	I <sub>CES</sub> / I <sub>R</sub>	—	—	100	μA	V <sub>CE</sub> = 600 V, V <sub>GE</sub> = 0
Gate to emitter leak current	I <sub>GES</sub>	—	—	±1	μA	$V_{GE}$ = ±30 V, $V_{CE}$ = 0
Gate to emitter cutoff voltage	V <sub>GE(off)</sub>	4.0	—	6.0	V	$V_{CE}$ = 10 V, I <sub>C</sub> = 1 mA
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	_	1.6	2.2	V	$I_{\rm C}$ = 10 A, $V_{\rm GE}$ = 15 V <sup>Note3</sup>
	V <sub>CE(sat)</sub>	_	1.8	—	V	$I_{C}$ = 20 A, $V_{GE}$ = 15 V <sup>Note3</sup>
Input capacitance	Cies	_	430	_	pF	V <sub>CE</sub> = 25 V
Output capacitance	Coes	_	35	_	pF	V <sub>GE</sub> = 0 f = 1 MHz
Reveres transfer capacitance	Cres	_	15	_	pF	
Total gate charge	Qg	_	19.1	_	nC	V <sub>GE</sub> = 15 V
Gate to emitter charge	Qge	_	3.0	_	nC	V <sub>CE</sub> = 300 V I <sub>C</sub> = 10A
Gate to collector charge	Qgc	_	9.0	_	nC	
Switching time	t <sub>d(on)</sub>	_	30	_	ns	I <sub>C</sub> = 10 A
	tr	_	30	_	ns	R <sub>L</sub> = 30 Ω
	t <sub>d(off)</sub>		50		ns	V <sub>GE</sub> = 15 V
	t <sub>f</sub>	_	90		ns	Rg = 5 Ω

FRD Forward voltage	VF	_	1.8	2.3	V	$I_F = 10 A^{Note3}$
FRD reverse recovery time	trr	_	100	—	ns	I <sub>F</sub> = 10 A
						di <sub>F</sub> /dt = 100 A/µs

Notes: 3. Pulse test.

4. Under development — The specifications potentially be changed without notice.

# **Package Dimension**



# **Ordering Information**

Part No.	Quantity	Shipping Container		
RJH60D2DPP-M0-T2	1050 pcs	Box (Tube)		

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