-100mA / -50V Digital transistor (with built-in resistor) DTA113TKA

Applications

Inverter, Interface, Driver

Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input, and parasitic effects are almost completely eliminated.
- Only the on/ off conditions need to be set for operation, making the device design easy.
- 4) Higher mounting densities can be achieved.

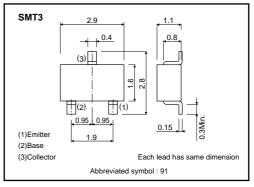
Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

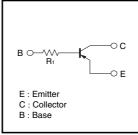
Packaging specifications

| | Package | SMT3 | | |
|-----------|------------------------------|--------|--|--|
| | Packaging type | Taping | | |
| | Code | T146 | | |
| Part No. | Basic ordering unit (pieces) | 3000 | | |
| DTA113TKA | | 0 | | |

•External dimensions (Unit : mm)



Equivalent circuit



 $R_1=1k\Omega$

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•Absolute maximum ratings (Ta=25°C)

| Parameter | Symbol | Limits | Unit |
|-----------------------------|--------|-------------|------|
| Collector-base voltage | Vсво | -50 | V |
| Collector-emitter voltage | VCEO | -50 | V |
| Emitter-base voltage | Vebo | -5 to +10 | V |
| Collector current | lc | -100 | mA |
| Collector Power dissipation | Pc | 200 | mW |
| Junction temperature | Tj | 150 | °C |
| Storage temperature | Tstg | -55 to +150 | °C |

Transistors

•Electrical characteristics (Ta=25°C)

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Conditions |
|--------------------------------------|----------|------|------|------|------|------------------------------------|
| Collector-base breakdown voltage | ВИсво | -50 | - | _ | V | Ic= -50μA |
| Collector-emitter breakdown voltage | BVCEO | -50 | - | _ | V | Ic=-1mA |
| Emitter-base breakdown voltage | BVEBO | -5 | - | - | V | Iε= -50μA |
| Collector cutoff current | Ісво | - | - | -0.5 | μA | Vcb=-50V |
| Emitter cutoff current | Іево | - | - | -0.5 | μA | VEB=-4V |
| Collector-emitter saturation voltage | VCE(sat) | - | - | -0.3 | V | Ic/I _B = -5mA / -0.25mA |
| DC current transfer ratio | hfe | 100 | 250 | 600 | - | Ic=-1mA , Vc=-5V |
| Input resistance | R1 | 0.7 | 1 | 1.3 | kΩ | - |
| Transition frequency | f⊤ * | _ | 250 | - | MHz | Vcb= -10V , IE=5mA , f=100MHz |

* Characteristics of built-in transistor

•Electrical characteristics curves

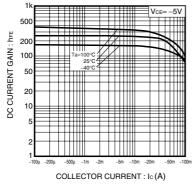
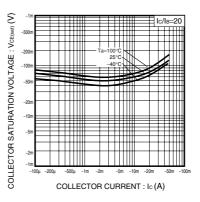
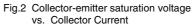


Fig.1 DC Current gain vs. Collector Current





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