RJH60D3DPE

Silicon N Channel IGBT
Application: Inverter

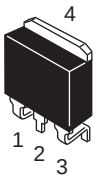
R07DS0161EJ0400
Rev.4.00
Mar 09, 2011

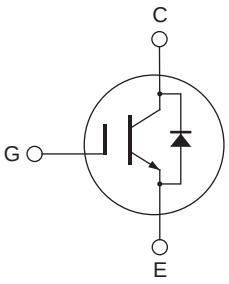
Features

- Short circuit withstand time (5 μs typ.)
- Low collector to emitter saturation voltage
  $V_{CE(sat)}$ = 1.6 V typ. (at I_C = 17 A, V_{GE} = 15 V, Ta = 25°C)
- Built in fast recovery diode (100 ns typ.) in one package
- Trench gate and thin wafer technology
- High speed switching
  t_f = 80 ns typ. (at V_{CC} = 300 V, V_{GE} = 15 V, I_C = 17 A, Rg = 5 Ω, Ta = 25°C, inductive load)

Outline

RENESAS Package code: PRSS0004AE-B
(Package name: LDPAK (S)-(1))





1. Gate
2. Collector
3. Emitter
4. Collector

Absolute Maximum Ratings

(Ta = 25°C)

Item		Symbol	Ratings	Unit
Collector to emitter voltage / diode reverse voltage		V_{CES} / V_R	600	V
Gate to emitter voltage		V_{GES}	±30	V
Collector peak current	Tc = 25°C	I_C	35	A
	Tc = 100°C	I_C	17	A
Collector peak current		ic(peak) Note1	70	A
Collector to emitter diode forward current		i_{DF}	17	A
Collector to emitter diode forward peak current		i_{DF}(peak) Note1	70	A
Collector dissipation		P_C Note2	113	W
Junction to case thermal resistance (IGBT)		θj-c Note2	1.11	°C/ W
Junction to case thermal resistance (Diode		θj-cd Note2	2.75	°C/ W
Junction temperature		Tj	150	°C
Storage temperature		Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%
2. Value at Tc = 25°C

Electrical Characteristics

(Ta = 25°C)

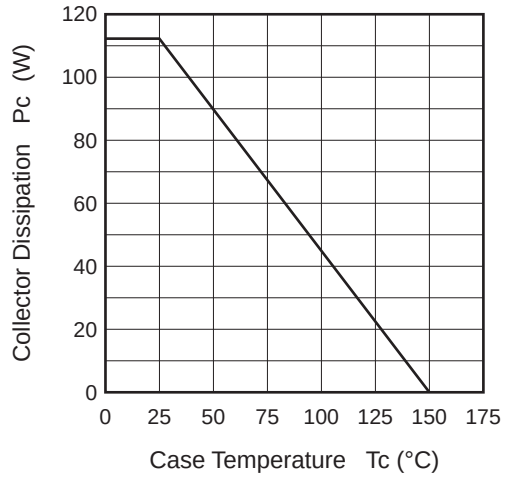
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Zero gate voltage collector current / Diode reverse current	I_{CES} / I_R	—	—	5	μA	V_{CE} = 600 V, V_{GE} = 0
Gate to emitter leak current	I_{GES}	—	—	±1	μA	V_{GE} = ±30 V, V_{CE} = 0
Gate to emitter cutoff voltage	$V_{GE(off)}$	4.0	—	6.0	V	V_{CE} = 10 V, I_C = 1 mA
Collector to emitter saturation voltage	$V_{CE(sat)}$	—	1.6	2.2	V	I_C = 17 A, V_{GE} = 15 V Note3
	$V_{CE(sat)}$	—	2.0	—	V	I_C = 35 A, V_{GE} = 15 V Note3
Input capacitance	Cies	—	900	—	pF	V_{CE} = 25 V V_{GE} = 0 f = 1 MHz
Output capacitance	Coes	—	60	—	pF	
Reveres transfer capacitance	Cres	—	30	—	pF	
Total gate charge	Qg	—	36	—	nC	V_{GE} = 15 V V_{CE} = 300 V I_C = 17 A
Gate to emitter charge	Qge	—	6	—	nC	
Gate to collector charge	Qgc	—	16	—	nC	
Switching time	$t_{d(on)}$	—	30	—	ns	V_{CC} = 300 V, V_{GE} = 15 V I_C = 17 A Rg = 5 Ω Inductive load
	t_r	—	15	—	ns	
	$t_{d(off)}$	—	80	—	ns	
	t_f	—	80	—	ns	
Short circuit withstand time	t_{sc}	3.0	5.0	—	μs	V_{CC} ≤ 360 V, V_{GE} = 15 V

FRD Forward voltage	V_F	—	1.3	1.7	V	I_F = 17 A Note3
FRD reverse recovery time	t_{rr}	—	100	—	ns	I_F = 17 A di_F/dt = 100 A/μs

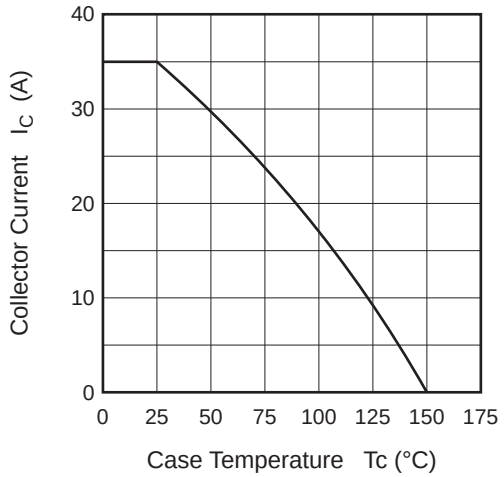
Notes: 3. Pulse test.

Main Characteristics

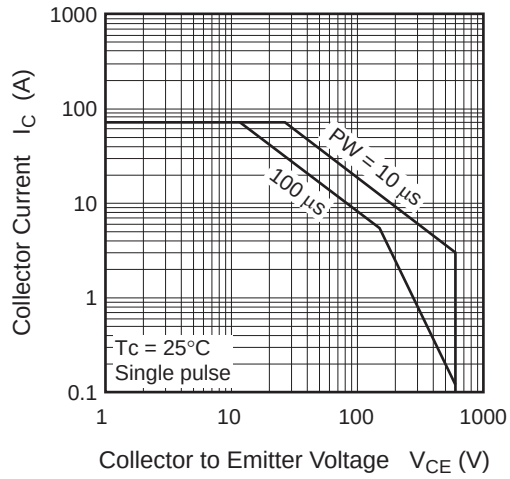
Power Dissipation vs. Case Temperature



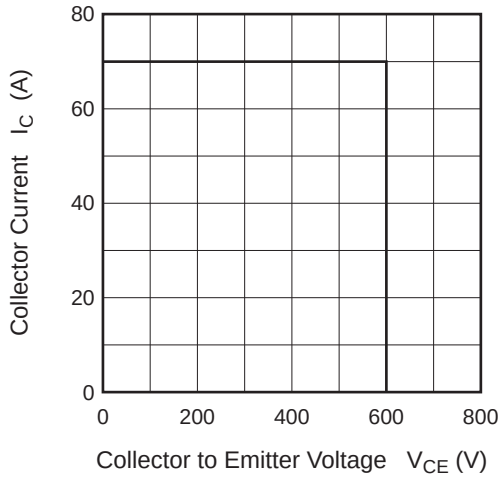
Maximum DC Collector Current vs. Case Temperature



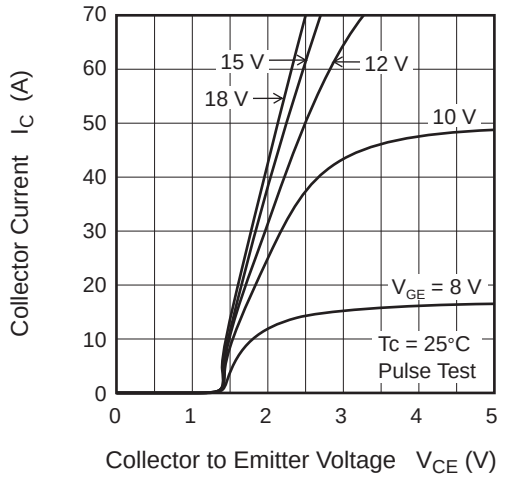
Maximum Safe Operation Area



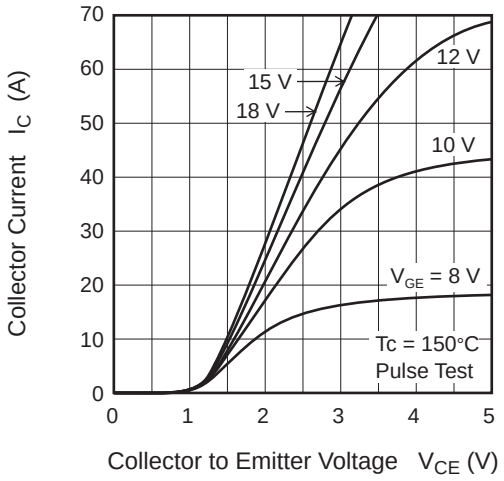
Turn-off SOA



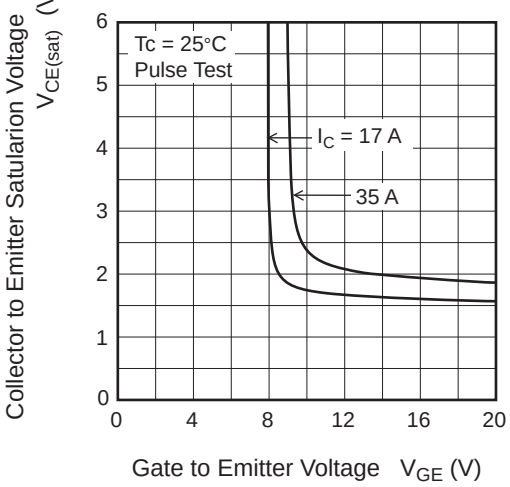
IGBT Output Characteristics (Typical)



IGBT Output Characteristics (Typical)



Collector to Emitter Satularion Voltage vs.
Gate to Emitter Voltage (Typical)



Collector to Emitter Satularion Voltage vs.
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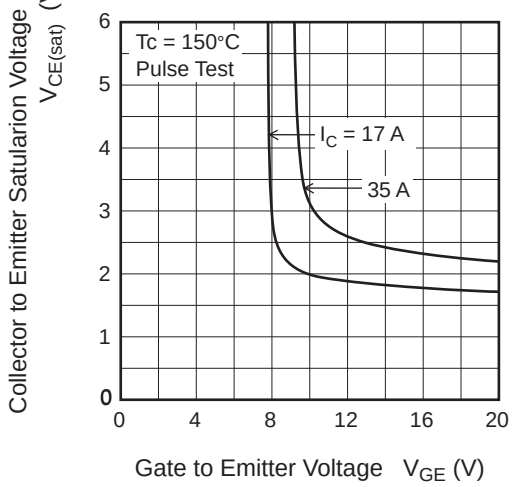
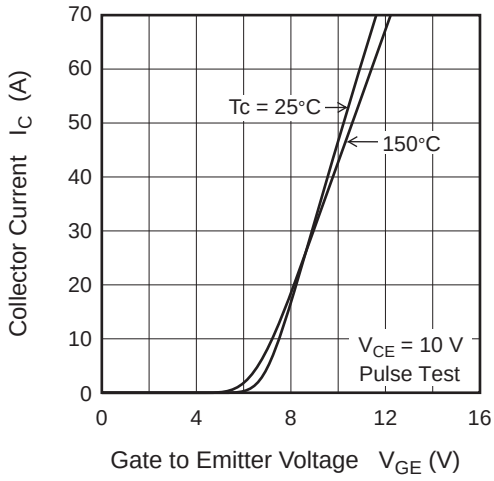
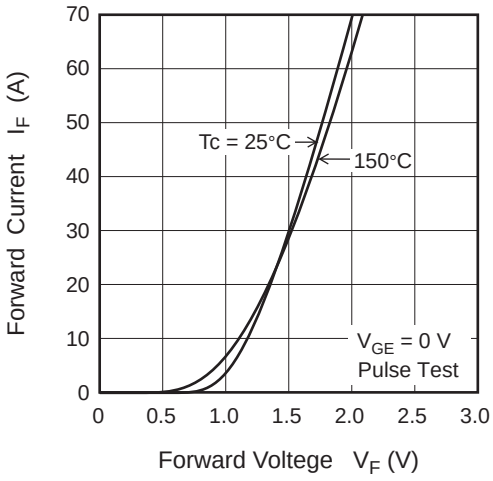


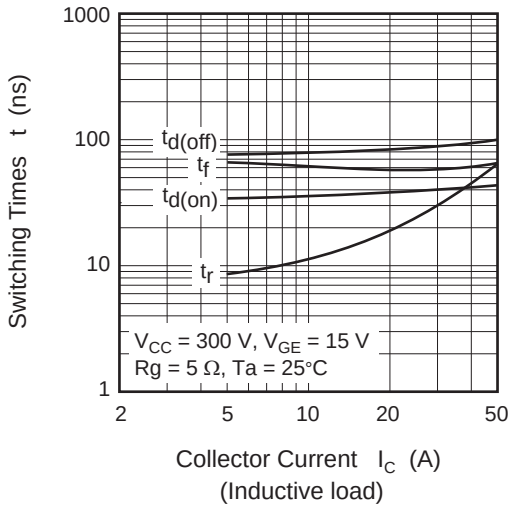
Transfer Characteristics (Typical)



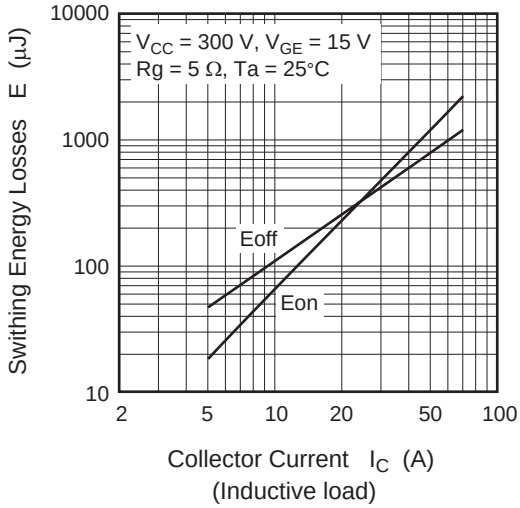
Diode Forward Characteristics (Typical)



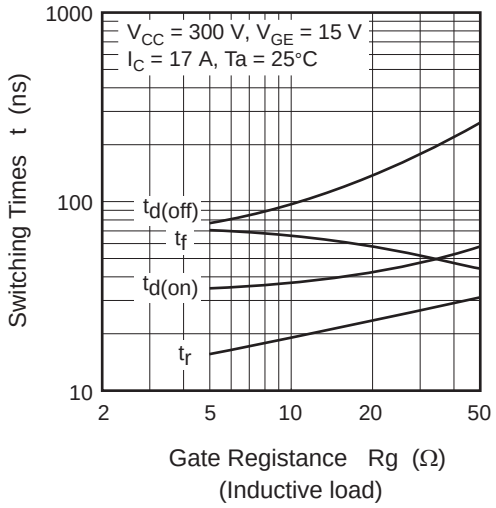
Switching Characteristics (Typical) (1)



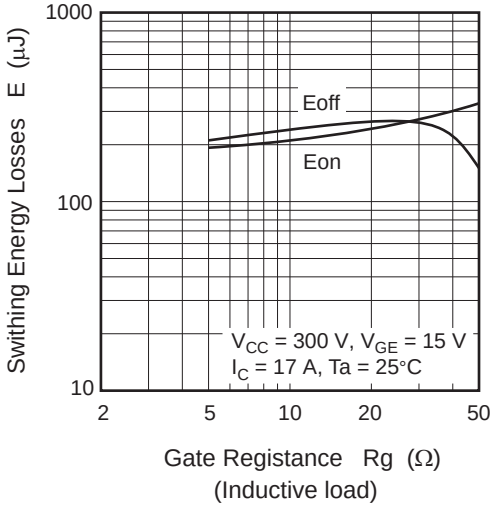
Switching Characteristics (Typical) (2)



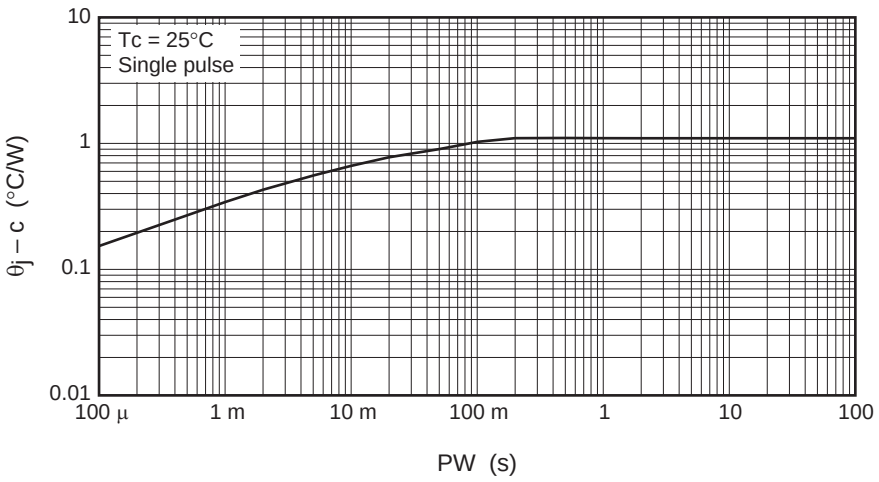
Switching Characteristics (Typical) (3)



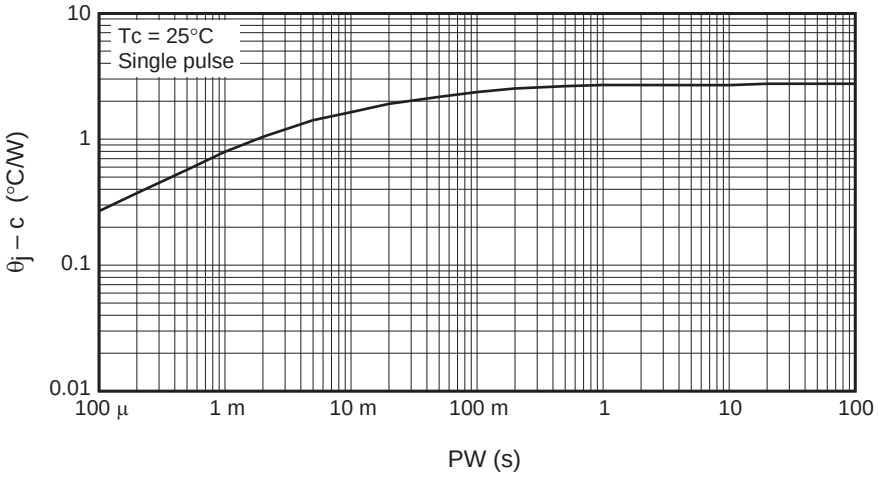
Switching Characteristics (Typical) (4)



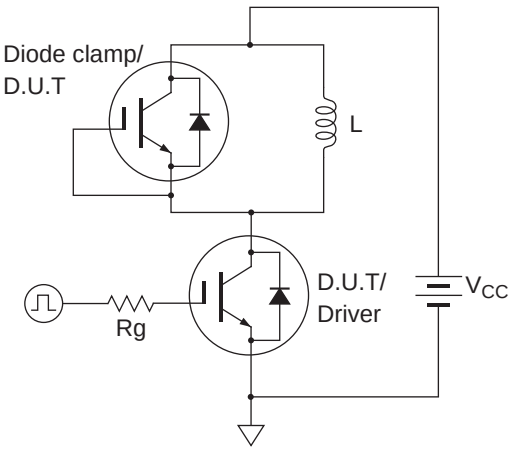
Thermal Impedance vs. Pulse Width



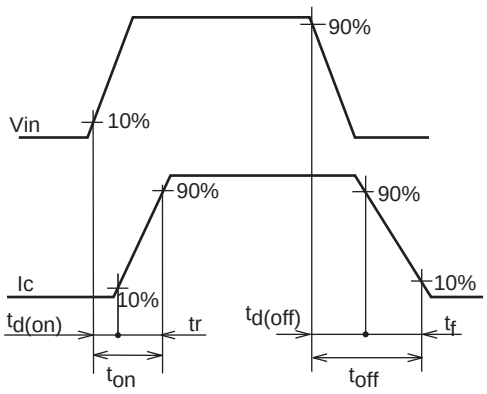
Transient Thermal Impedance Junction to Case (Diode)



Switching Time Test Circuit



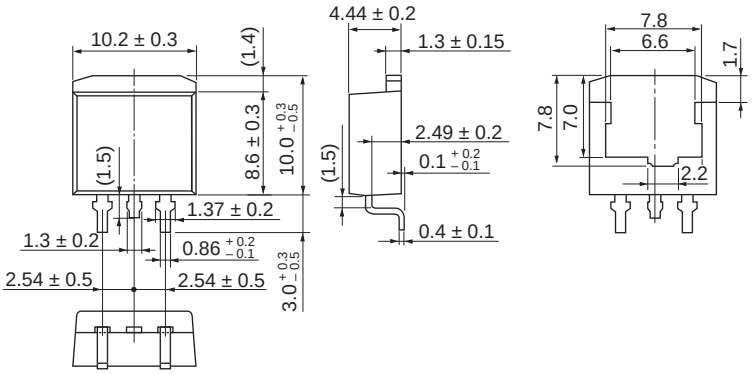
Waveform



Package Dimension

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDPAK(S)-(1)	SC-83	PRSS0004AE-B	LDPAK(S)-(1) / LDPAK(S)-(1)V	1.30g

Unit: mm



Ordering Information

Orderable Part No.	Quantity	Shipping Container
RJH60D3DPE-00-J3	1000 pcs	Taping

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