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VSKDU162/12PbF

Vishay High Power Products

HEXFRED[®] Ultrafast Diodes, 100 A (New INT-A-PAK Power Modules)

FEATURES

- Electrically isolated: DBC base plate
- Standard JEDEC package
- Simplified mechanical designs, rapid assembly
- High surge capability
- Large creepage distances
- UL approved file E78996
- Case style New INT-A-PAK
- Compliant to RoHS directive 2002/95/EC
- · Designed and gualified for industrial level

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	V _R		1200	V	
Continuous forward current	I _F	T _C = 25 °C	205		
		T _C = 100 °C	110	А	
Single pulse forward current	I _{FSM}	Limited by junction temperature	800		
Maximum power dissipation	D	$T_{\rm C} = 25 \ ^{\circ}{\rm C}$	695	w	
	PD	T _C = 100 °C	280	vv	
RMS isolation voltage	V _{ISOL}	50 Hz, circuit to base, all terminal shorted, t = 1 s	3500	V	
Operating junction and storage temperature range	T _J , T _{Stg}		- 40 to + 150	°C	

ELECTRICAL SPECIFICATIONS PER LEG ($T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 100 μA	1200	-	-	
Maximum forward voltage	V _{FM}	I _F = 100 A	-	2.5	3.2	V
		I _F = 160 A	-	2.9	3.9	
Maximum reverse leakage current	I _{RM}	T _J = 150 °C, V _R = 1200 V	-	18	30	mA



1200 V

2.5 V

150 ns 110 A at 100 °C

PRODUCT SUMMARY

 V_{R} V_F (typical)

t_{rr} (typical)

I_{F(DC)} at T_C



RoHS COMPLIANT

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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t _{rr}	T _J = 25 °C		-	150	200	ns
Reverse recovery current	I _{RRM}	T _J = 25 °C	dl⊧/dt = 200 A/us	-	20	22	А
Reverse recovery charge	Qrr	T _J = 25 °C		-	2000	2400	nC
Peak rate of recovery current	dl _{(rec)M} /dt	T _J = 25 °C		-	-	300	A/µs

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Junction operating and storage temperature range		T _J , T _{Stg}		- 40 to 150	°C	
Maximum internal thermal resistance, junction to case per leg		R _{thJC}	DC operation	0.18 sed 0.05		
Typical thermal resistance, case to heatsink per module		R _{thCS}	Mounting surface flat, smooth and greased			
Mounting torque ± 10 % -	to heatsink		A mounting compound is recommended and the torgue should be rechecked after a period of 3 hours		Nim	
	busbar		to allow for the spread of the compound.	6	Nm	
Approximate weight				200	g	
				7.1	oz.	
Case style				New INT-A-PAK		



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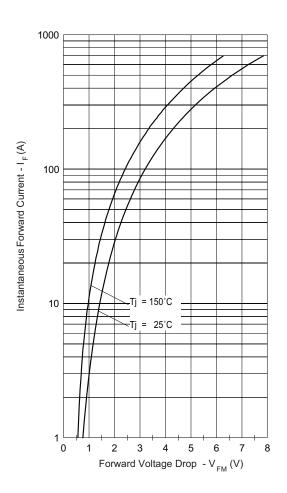


Fig. 1 - Maximum Forward Voltage Drop Characteristics

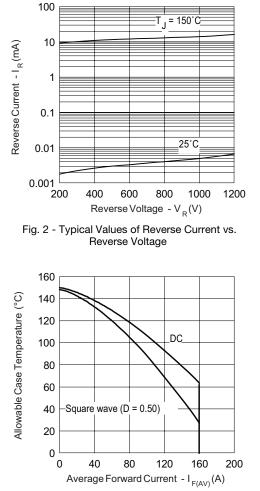


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current

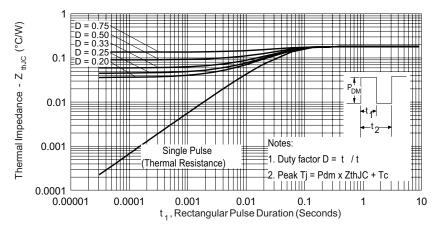


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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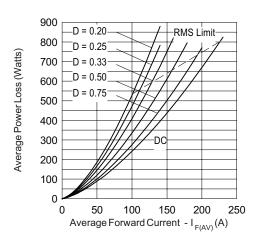
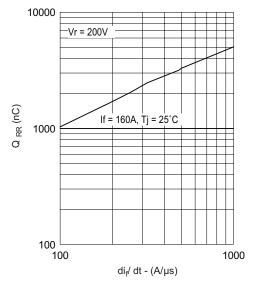
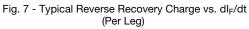


Fig. 5 - Forward Power Loss Characteristics





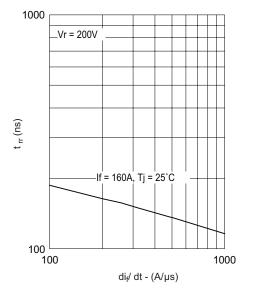


Fig. 6 - Typical Reverse Recovery Time vs. $dI_{\rm F}/dt$ (Per Leg)

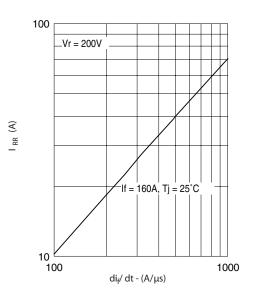
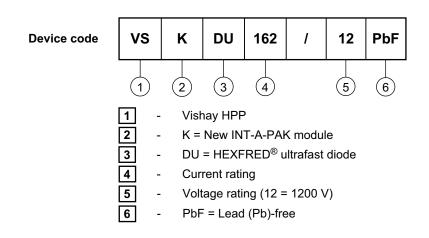


Fig. 8 - Typical Reverse Recovery Current vs. dl_F/dt (Per Leg)

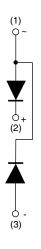


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ORDERING INFORMATION TABLE



CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95254				

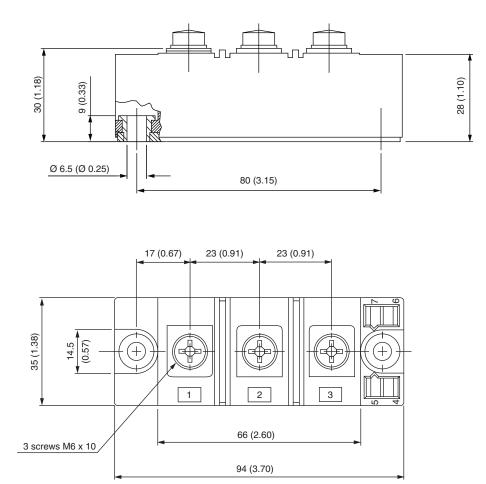


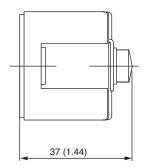
Outline Dimensions

Vishay Semiconductors

INT-A-PAK DBC

DIMENSIONS in millimeters (inches)







Vishay

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