

# **STC4081**

**NPN Silicon Transistor** 

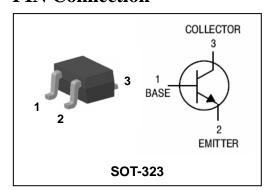
## **Description**

• General small signal amplifier

#### **Features**

- $\bullet$  Low collector saturation voltage :  $V_{\text{CE}}{=}0.4V(\text{Max.})$
- Low output capacitance : C<sub>ob</sub>=2pF(Typ.)
- Complementary pair with STA1576

### **PIN Connection**



# **Ordering Information**

Type NO.	Marking	Package Code	
STC4081	<u>D</u> <u> </u>	SOT-323	

①Device Code ②hFE Rank ③Year&Week Code

## **Absolute maximum ratings**

(Ta=25°C)

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	$V_{CBO}$	50	V
Collector-Emitter voltage	V <sub>CEO</sub>	50	V
Emitter-Base voltage	$V_{EBO}$	5	V
Collector current	I <sub>C</sub>	150	mA
Collector dissipation	P <sub>C</sub>	200	mW
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

### **Electrical Characteristics**

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV <sub>CBO</sub>	$I_{C}=50\mu A,\ I_{E}=0$	50	-	-	V
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	$I_C=1$ mA, $I_B=0$	50	-	-	V
Emitter-Base breakdown voltage	BV <sub>EBO</sub>	$I_E = 50 \mu A, I_C = 0$	5	-	-	V
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = 30V, I_{E} = 0$	-	-	0.5	μΑ
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB}=4V$ , $I_{C}=0$	-	-	0.5	μΑ
DC current gain	h <sub>FE</sub> *	$V_{CE}=6V$ , $I_{C}=1mA$	70	-	700	-
Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C=50\text{mA},\ I_B=5\text{mA}$	-	-	0.4	V
Transistion frequency	f <sub>T</sub>	$V_{CE}=12V$ , $I_{C}=2mA$	-	180	-	MHz
Collector output capacitance	C <sub>ob</sub>	V <sub>CB</sub> =12V, I <sub>E</sub> =0, f=1MHz	-	2	-	pF
Noise figure	NF	$V_{CE}$ =6V, $I_{C}$ =0.1mA, f=1KHz, $Rg$ =10K $\Omega$	-	1	10	dB

<sup>\* :</sup>  $h_{FE}$  rank / O : 70 ~ 140, Y : 120 ~ 240, G : 200 ~ 400, L : 300 ~ 700

## **Electrical Characteristic Curves**

Fig. 1  $P_C - T_a$ 

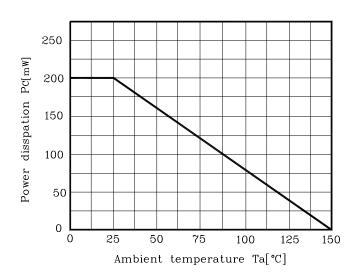


Fig. 2  $I_C$  - $V_{BE}$ 

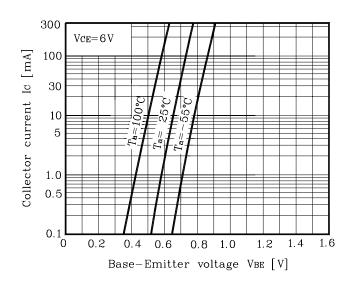


Fig. 3  $I_C$  - $V_{CE}$ 

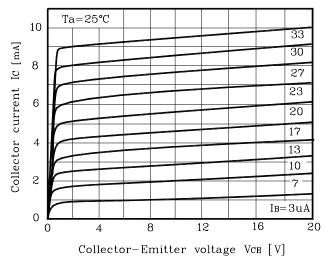


Fig. 4  $h_{FE}$  - $I_C$ 

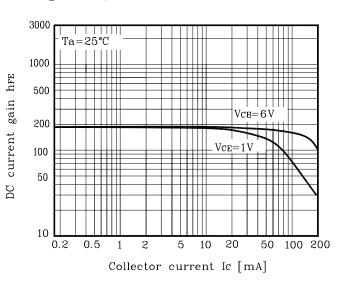
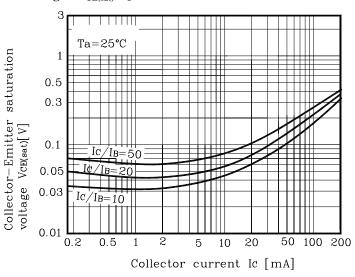
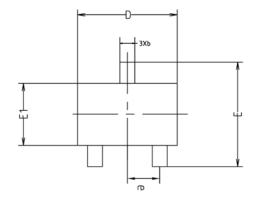
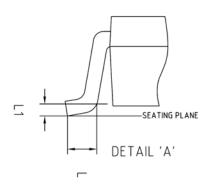


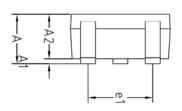
Fig. 5  $V_{\text{CE}(\text{sat})}$  -I  $_{\text{C}}$ 

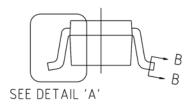


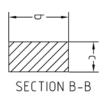
# **Outline Dimension**





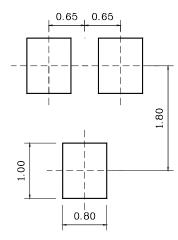






SYMBOL	MILLIMETERS			NOTE
3 THEOL	MINIMUM	NOMINAL	MAXIMUM	NUTE
Α	0.90	-	1.25	
A1	0.00	-	0.10	
A2	0.85	0.90	0.95	
Ь	0.30	-	0.40	
С	0.10	-	0.25	
D	1.90	2.00	2.10	
Ε	1.95	2.10	2.25	
E1	1.15	1.25	1.35	
е	0.65BSC			
e1	1.20	-	1.40	
L	0.10	-	-	
L1	0.12BSC			

## \*Recommend PCB solder land [Unit: mm]



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